

# HD74BC534A

Octal D Type Flip Flops With 3 State Outputs

**HITACHI**

ADE-205-012 (Z)

Rev. 0

June 1993

## Description

The HD74BC534A provides high drivability and operation equal to or better than high speed bipolar standard logic IC by using Bi-CMOS process. The device features low power dissipation that is about 1/5 of high speed bipolar logic IC, when the frequency is 10 MHz. The device has eight edge trigger D type flip flop with three state outputs in a 20 pin package. Data at the D inputs meeting set up requirements, are transferred to the  $\bar{Q}$  outputs on positive going transitions of the clock input. When the latch enable goes low, data at the D inputs will be retained at the outputs until latch enable returns high again. When a high logic level is applied to the output control input, all outputs go to a high impedance state, regardless of what signals are present at the other inputs and the state of the strange elements.

## Features

- Input/Output are at high impedance state when power supply is off.
- Built in input pull up circuit can make input pins be open, when not used.
- TTL level input
- Wide operating temperature range  
 $T_a = -40$  to  $+85^\circ\text{C}$

## Function Table

### Inputs

G	CK	D	Output $\bar{Q}$
H	X	X	Z
L	—	L	H
L	—	H	L
L	L	X	No change

H : High level

L : Low level

X : Immaterial

Z : High impedance

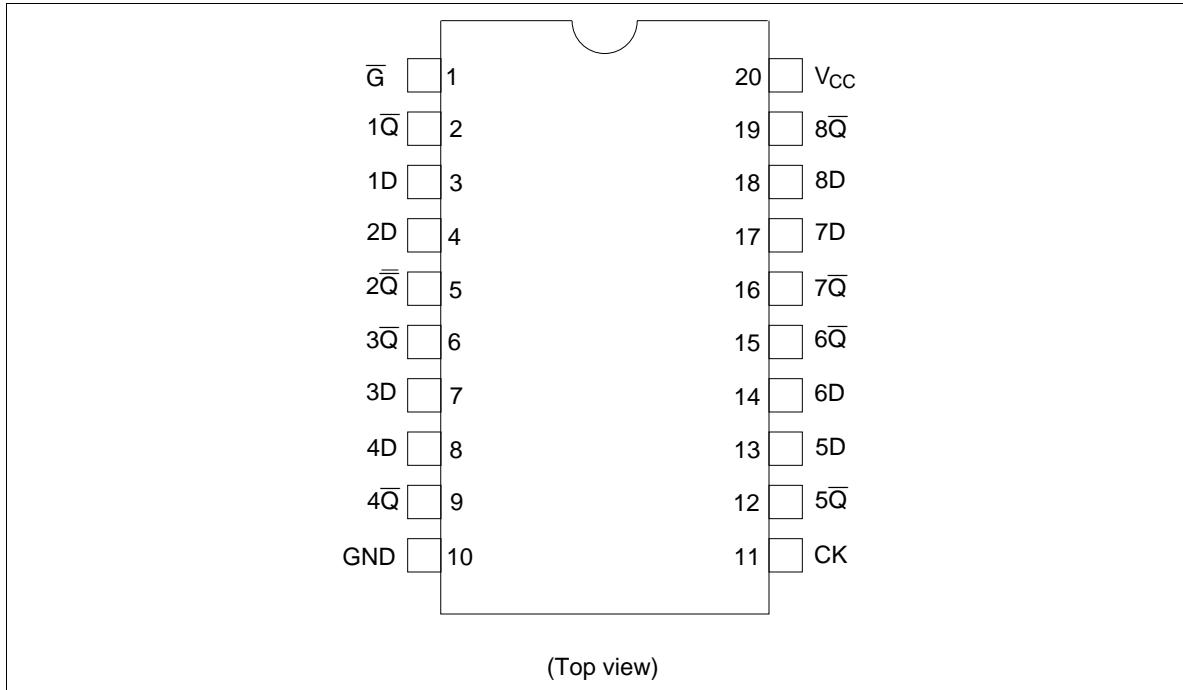
— : Low to high transitions

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### Pin Arrangement



### Absolute Maximum Ratings

Item	Symbol	Rating	Unit
Supply voltage	$V_{cc}$	–0.5 to +7.0	V
Input diode current	$I_{ik}$	$\pm 30$	mA
Input voltage	$V_{in}$	–0.5 to +7.5	V
Output voltage	$V_{out}$	–0.5 to +7.5	V
Off state output voltage	$V_{out(off)}$	–0.5 to +5.5 V	V
Storage temperature	$T_{stg}$	–65 to +150	°C

Note: 1. The absolute maximum ratings are values which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

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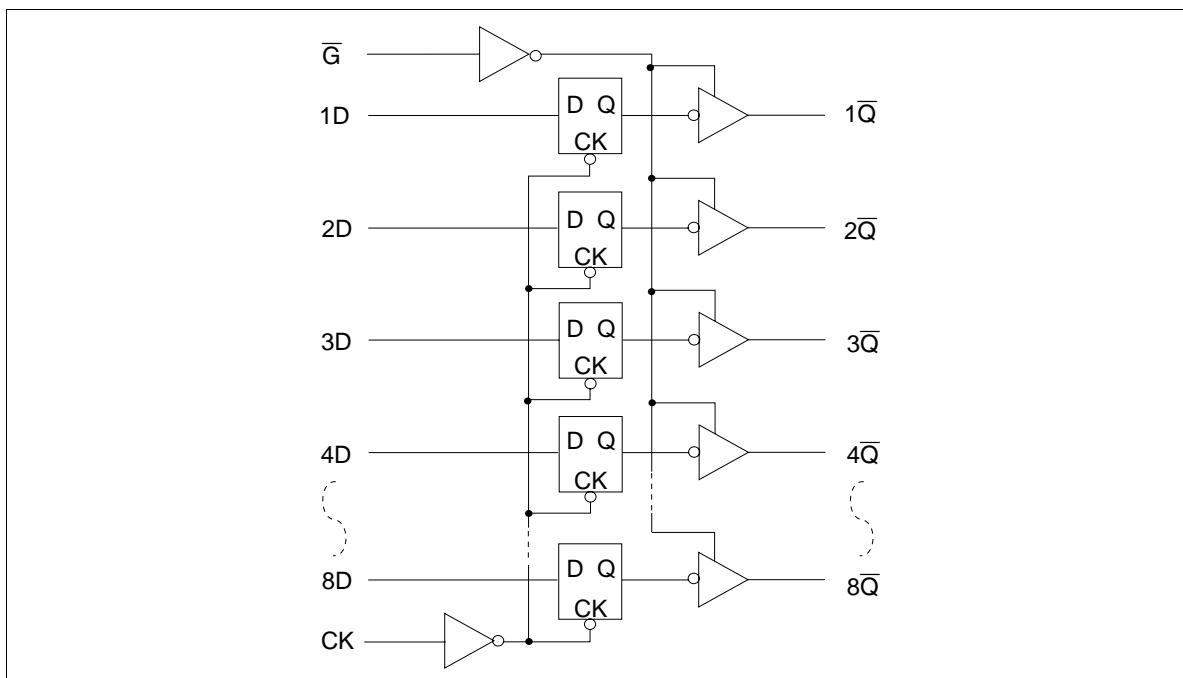
## Recommended Operating Conditions

Item	Symbol	Min	Typ	Max	Unit
Supply voltage	$V_{CC}$	4.5	5.0	5.5	V
Input voltage	$V_{IN}$	0	—	$V_{CC}$	V
Output voltage	$V_{OUT}$	0	—	$V_{CC}$	V
Operating temperature	$Topr$	-40	—	85	°C
Input rise/fall time* <sup>1</sup>	$t_r, t_f$	0	—	8	ns/V

Note: 1. This item guarantees maximum limit when one input switches.

Waveform: Refer to test circuit of switching characteristics.

## Logic Diagram



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### Electrical Characteristics (Ta = -40°C to +85°C)

Item	Symbol	V <sub>cc</sub> (V)	Min	Max	Unit	Test Conditions
Input voltage	V <sub>IH</sub>	2.0	—	—	V	
	V <sub>IL</sub>	—	0.8	—	V	
Output voltage	V <sub>OH</sub>	4.5	2.4	—	V	I <sub>OH</sub> = -3 mA
		4.5	2.0	—	V	I <sub>OH</sub> = -15 mA
	V <sub>OL</sub>	4.5	—	0.4	V	I <sub>OL</sub> = 24 mA
		4.5	—	0.5	V	I <sub>OL</sub> = 48 mA
Input diode voltage	V <sub>IK</sub>	4.5	—	-1.2	V	I <sub>IN</sub> = -18 mA
Input current	I <sub>I</sub>	5.5	—	1.0	μA	V <sub>IN</sub> = 5.5 V
		5.5	—	-250	μA	V <sub>IN</sub> = 0 V
		5.5	—	100	μA	V <sub>IN</sub> = 7.0 V
Short circuit output current <sup>*1</sup>	I <sub>OS</sub>	5.5	-100	-225	mA	V <sub>IN</sub> = 0 or 5.5 V
Off state output current	I <sub>OZH</sub>	5.5	—	-100	μA	V <sub>O</sub> = 2.7 V
	I <sub>OZL</sub>	5.5	—	-250	μA	V <sub>O</sub> = 0.5 V
Supply current	I <sub>CCL</sub>	5.5	—	29.5	mA	V <sub>IN</sub> = 0 or 5.5 V All outputs is "L"
	I <sub>CCH</sub>	5.5	—	2.5	mA	V <sub>IN</sub> = 0 or 5.5 V All outputs is "H"
	I <sub>CCZ</sub>	5.5	—	2.5	mA	V <sub>IN</sub> = 0 or 5.5 V All outputs is "Z"
I <sub>CCT</sub> <sup>*2</sup>	5.5	—	1.5	mA	—	V <sub>IN</sub> = 3.4 or 0.5 V

Notes: 1. Not more than one output should be shorted at a time and duration of the short circuit should not exceed one second.

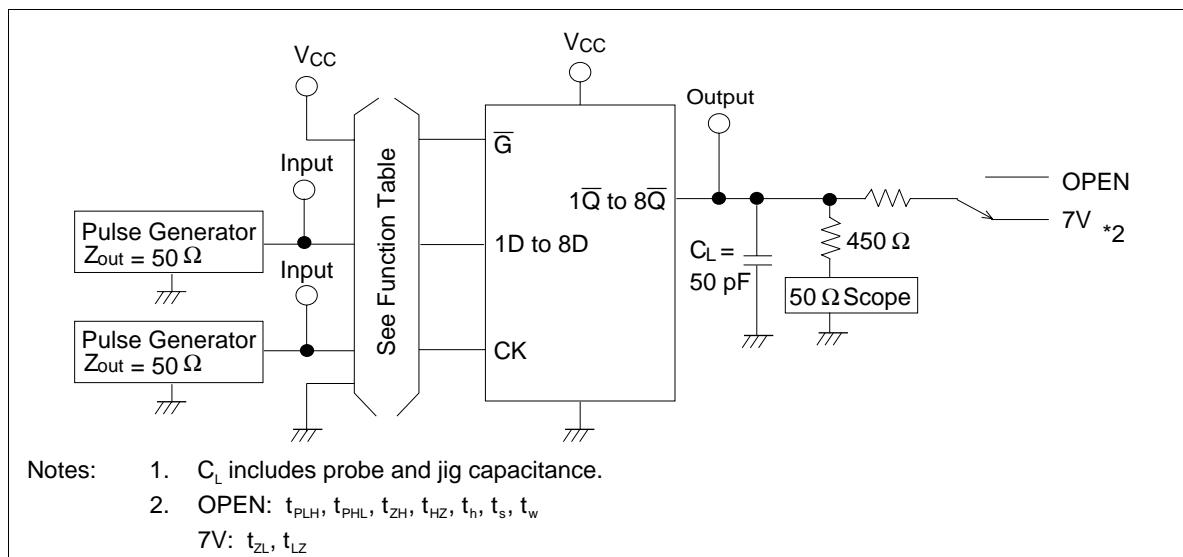
2. When input by the TTL level, it shows I<sub>CC</sub> increase at per one input pin.

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### Switching Test Method ( $C_L = 50 \text{ pF}$ )

Item	Symbol	Ta = 25°C V <sub>cc</sub> = 5.0 V		Ta = -40 to 85°C V <sub>cc</sub> = 5.0 V ±10%		Unit	Test Conditions
		Min	Max	Min	Max		
Propagation delay time	$t_{PLH}$	3.0	8.0	3.0	10.0	ns	See under figure
	$t_{PHL}$	3.0	8.0	3.0	10.0		
Output enable time	$t_{ZH}$	3.0	9.0	3.0	11.0	ns	
	$t_{ZL}$	3.0	9.0	3.0	11.0		
Output disable time	$t_{HZ}$	3.0	8.0	3.0	10.0	ns	
	$t_{LZ}$	3.0	8.0	3.0	10.0		
Setup time	$t_s(H)$	2.0	—	2.0	—	ns	
	$t_s(L)$	2.0	—	2.0	—		
Hold time	$t_h(H)$	2.0	—	2.0	—	ns	
	$t_h(L)$	2.0	—	2.0	—		
Pulse width	$t_w(H)$	6.0	—	6.0	—	ns	
	$t_w(L)$	6.0	—	6.0	—		
Input capacitance	$C_{IN}$	3.0(Typ)	—	—	—	pF	$V_{IN} = V_{cc}$ or GND
Output capacitance	$C_o$	15.0(Typ)	—	—	—	pF	$V_o = V_{cc}$ or GND

### Test Circuit

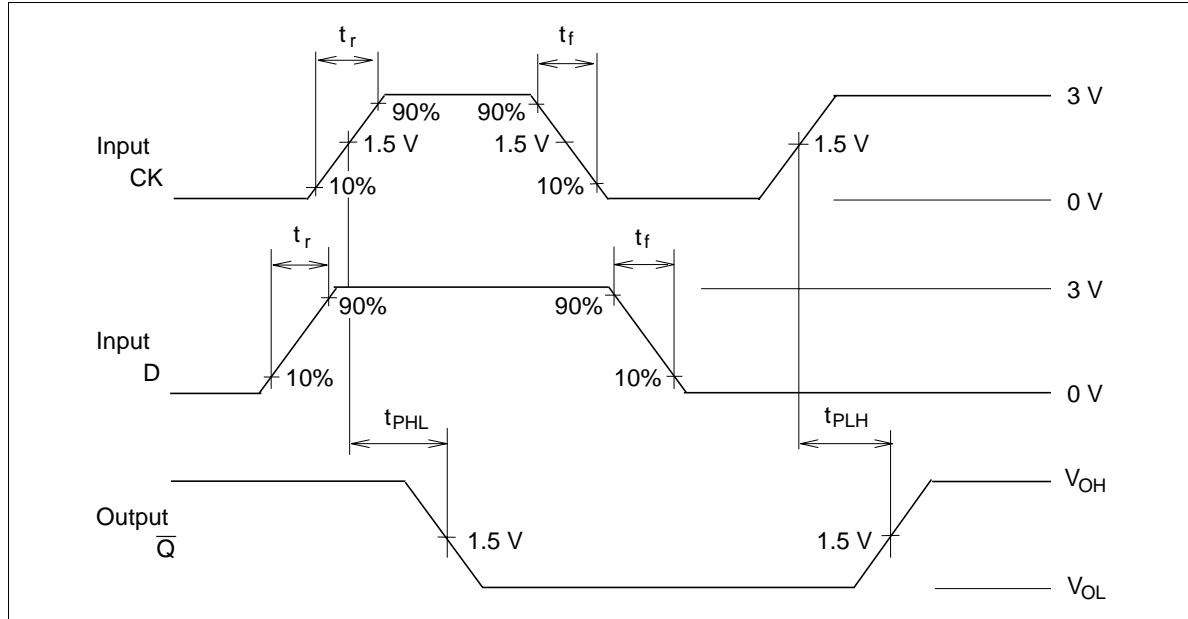


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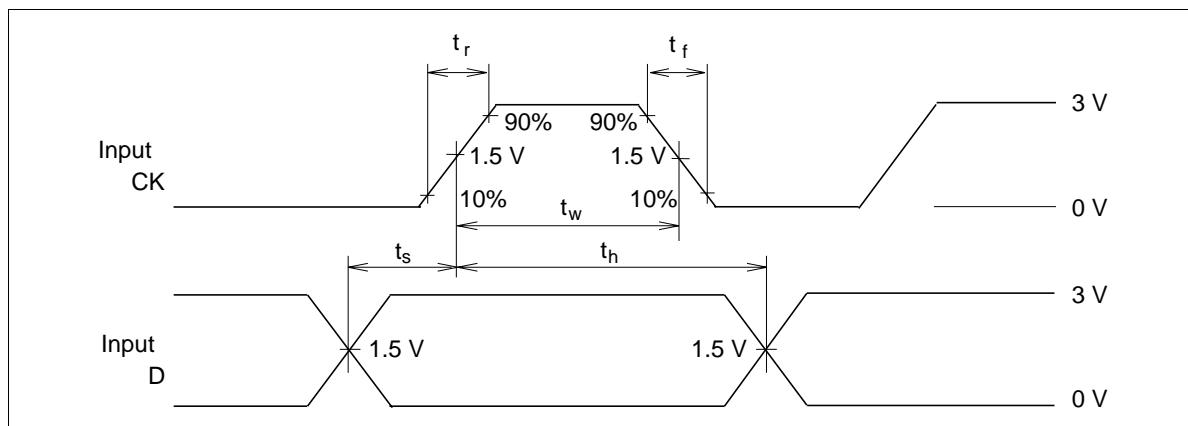
## HD74BC534A

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### Waveforms-1



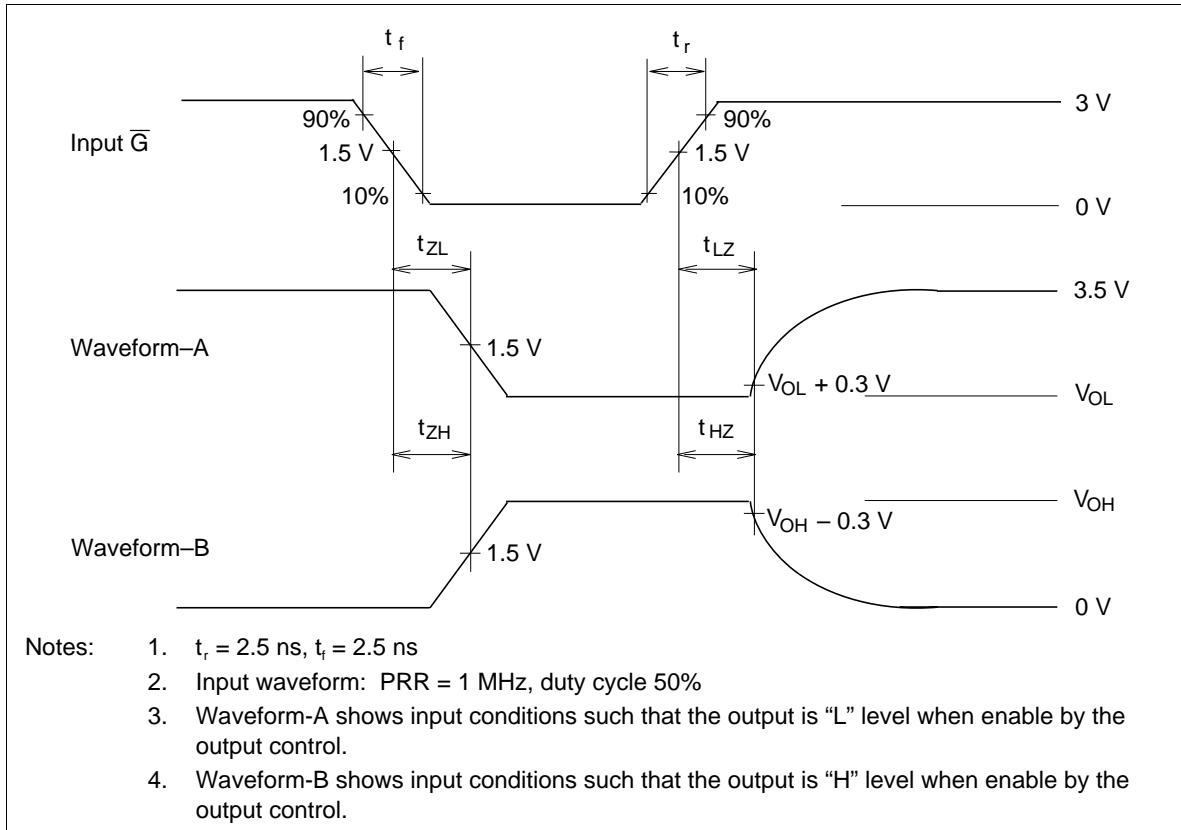
### Waveforms-2



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**HD74BC534A**

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**Waveforms-3**

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## **HD74BC534A**

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### **Package Dimensions**

Unit: mm



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## Cautions

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