Silicon Schottky Barrier Diode for Rectifying

HITACHI

ADE-208-108E(Z) Rev 5 Oct. 1997

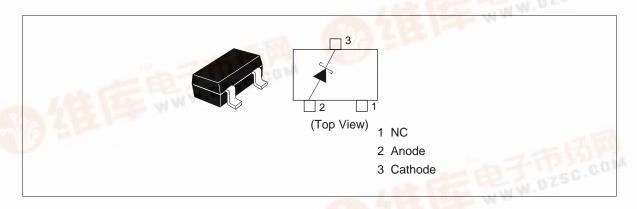
Features

- Low forward voltage drop and suitable for high effifiency rectifying.
- MPAK Package is suitable for high density surface mounting and high speed assembly.

Ordering Information

Type No.	Laser Mark	Package Code
HRW0502A	S10	MPAK

Outline





Absolute Maximum Ratings ($Ta = 25^{\circ}C$)

Item	Symbol	Value	Unit
Repetitive peak reverse voltage	V _{RRM} *1	20	V
Average rectified current	I ₀ *1	500	mA
Non-Repetitive peak forward surge current	I _{FSM} *2	5	Α
Junction temperature	Tj	125	°C
Storage temperature	Tstg	-55 to +125	°C

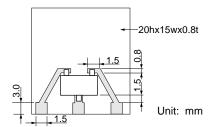
Notes 1. See from Fig.4 to Fig.6

Notes 2. 10msec sine wave 1 pulse

Electrical Characteristics (Ta = 25^{\circ}C)

Item	Symbol	Min	Тур	Max	Unit	Test Condition
Forward voltage	V_{F}	_	_	0.4	V	$I_{F} = 500 \text{ mA}$
Reverse current	I _R	_	_	200	μΑ	V _R = 20V
Capacitance	С	_	120	_	pF	$V_R = 0V, f = 1 MHz$
Thermal resistance	R _{th(i-a)}	_	340	_	°C/W	Polyimide board*1

Notes 1. Polyimide board



Main Characteristic

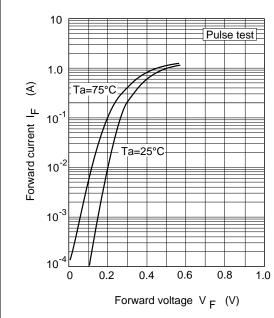


Fig.1 Forward current Vs. Forward voltage

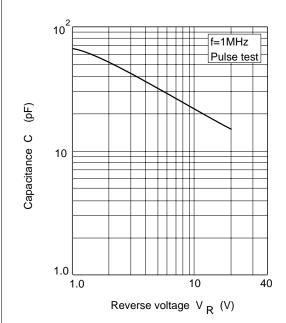


Fig.3 Capacitance Vs. Reverse voltage

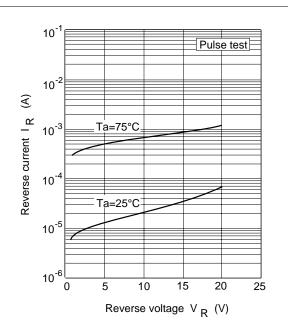


Fig.2 Reverse current Vs. Reverse voltage

Main Characteristic

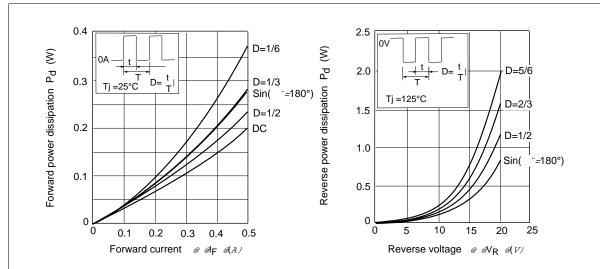


Fig4. Forward power dissipation Vs. Forward current

Fig5. Reverse power dissipation Vs. Reverse voltage

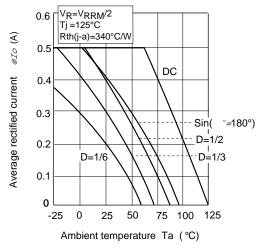
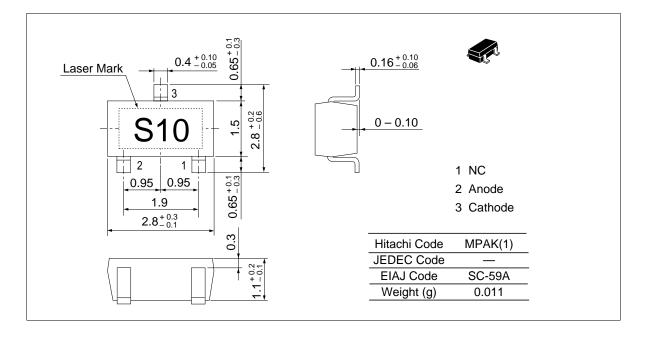


Fig.6 Average rectified current Vs. Ambient temperature

Package Dimensions

Unit: mm



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