HRW0503A

Silicon Schottky Barrier Diode for Rectifying

HITACHI

ADE-208-016C (Z)
Rev 3
Oct. 1997

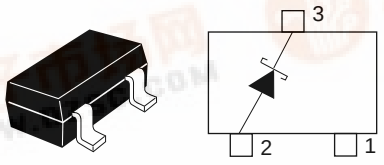
Features

- Low forward voltage drop and suitable for high effifiency rectifying.
- MPAK package is suitable for high density surface mounting and high speed assembly.

Ordering Information

Type No.	Laser Mark	Package Code
HRW0503A	S6	MPAK

Outline



(Top View)

1 NC
2 Anode
3 Cathode

Hitachi semiconductor

Absolute Maximum Ratings (Ta = 25°C)

Item	Symbol	Value	Unit
Repetitive peak reverse voltage	V_{RRM} *1	30	V
Average rectified current	I_o *1	500	mA
Non-Repetitive peak forward surge current	I_{FSM} *2	5	A
Junction temperature	Tj	125	°C
Storage temperature	Tstg	-55 to +125	°C

Note 1. See from Fig.1 to Fig.5, with polyimide board

Note 2. 50Hz sine wave 1 pulse

Electrical Characteristics (Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Forward voltage	V_F	—	—	0.55	V	I_F = 500 mA
Reverse current	I_R	—	—	50	µA	V_R = 30V
Capacitance	C	—	65	—	pF	V_R = 0V, f = 1MHz

Main Characteristic

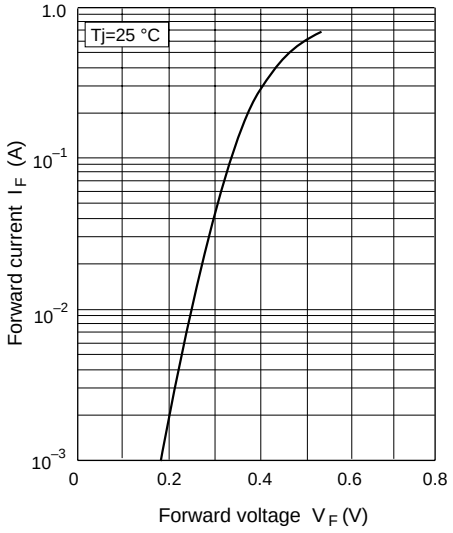


Fig.1 Forward current Vs. Forward voltage

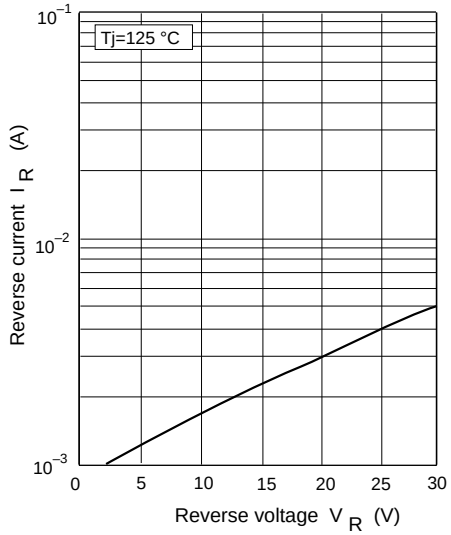


Fig.2 Reverse current Vs. Reverse voltage

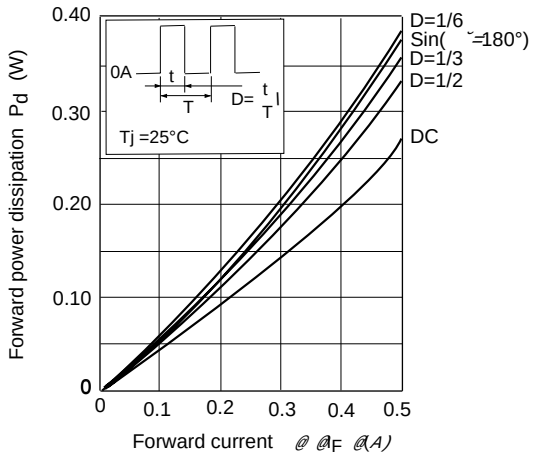


Fig3. Forward power dissipation Vs. Forward current

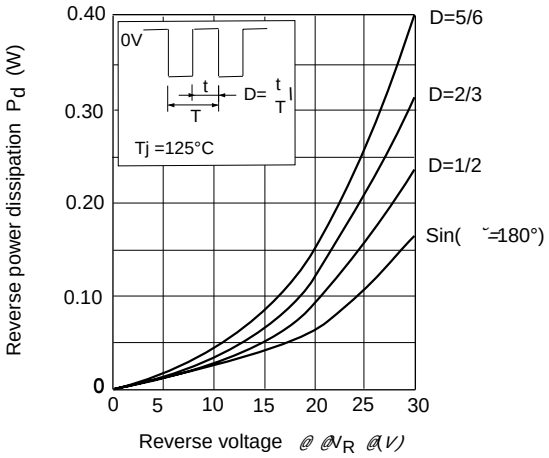


Fig4. Reverse power dissipation Vs. Reverse voltage

Main Characteristic

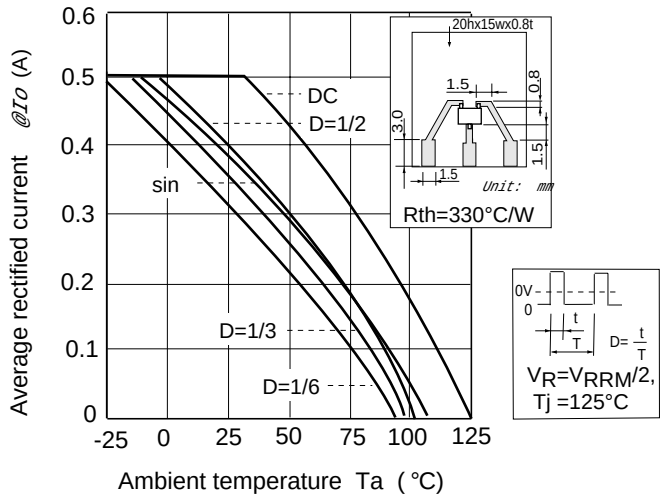


Fig.5 Average rectified current Vs. Ambient temperature

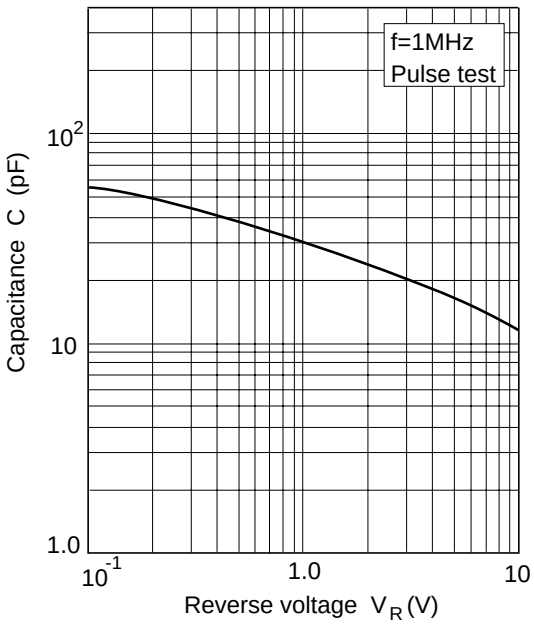
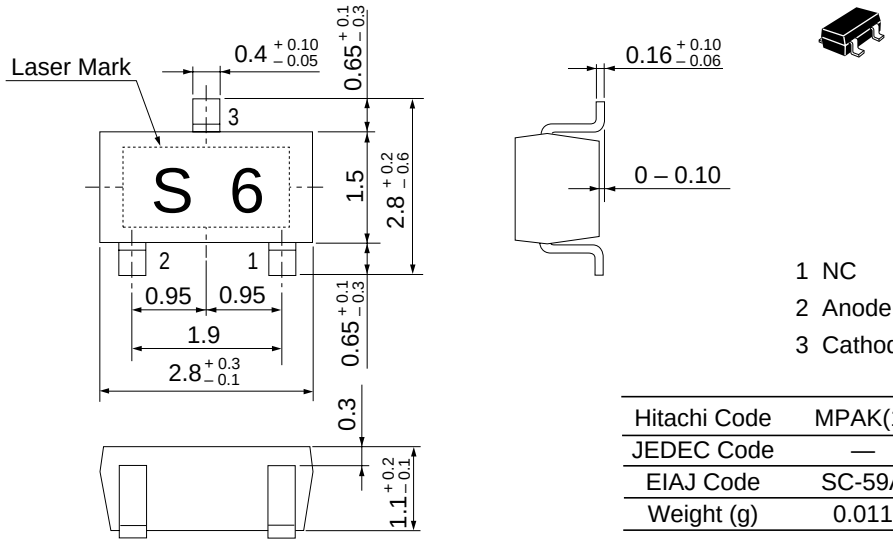


Fig.6 Capacitance Vs. Reverse voltage

Package Dimensions

Unit : mm



Hitachi Code	MPAK(1)
JEDEC Code	—
EIAJ Code	SC-59A
Weight (g)	0.011

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