Silicon Schottky Barrier Diode for Balanced Mixer

HITACHI

ADE-208-026C (Z)

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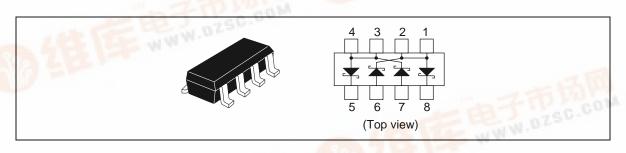
Features

- Small $\Delta V_{\rm F}$ and ΔC .
- Good for surface mounting on printed circuit board.
- Each diode can be biased. W.DZSC.COM
- Wideband operation.

Ordering Information

Type No.	Laser Mark	Package Code
HSB88WS	_	MOP

Pin Arrangemant





Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Value	Unit	
Reverse voltage	V_{R}	10	V	
Average rectified current	I ₀ *	15	mA	
Power dissipation	Pd *	150	mW	
Junction temperature	Tj	125	°C	
Operation temperature	Topr	-40 to +85	°C	
Storage temperature	Tstg	-55 to +125	°C	

Note: 4 devices total

Electrical Characteristics

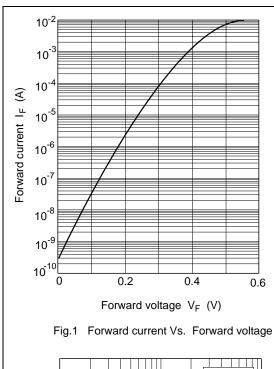
 $(Ta = 25^{\circ}C)$

Item	Symbol	Min	Тур	Max	Unit	Test Condition
Forward voltage	V_{F1}	365	_	435	mV	I _F = 1 mA
	V _{F2}	520	_	600	_	I _F = 10 mA
Reverse current	I _{R1}	_	_	0.2	μΑ	$V_R = 2 V$
	I _{R2}	_	_	10	_	V _R = 10 V
Capacitance	С	_	_	0.85	pF	V _R = 0 V, f = 1 MHz
Capacitance deviation	ΔC *1	_	_	0.2	pF	$V_R = 0 \text{ V}, f = 1 \text{ MHz}$
Forward voltage deviation	$\Delta V_{_F}^{~*1}$	_	_	15	mV	I _F = 10 mA
ESD-Capability *2	_	30	_		V	$C = 200 \text{ pF}, R = 0 \Omega$, Both forward and reverse direction 1 pulse.

Notes: 1. Deviation between 4 devices in one package

2. Failure criterion ; $I_{\mbox{\tiny R}} > 0.4~\mu\mbox{A}$ at $V_{\mbox{\tiny R}} = 2~\mbox{V}$

Main Characteristic



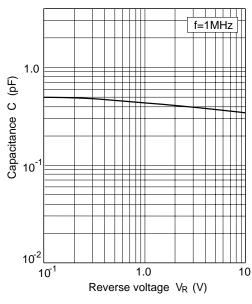


Fig.3 Capacitance Vs. Reverse voltage

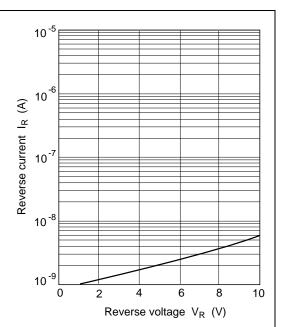
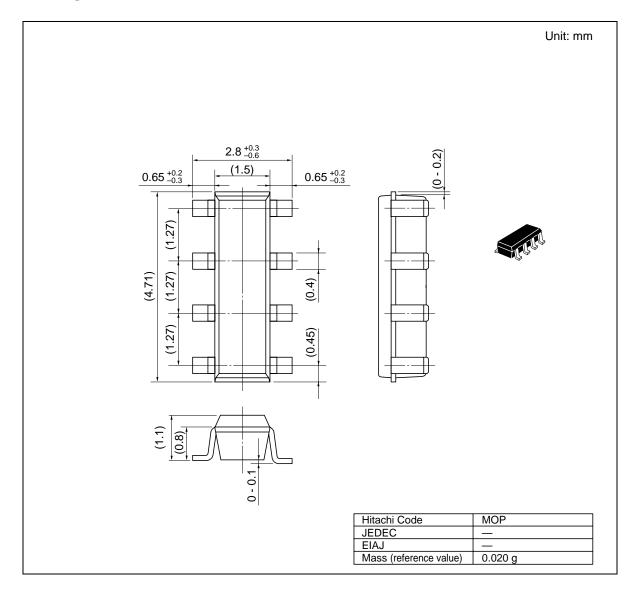


Fig.2 Reverse current Vs. Reverse voltage

Package Dimensions



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Semiconductor & Integrated Circuits Nippon Bldg., 2-6-2, Onte-machi, Chiyoda-ku, Tokyo 100-0004, Japan Tel: (03) 3270-2111 Fax: (03) 3270-5109

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For further information write to:

Hitachi Semiconductor (America) Inc. 179 East Tasman Drive Hitachi Europe Ltd. Electronic Components Group Whitebrook Park Tel: <44> (1628) 585000

Fax: <44> (1628) 585200 Hitachi Europe GmbH Electronic Components Group Dornacher Straße 3 D-85622 Feldkirchen, Munich Germany Tel: <49> (89) 9 9180-0

Fax: <49> (89) 9 29 30 00

Hitachi Tower 16 Collyer Quay #20-00 URL: http://www.hitachi.com.sg Hitachi Asia Ltd.

Hitachi Asia Ltd.

(Taipei Branch Office) 4/F, No. 167, Tun Hwa North Road Hung-Kuo Building Taipei (105), Taiwan Tel: <886>-(2)-2718-3666 Fax: <886>-(2)-2718-8180

7/F North Tower World Finance Centre, Harbour City, Canton Road Tsim Sha Tsui, Kowloon Hong Kong Tel : <852>-(2)-735-9218 Fax: <852>-(2)-730-0281

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