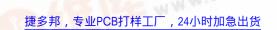
查询HVD132供应商



ADE-208-830 (Z)

Rev 0 Nov. 1999

HVD132

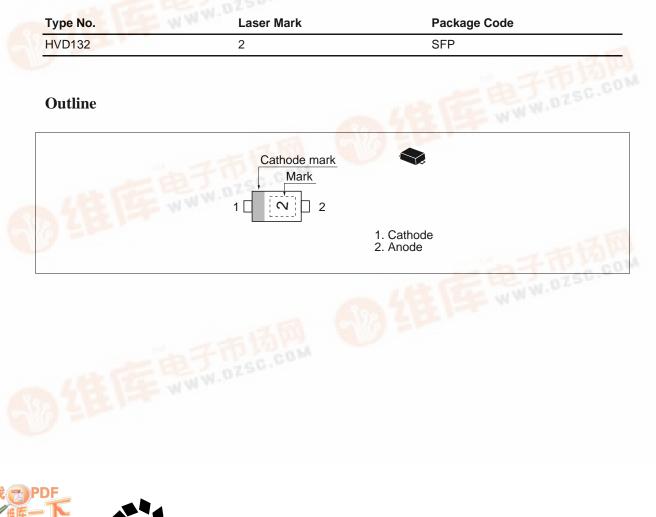
Silicon Epitaxial Planar Pin Diode for High Frequency Switching

	HITACHI
Fe	atures
٠	Low capacitance.(C=0.5pF max)
٠	Low forward resistance. (rf= 2.0Ω max)

Super small Flat Package (SFP) is suitable for surface mount design. •

Ordering Information

c. chitach



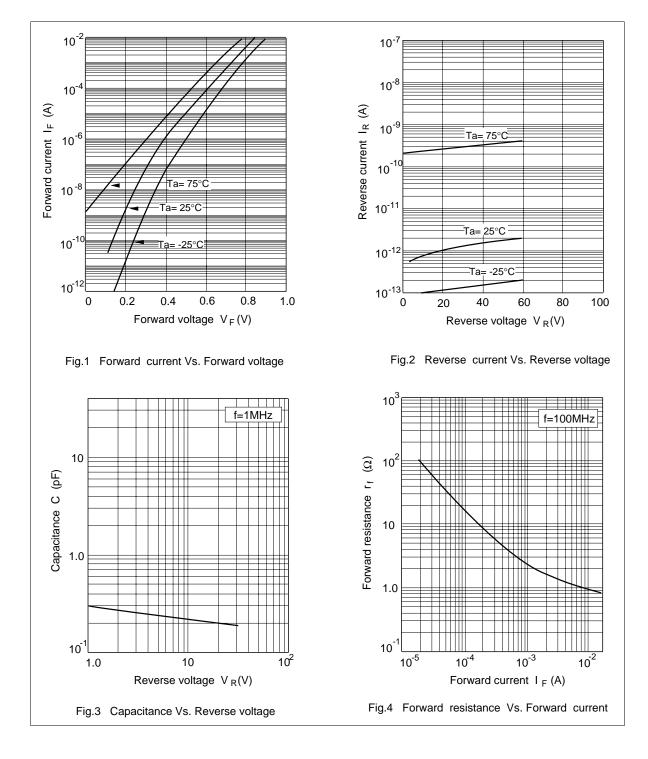
Absolute Maximum Ratings (Ta = 25° C)

Item	Symbol	Value	Unit
Peak reverse voltage	V _{RM}	65	V
Reverse voltage	V _R	60	V
Forward current	I _F	100	mA
Power dissipation	P _d	150	mW
Junction temperature	Тј	125	°C
Storage temperature	Tstg	-55 to +125	°C

Electrical Characteristics (Ta = 25°C)

ltem	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse current	I _R	_	_	0.1	μΑ	$V_R = 60V$
Forward voltage	V _F	—		1.0	V	I _F = 10 mA
Capacitance	С		—	0.5	pF	$V_R = 1V$, f = 1 MHz
Forward resistance	r _f	—		2.0	Ω	I _F = 10 mA, f = 100 MHz

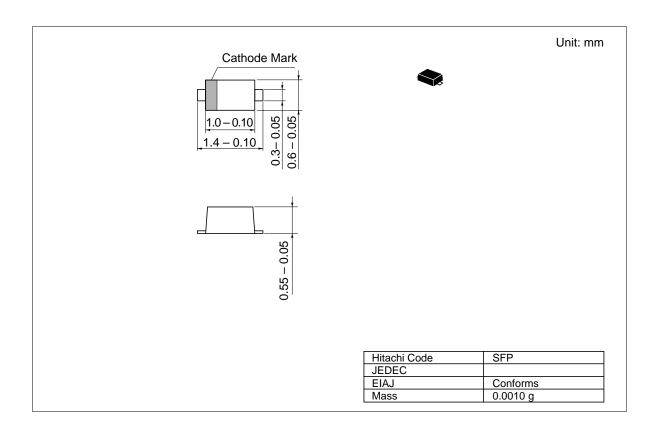
Note: Please mount soldering SFP by the reflow. (Reflow condition: 235°C max, for 10 seconds) Do not use the soldering iron because soldering SFP causes package a big heat stress.



Main Characteristic

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Package Dimensions



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