Silicon Epitaxial Planar Zener Diode for Surge Absorb

HITACHI

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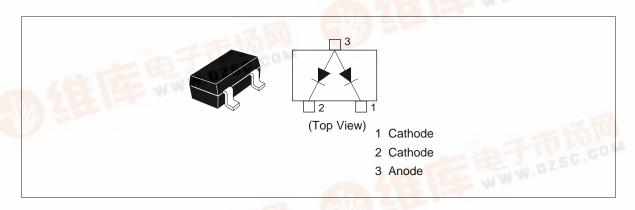
Features

- HZM6.8ZWA has two devices, and can absorb external + and -surge.
- Low capacitance (C=25pF max) and can protect ESD of signal line.
- MPAK Package is suitable for high density surface mounting and high speed assembly.

Ordering Information

Type No.	Laser Mark	Package Code
HZM6.8ZWA	68Z	MPAK

Outline





Absolute Maximum Ratings ($Ta = 25^{\circ}C$)

Item	Symbol	Value	Unit
Power dissipation	Pd *1	200	mW
Junction temperature	Тј	150	°C
Storage temperature	Tstg	-55 to +150	°C

Note 1. Two device total, See Fig.2.

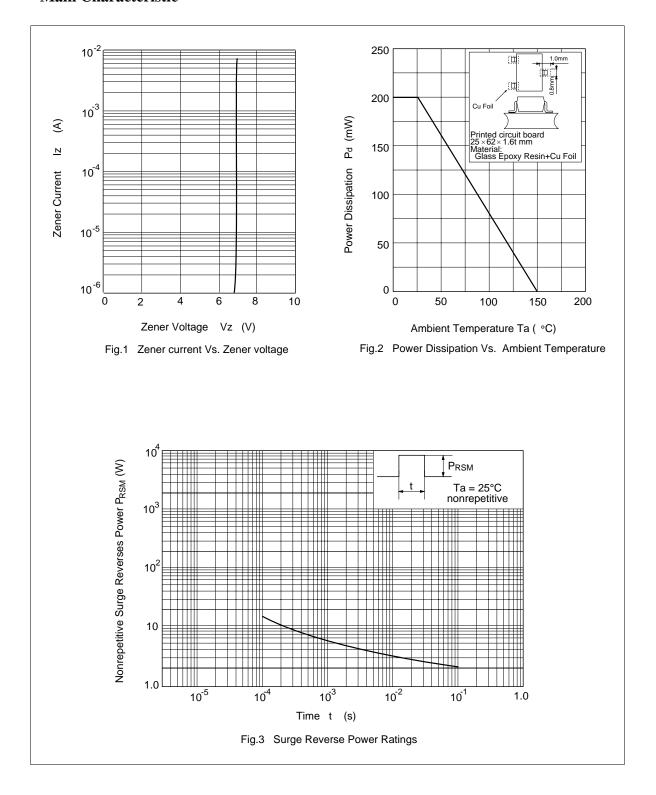
Electrical Characteristics (Ta = 25° C) *1

Item	Symbol	Min	Тур	Max	Unit	Test Condition
Zener voltage	V _z	6.47	_	7.00	V	$I_z = 5$ mA, 40ms pulse
Reverse current	I_R	_	_	2	μΑ	V _R = 3.5V
Capacitance	С	_	_	25	pF	$V_R = 0V, f = 1 MHz$
Dynamic resistance	r _d	_	_	30	Ω	$I_z = 5 \text{ mA}$
ESD-Capability *2	_	20	_	_	kV	C =150pF, R = 330 Ω , Both forward and reverse direction 10 pulse

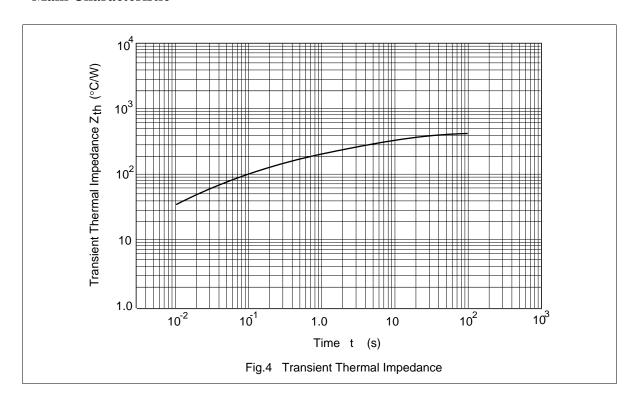
Notes 1. Per one device.

2. Failure criterion ; IR > 2 μ A at VR = 3.5V.

Main Characteristic

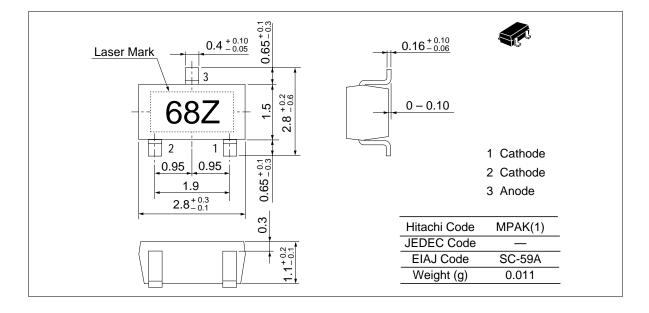


Main Characteristic



Package Dimensions

Unit: mm



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