

# HZU6.2Z

Silicon Epitaxial Planar Zener Diode for Surge Absorb

# HITACHI

ADE-208-581(Z)

Rev 0

Oct. 1997

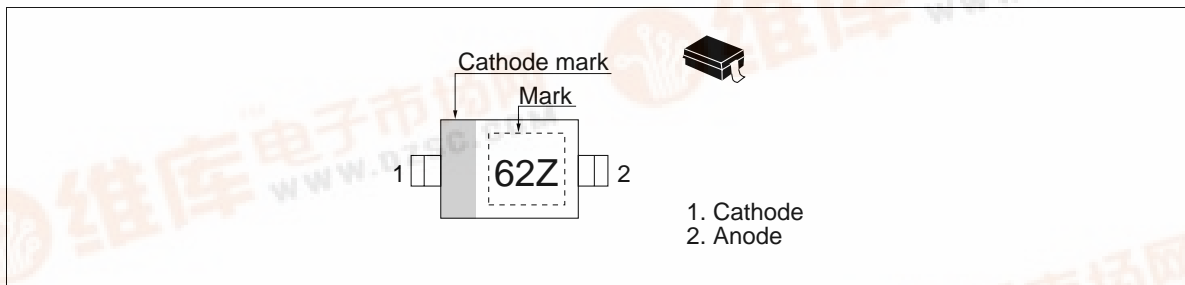
## Features

- Low capacitance ( $C=8.5\text{pF}$  max) and can protect ESD of signal line.
- Ultra small Resin Package (URP) is suitable for surface mount design.

## Ordering Information

Type No.	Laser Mark	Package Code
HZU6.2Z	62Z	URP

## Outline



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### Absolute Maximum Ratings (Ta = 25°C)

Item	Symbol	Value	Unit
Power dissipation	Pd **	200	mW
Junction temperature	Tj	150	°C
Storage temperature	Tstg	-55 to +150	°C

Note 1. Two device total, See Fig.2.

### Electrical Characteristics (Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Zener voltage	V <sub>Z</sub>	5.90	—	6.50	V	I <sub>Z</sub> = 5 mA, 40ms pulse
Reverse current	I <sub>R</sub>	—	—	3	μA	V <sub>R</sub> = 5.5V
Capacitance	C	—	8.0	8.5	pF	V <sub>R</sub> = 0V, f = 1 MHz
Dynamic resistance	r <sub>d</sub>	—	—	60	Ω	I <sub>Z</sub> = 5 mA

Main Characteristic

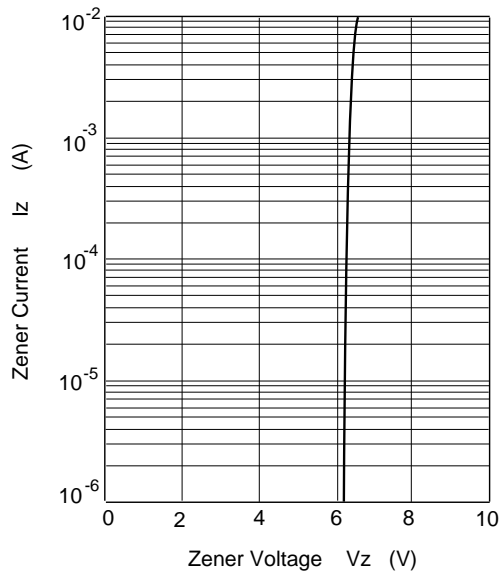


Fig.1 Zener current Vs. Zener voltage

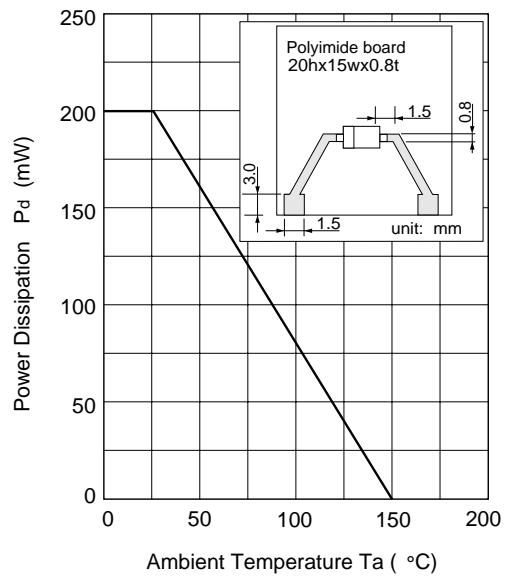


Fig.2 Power Dissipation Vs. Ambient Temperature

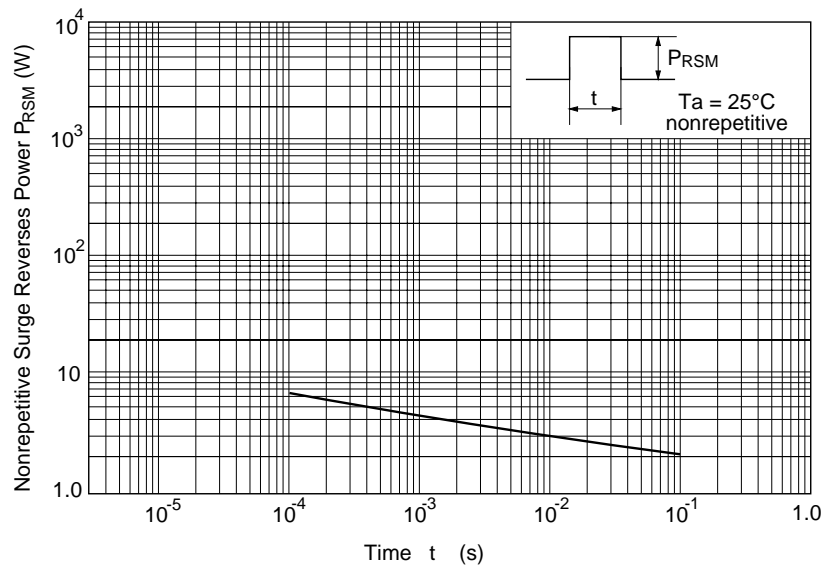


Fig.3 Surge Reverse Power Ratings

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## HZU6.2Z

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### Main Characteristic

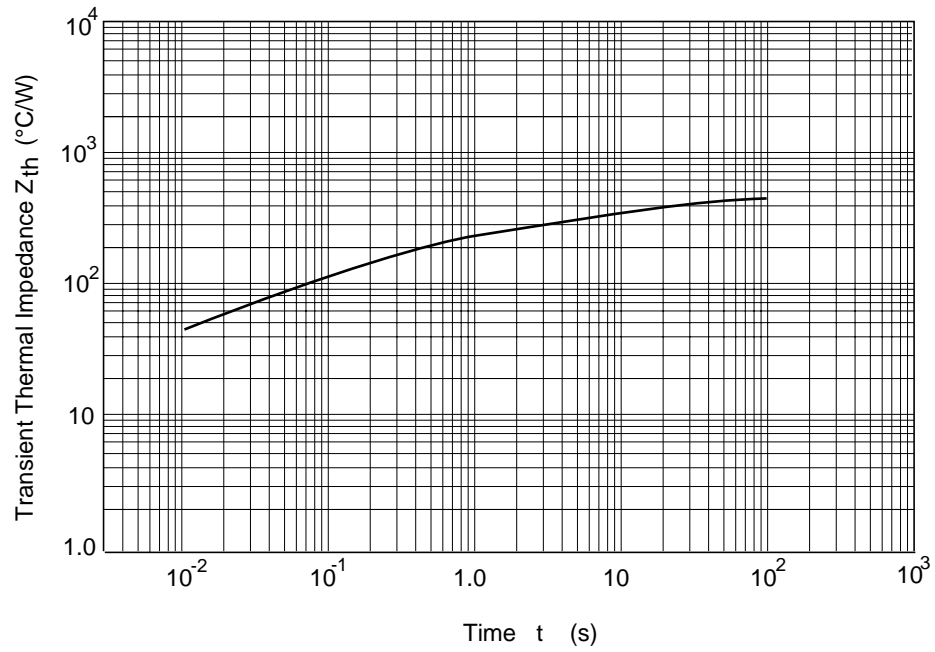
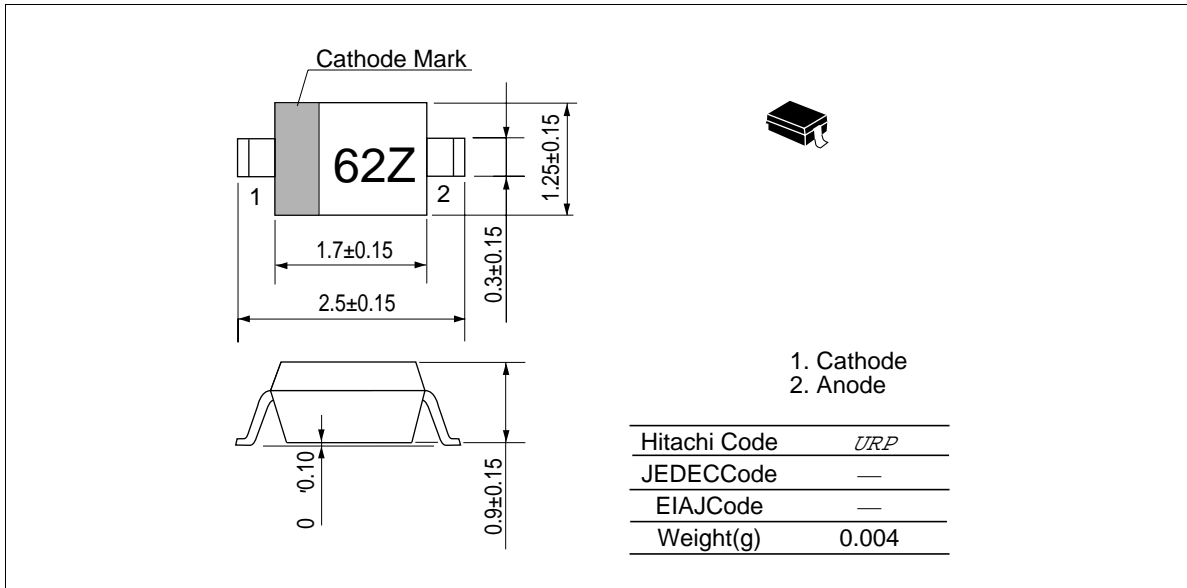


Fig.4 Transient Thermal Impedance

**Package Dimensions**

Unit : mm



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