



New Product

**Si4429EDY**  
Vishay Siliconix

**P-Channel 30-V (D-S) MOSFET**

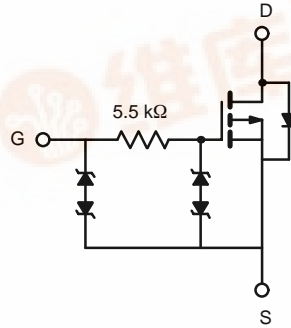
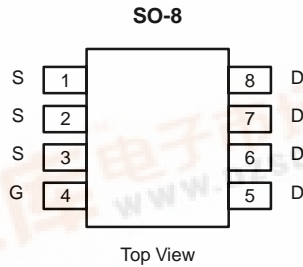
PRODUCT SUMMARY		
$V_{DS}$ (V)	$r_{DS(on)}$ ( $\Omega$ )	$I_D$ (A)
-30	0.0105 @ $V_{GS} = -10$ V	-13.0
	0.0125 @ $V_{GS} = -4.5$ V	-12.0
	0.0195 @ $V_{GS} = -2.5$ V	-9.0

**FEATURES**

- TrenchFET® Power MOSFET
- $V_{GS}$  Surge Protection to 18 V
- ESD Protected: 4000 V

**APPLICATIONS**

- Battery Switch
- Load Switch



ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)					
Parameter	Symbol	10 secs	Steady State	Unit	
Drain-Source Voltage	$V_{DS}$	-30		V	
Gate-Source Voltage	$V_{GS}$	$\pm 12$			
Continuous Drain Current ( $T_J = 150^\circ\text{C}$ ) <sup>a</sup>	$I_D$	$T_A = 25^\circ\text{C}$	-13.0	-9.4	A
		$T_A = 70^\circ\text{C}$	-10.0	-7.5	
Pulsed Drain Current	$I_{DM}$	-50			
continuous Source Current (Diode Conduction) <sup>a</sup>	$I_S$	-2.5	-1.3		
Maximum Power Dissipation <sup>a</sup>	$P_D$	$T_A = 25^\circ\text{C}$	3.0	1.5	W
		$T_A = 70^\circ\text{C}$	1.9	0.9	
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	-55 to 150		$^\circ\text{C}$	

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>a</sup>	$R_{thJA}$	$t \leq 10$ sec	32	42	$^\circ\text{C/W}$
		Steady State	68	85	
Maximum Junction-to-Foot (Drain)	$R_{thJF}$	15	18		

Notes:  
a. Surface Mounted on 1" x 1" FR4 Board.

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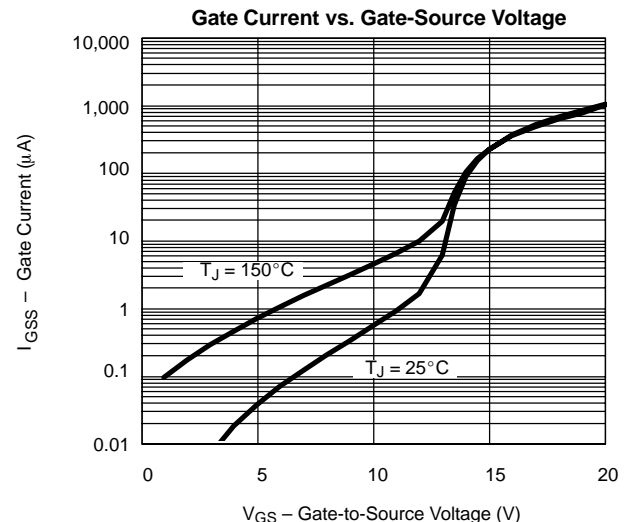
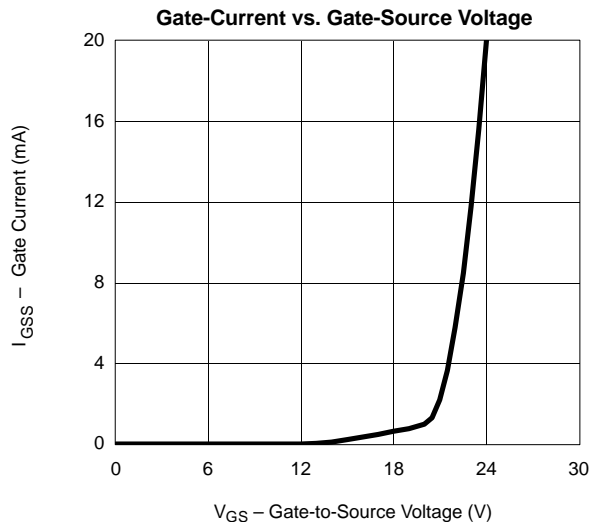


SPECIFICATIONS (T <sub>J</sub> = 25 °C UNLESS OTHERWISE NOTED)						
Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
<b>Static</b>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250 μA	-0.60			V
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ± 12 V			± 20	μA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = -24 V, V <sub>GS</sub> = 0 V			-1	
		V <sub>DS</sub> = -24 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			-5	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> ≤ -5 V, V <sub>GS</sub> = -10 V	-30			A
Drain-Source On-State Resistance <sup>a</sup>	r <sub>DS(on)</sub>	V <sub>GS</sub> = -10 V, I <sub>D</sub> = -13.0 A		0.0086	0.0105	Ω
		V <sub>GS</sub> = -4.5 V, I <sub>D</sub> = -12.0 A		0.0105	0.0125	
		V <sub>GS</sub> = -2.5 V, I <sub>D</sub> = -9.0 A		0.0160	0.0195	
Forward Transconductance <sup>a</sup>	g <sub>fs</sub>	V <sub>DS</sub> = -15 V, I <sub>D</sub> = -13.0 A		40		S
Diode Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>S</sub> = -2.5 A, V <sub>GS</sub> = 0 V		-0.8	-1.2	V
<b>Dynamic<sup>b</sup></b>						
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> = -15 V, V <sub>GS</sub> = -4.5 V, I <sub>D</sub> = -13.0 A		51	75	nC
Gate-Source Charge	Q <sub>gs</sub>			9		
Gate-Drain Charge	Q <sub>gd</sub>			12.0		
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> = -15 V, R <sub>L</sub> = 15 Ω I <sub>D</sub> ≅ -1 A, V <sub>GEN</sub> = -10 V, R <sub>G</sub> = 6 Ω		14	21	μs
Rise Time	t <sub>r</sub>			19	29	
Turn-Off Delay Time	t <sub>d(off)</sub>			54	80	
Fall Time	t <sub>f</sub>			41	62	

**Notes**

- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2%.
- b. Guaranteed by design, not subject to production testing.

## TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)

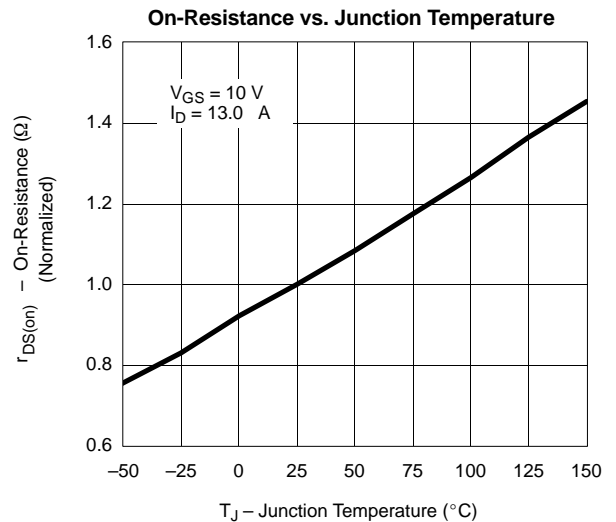
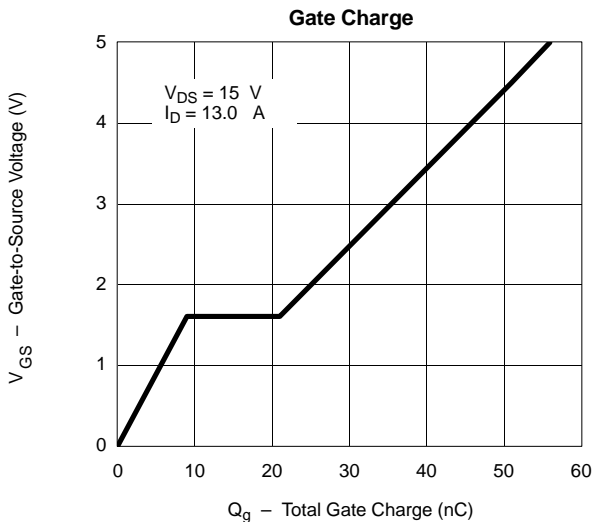
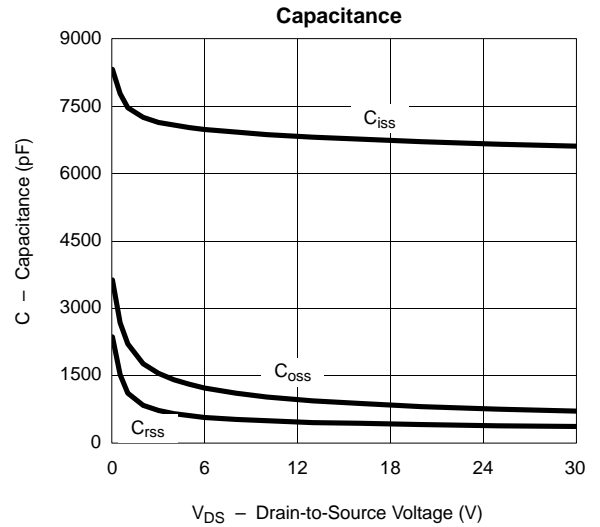
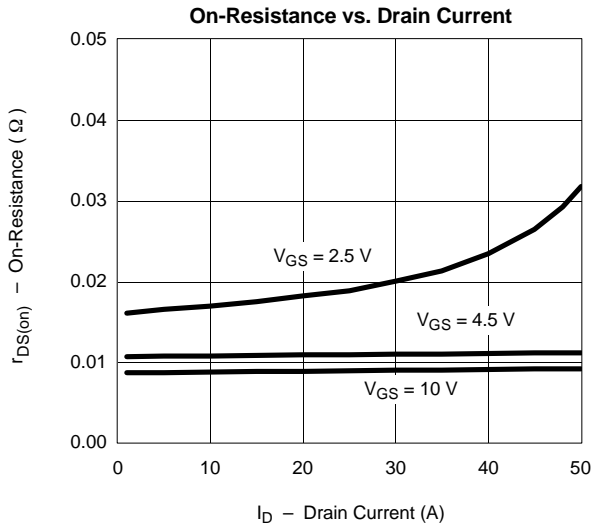
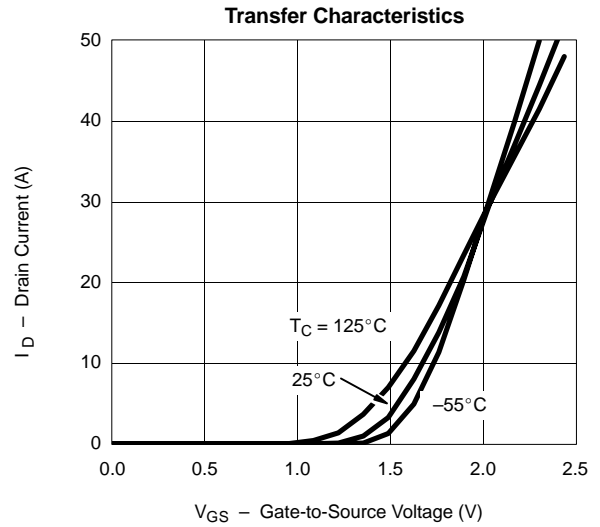
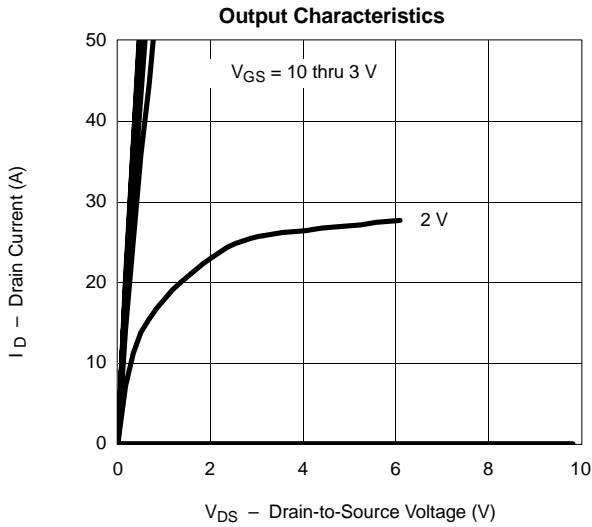




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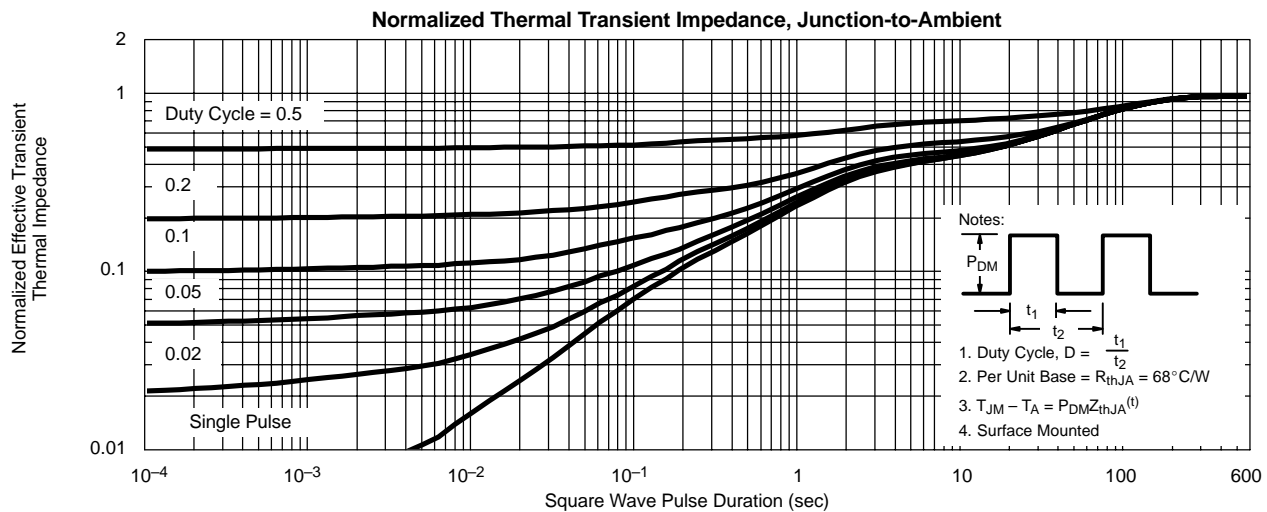
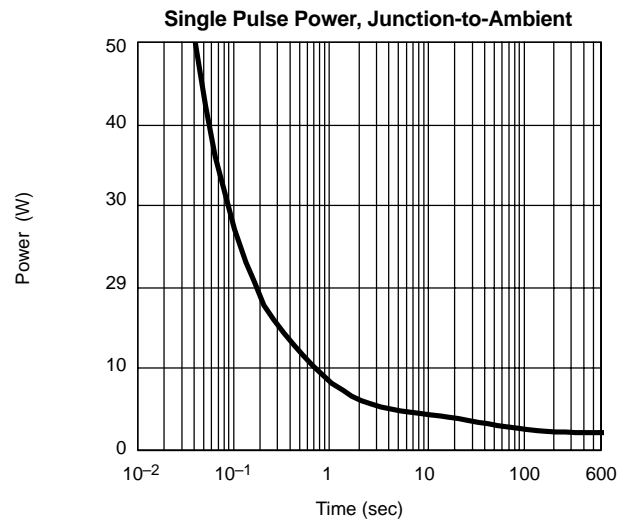
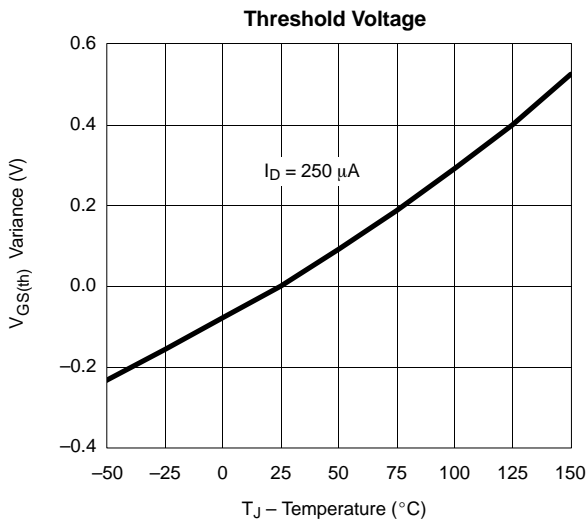
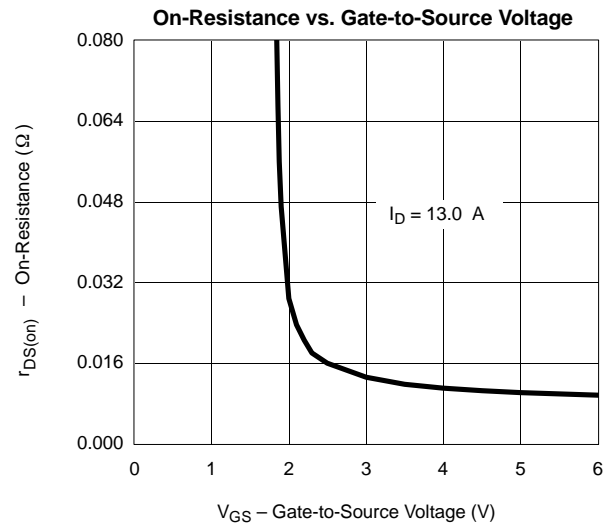
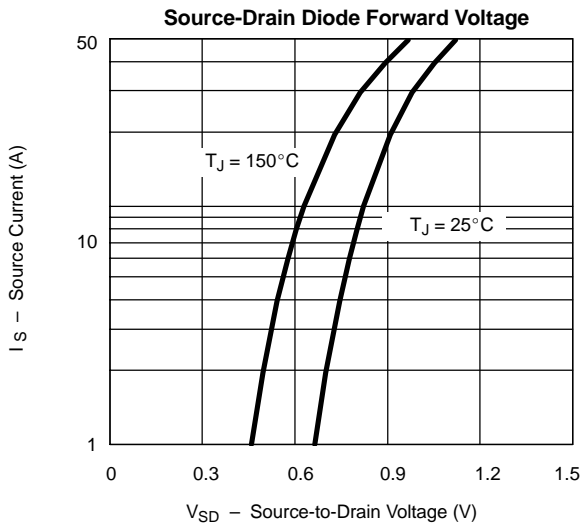
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