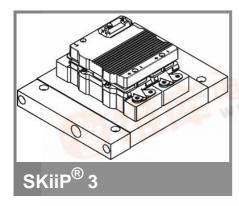
捷多邦,专业PCB打样工厂,24小时加急出货

SKiiP 1203GB172-2DW



2-pack-integrated intelligent Power System

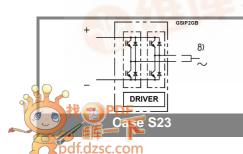
Power section

SKiiP 1203GB172-2DW

Preliminary Data

Features

- SKiiP technology inside
- Trench IGBTs
- CAL diode technology
- Integrated current sensor
- Integrated temperature sensor
- Integrated heat sink
- IEC 60721-3-3 (humidity) class 3K3/IE32 (SKiiP[®] 3 System)
- IEC 60068-1 (climate) 40/125/56
- UL recognized file no. E63532
- with assembly of suitable MKP capacitor per terminal (SEMIKRON type is recommended)
- AC connection busbars must be connected by the user; copper busbars available on request

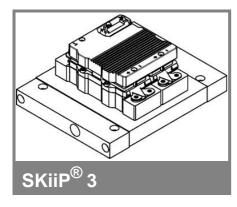


Absolute Maximum Ratings					
Symbol	Conditions	Values	Units		
IGBT	ET CHIL				
V _{CES}	MOD	1700	V		
V _{CES} V _{CC} ¹⁾	Operating DC link voltage	1200	V		
V _{GES}		± 20	V		
I _C	T _s = 25 (70) °C	1200 (900)	Α		
Inverse diode					
	T _s = 25 (70) °C	900 (700)	A		
I _{FSM}	T _j = 150 °C, t _p = 10 ms; sin.	6900	A		
I²t (Diode)	Diode, T _j = 150 °C, 10 ms	238	kA²s		
T _j , (T _{stg})		- 40 + 150 (125)	°C		
V _{isol}	rms, AC, 1 min, m <mark>ain termin</mark> als to heat sink	4000	V		
I _{AC-terminal}	per AC terminal, rms, T _s = 70 °C,	400	А		
TP	T _{terminal} <115 °C				

Characteristics			$T_s = 25^{\circ}C$ unless otherwise specified					
Symbol	Conditions			min.	typ.	max.	Units	
IGBT								
V _{CEsat}	I _C = 600 A measured at	A, T _j = 25 (1 terminal	25) °C;		="#	1,9 (2,2)	2,4	V
V _{CEO}	$T_i = 25 (12)$	25) °C; at t	erminal		ALC: N	1 (0,9)	1,2 (1,1)	V
r _{CE}	T _i = 25 (12	25) °C; <mark>at t</mark>	erminal			1,5 (2,1)	1,9 (2,5)	mΩ
I _{CES}	$V_{GE} = 0 V$ T _i = 25 (12)	^r , V _{CE} = V _C 25) °C	ES'			2,4 (144)		mA
E _{on} + E _{off}	$I_{\rm C} = 600 A$	A, V _{CC} = 90	0 V			390		mJ
191.02	T _j = 125 °	C, V _{CC} = 1	200 V			575		mJ
R _{CC+EE}	terminal c	hip, T _i = 25	°C			0,25		mΩ
L _{CE}	top, bottor	m				6		nH
C _{CHC}	per phase	, AC-side				2		nF
Inverse o	diode				100	17	270	A.
V _F = V _{EC}	I _F = 600 A measured at	v, T _j = 25 (1 terminal	25) °C		E E	2 (1,8)	2,15	V
V _{TO}	$T_i = 25(12)$	25) °C			-	1,1 (0,8)	1,2 (0,9)	V
r _T	$T_{i} = 25(12)$					1,5 (1,7)	1,6 (1,8)	mΩ
Err	$I_{\rm C} = 600 A$	A, V _{CC} = 90	0 V			72		mJ
5 3	T _j = 125 °	C, V _{CC} = 1	200 V			86		mJ
Mechani	cal data							
M _{dc}	DC termin	nals, SI Uni	ts		6		8	Nm
M_{ac}		ials, SI Uni			13		15	Nm
w	SKiiP [®] 3 S	System w/o	heat sink			1,7		kg
w	heat sink					4,3	200	kg
						:.); "s" re (acc. IEC		
R _{th(j-s)I}	per IGBT						0,026	K/W
R _{th(j-s)D}	per diode						0,05	K/W
Z _{th}	R _i (mK/W)) (max. valu	ues)			tau		
7 4	10.00	2	3	4	1	2	3	4
Z _{th(j-r)I}	2,8	11,6	13,6	0	69	0,35	0,02	1
Z _{th(j-r)D}	4	6	26	26	50	5	0,25	0,04
Z _{th(r-a)}	5,5	4,8	1,1	0,6	48	15	2,8	0,4

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SKiiP 1203GB172-2DW



2-pack-integrated intelligent Power System

2-pack integrated gate driver SKiiP 1203GB172-2DW

Preliminary Data

Gate driver features

- CMOS compatible inputs
- Wide range power supply
- Integrated circuitry to sense phase current, heat sink temperature and DC-bus voltage (option)
- Short circuit protection
- Over current protection
- Over voltage protection (option)
- Power supply protected against under voltage
- Interlock of top/bottom switch
- Isolation by transformers
- Fibre optic interface (option for GB-types only)
- IEC 60068-1 (climate) 40/85/56)
- UL recognized file no. 242581

Absolute	Maximum Ratings	$T_a = 25^{\circ}C$ unless otherwise specified		
Symbol	Conditions	Values	Units	
V _{S2}	unstabilized 24 V power supply	30	V	
V _i	input signal voltage (high)	15 + 0,3	V	
dv/dt	secondary to primary side	75	kV/µs	
V _{isolIO}	input / output (AC, rms, 2s)	4000	V	
VisoIPD	partial discharge extinction voltage, rms, $Q_{PD} \leq 10 \text{ pC}$;	1500	V	
V _{isol12}	output 1 / output 2 (AC, rms, 2s)	1500	V	
f _{sw}	switching frequency	14	kHz	
f _{out}	output frequency for I=I _C ; sin.	1	kHz	
T _{op} (T _{stg})	operating / storage temperature	- 40 + 85	°C	

Characte	eristics	(T _a = 2			= 25°C)
Symbol	Conditions	min.	typ.	max.	Units
V _{S2}	supply voltage non stabilized	13	24	30	V
I _{S2}	V _{S2} = 24 V	320+23*f/kHz+0,00022*(I _{AC} /A) ²			mA
V _{iT+}	input threshold voltage (High)			12,3	V
V _{iT-}	input threshold voltage (Low)	4,6			V
R _{IN}	input resistance		10		kΩ
C _{IN}	input capacitance		1		nF
t _{d(on)IO}	input-output turn-on propagation time		1,3		μs
t _{d(off)IO}	input-output turn-off propagation time		1,3		μs
tpERRRESET	error memory reset time		9		μs
t _{TD}	top / bottom switch interlock time		3,3		μs
l analogOUT	max. 5mA; 8 V corresponds to 15 V supply voltage for external components		1000		A
I _{s1out}	max. load current			50	mA
ITRIPSC	over current trip level (I _{analog} OUT = 10 V)		1250		А
T _{tp}	over temperature protection	110	1200	120	°C
	U_{DC} -protection ($U_{analog OUT} = 9 V$);	i	not mplemente	d	V
	(option for GB types)				

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