

SONY

ILX547K

2700 × 3 pixel CCD Linear Sensor (Color)

Description

The ILX547K is a reduction type CCD linear sensor developed for color image scanner. This sensor reads A4-size documents at a density of 300DPI. The distance between lines is only 4 line (32μm). The power supply of this device is single 5V.

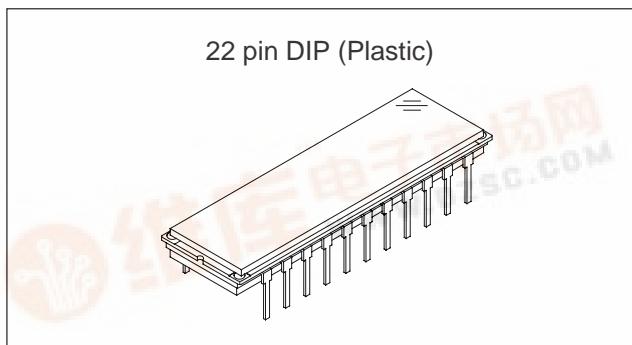
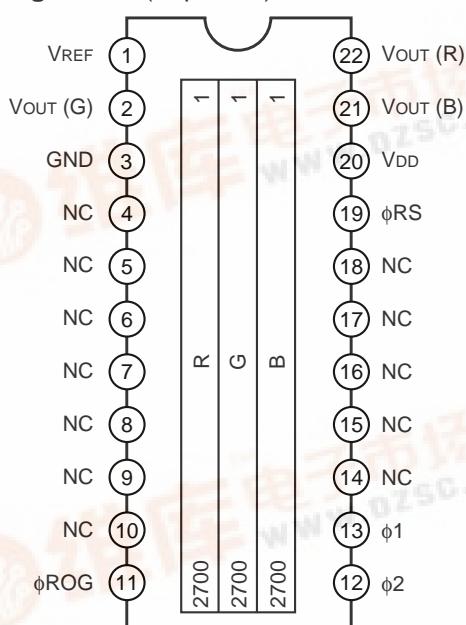
Features

- Number of effective pixels: 8100 pixels
(2700 pixels × 3)
- Pixel size: 8μm × 8μm (8μm pitch)
- Distance between line: 32μm (4 lines)
- Maximum data rate: 6MHz (2MHz × 3)
- Built-in clamp circuit
- Low lag/High sensitivity
- Single 5V power supply
- Input clock pulse: CMOS 5V drive
- Number of output: 3 (R, G, B)
- Package: 22 pin Plastic DIP (400mil)

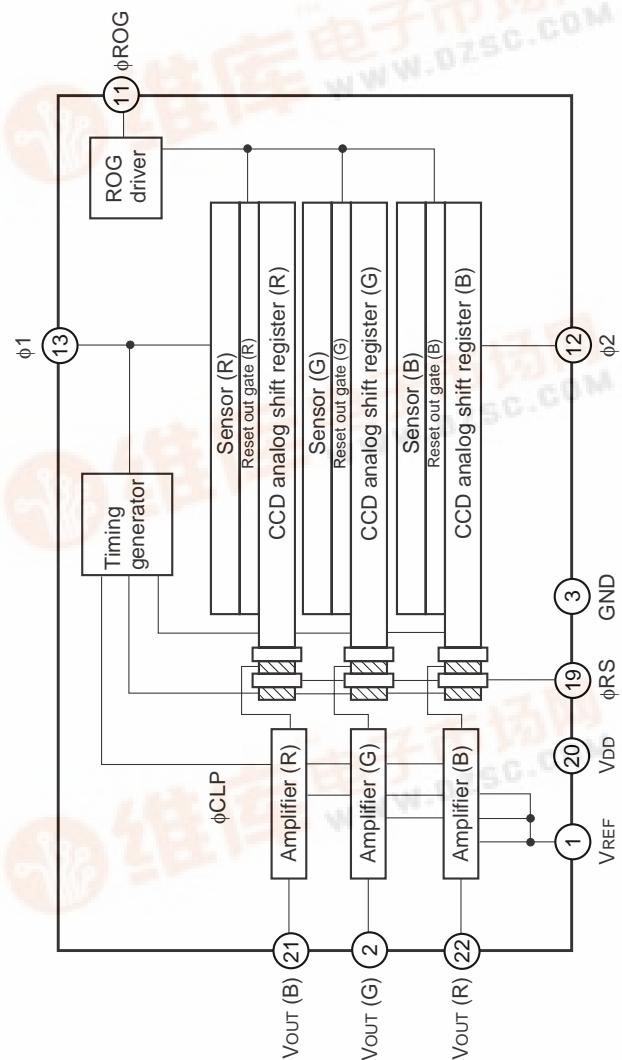
Absolute Maximum Ratings

- Supply voltage V_{DD} 6 V
- Operating temperature -10 to +60 °C
- Storage temperature -30 to +80 °C

Pin Configuration (Top View)



Block Diagram



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Pin Description

Pin No.	Symbol	Description	Pin No.	Symbol	Description
1	V_{REF}	Reference voltage	12	ϕ_2	Clock pulse input
2	V_{OUT} (G)	Signal output (G)	13	ϕ_1	Clock pulse input
3	GND	GND	14	NC	NC
4	NC	NC	15	NC	NC
5	NC	NC	16	NC	NC
6	NC	NC	17	NC	NC
7	NC	NC	18	NC	NC
8	NC	NC	19	ϕ_{RS}	Clock pulse input
9	NC	NC	20	V_{DD}	Power supply
10	NC	NC	21	V_{OUT} (B)	Signal output (B)
11	ϕ_{ROG}	Clock pulse input	22	V_{OUT} (R)	Signal output (R)

Recommended Supply Voltage

Item	Min.	Typ.	Max.	Unit
V_{DD}	4.75	5	5.25	V

Input Pin Capacity

Item	Symbol	Min.	Typ.	Max.	Unit
Input capacity of ϕ_1 , ϕ_2	C_{ϕ_1} , C_{ϕ_2}	—	900	—	pF
Input capacity of ϕ_{RS}	$C_{\phi_{RS}}$	—	60	—	pF
Input capacity of ϕ_{ROG}	$C_{\phi_{ROG}}$	—	10	—	pF

Clock Frequency

Item	Symbol	Min.	Typ.	Max.	Unit
Frequency of ϕ_1 , ϕ_2 , ϕ_{RS}	f_{ϕ_1} , f_{ϕ_2} , $f_{\phi_{RS}}$	—	1	2	MHz

Input Clock Pulse Voltage

Item	Min.	Typ.	Max.	Unit
ϕ_1 , ϕ_2 , ϕ_{RS} , ϕ_{ROG} pulse voltage	High level	4.75	5.0	V
	Low level	—	0	0.1

Electrooptical Characteristics (Note 1)

(Ta = 25°C, V_{DD} = 5V, f_{φRS} = 1MHz, Input clock = 5Vp-p, Light source = 3200K, IR cut filter CM-500S (t = 1.0mm))

Item	Symbol	Min.	Typ.	Max.	Unit	Remarks
Sensitivity	R _R	3.6	5.6	7.6	V/(Ix · s)	Note 2
	R _G	4.4	6.8	9.2		
	R _B	3.1	4.8	6.5		
Sensitivity nonuniformity		PRNU	—	4	20	%
Saturation output voltage		V _{SAT}	1.2	1.5	—	V
Saturation exposure	S _E _R	0.16	0.27	—	Ix · s	Note 5
	S _E _G	0.13	0.22	—		
	S _E _B	0.19	0.31	—		
Dark voltage average		V _{DRK}	—	0.5	3	mV
Dark signal nonuniformity		D _{SNU}	—	3	10	mV
Image lag		I _L	—	1	—	%
Supply current		I _{VDD}	—	9.5	15	mA
Total transfer efficiency		T _{TE}	92	98	—	%
Output impedance		Z _O	—	430	—	Ω
Offset level		V _{OS}	—	3.6	—	V
						Note 8

Notes)

1. In accordance with the given electrooptical characteristics, the black level is defined as the average value of the signal level of the optical black pixels.
2. For the sensitivity test light is applied with a uniform intensity of illumination.
3. PRNU is defined as indicated below. Ray incidence conditions are the same as for Note 2.

$$V_{OUT} (G) = 500 \text{mV (Typ.)}$$

$$PRNU = \frac{(V_{MAX} - V_{MIN})/2}{V_{AVE}} \times 100 [\%]$$

Where the 2700 pixels are divided to blocks of 150, R, G and B pixels, respectively. The maximum output of each block is set to V_{MAX}, the minimum output to V_{MIN} and the average output to V_{AVE}.

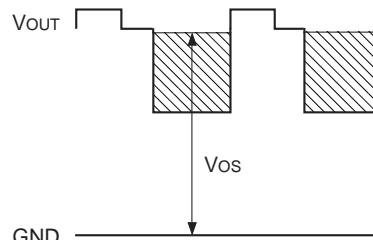
4. Use below the minimum value of the saturation output voltage.
5. Saturation exposure is defined as follows.

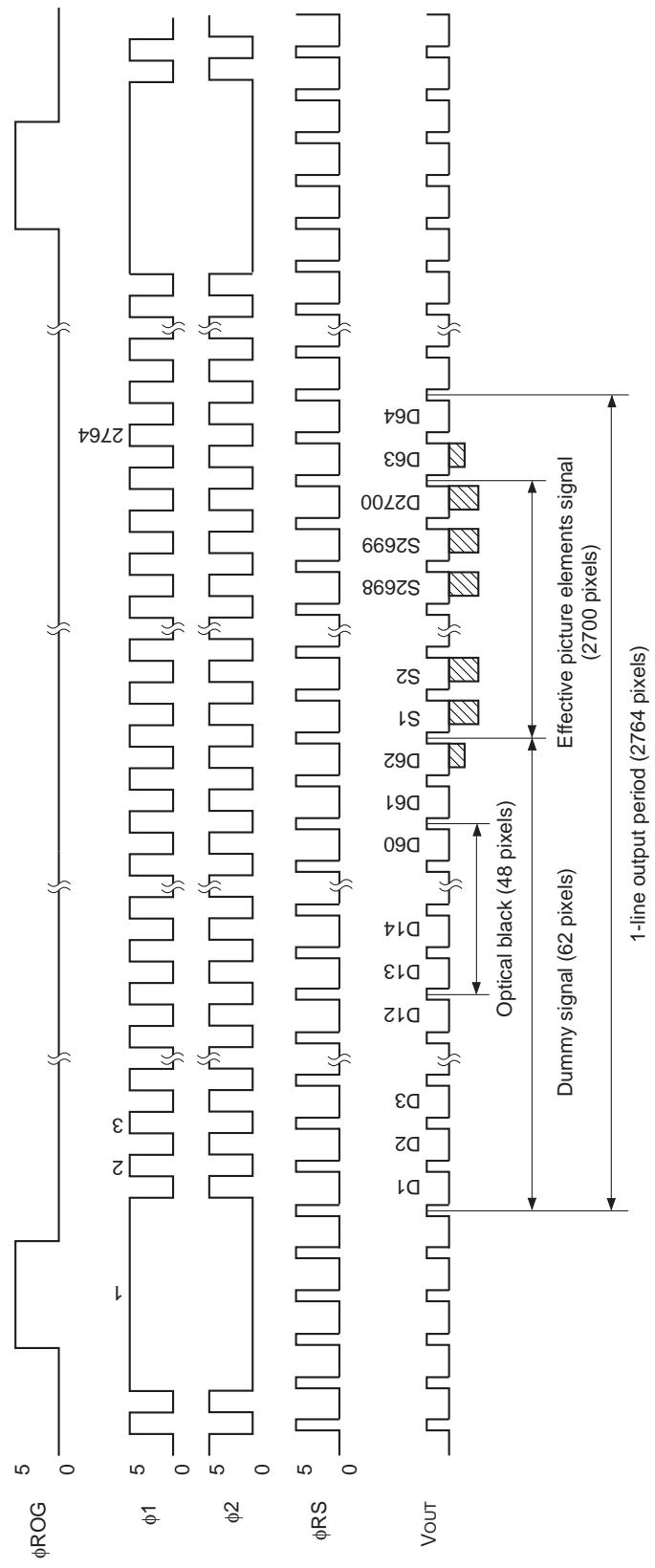
$$SE = \frac{V_{SAT}}{R}$$

Where R indicates R_R, R_G, R_B, and SE indicates S_E_R, S_E_G, S_E_B.

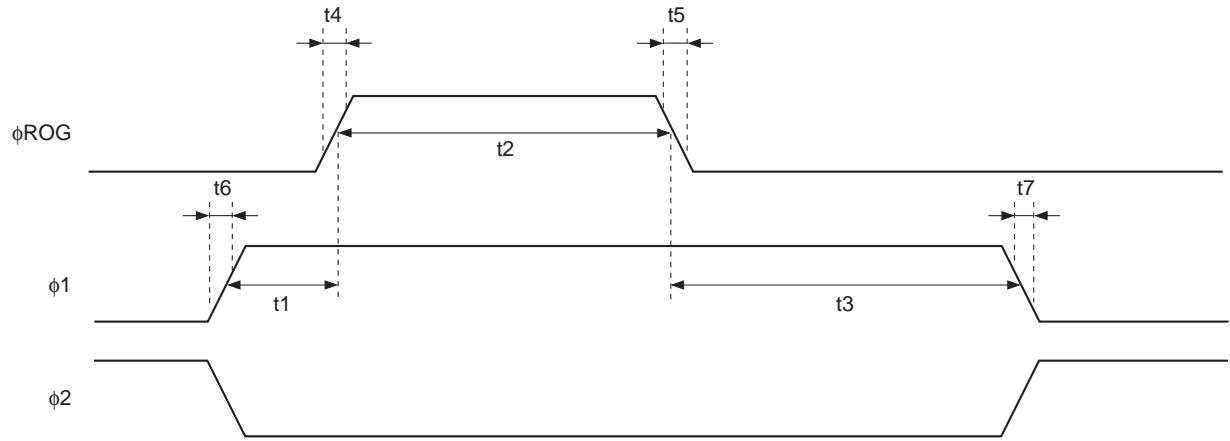
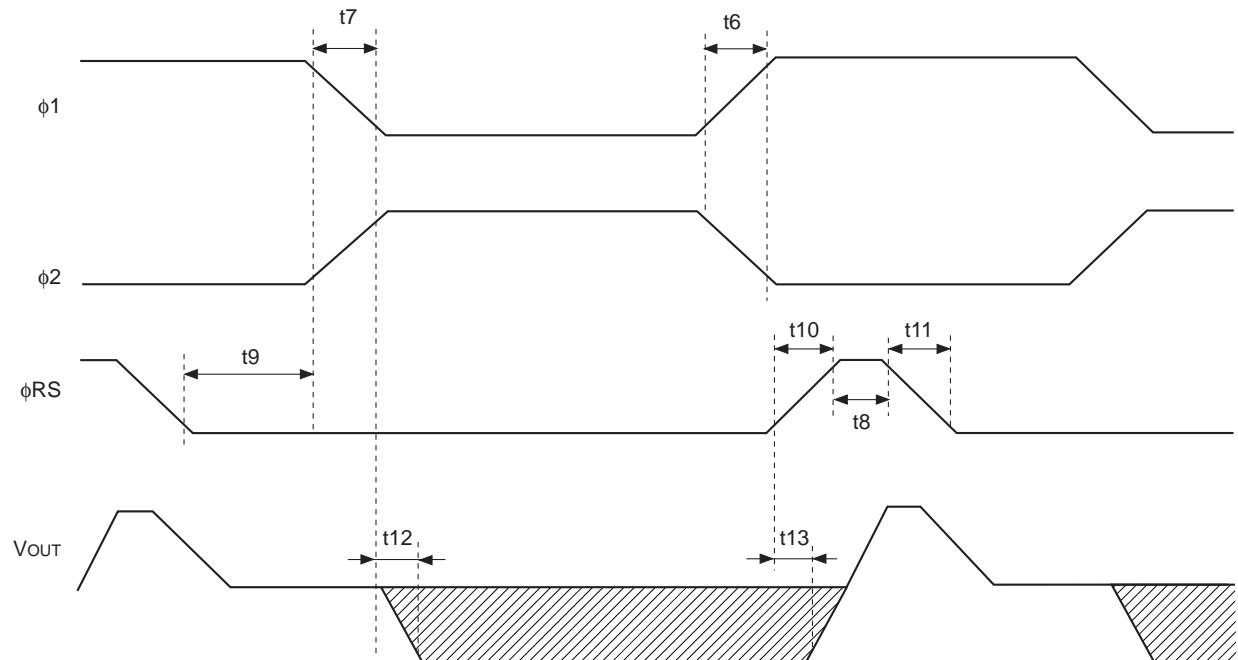
6. Optical signal accumulated time τ int stands at 10ms.
7. V_{OUT} (G) = 500mV (Typ.)
8. V_{OS} is defined as indicated below.

V_{OUT} indicates V_{OUT} (R), V_{OUT} (G) and V_{OUT} (B).





Note) The transfer pulses (ϕ_1, ϕ_2) must have more than 2764 cycles.

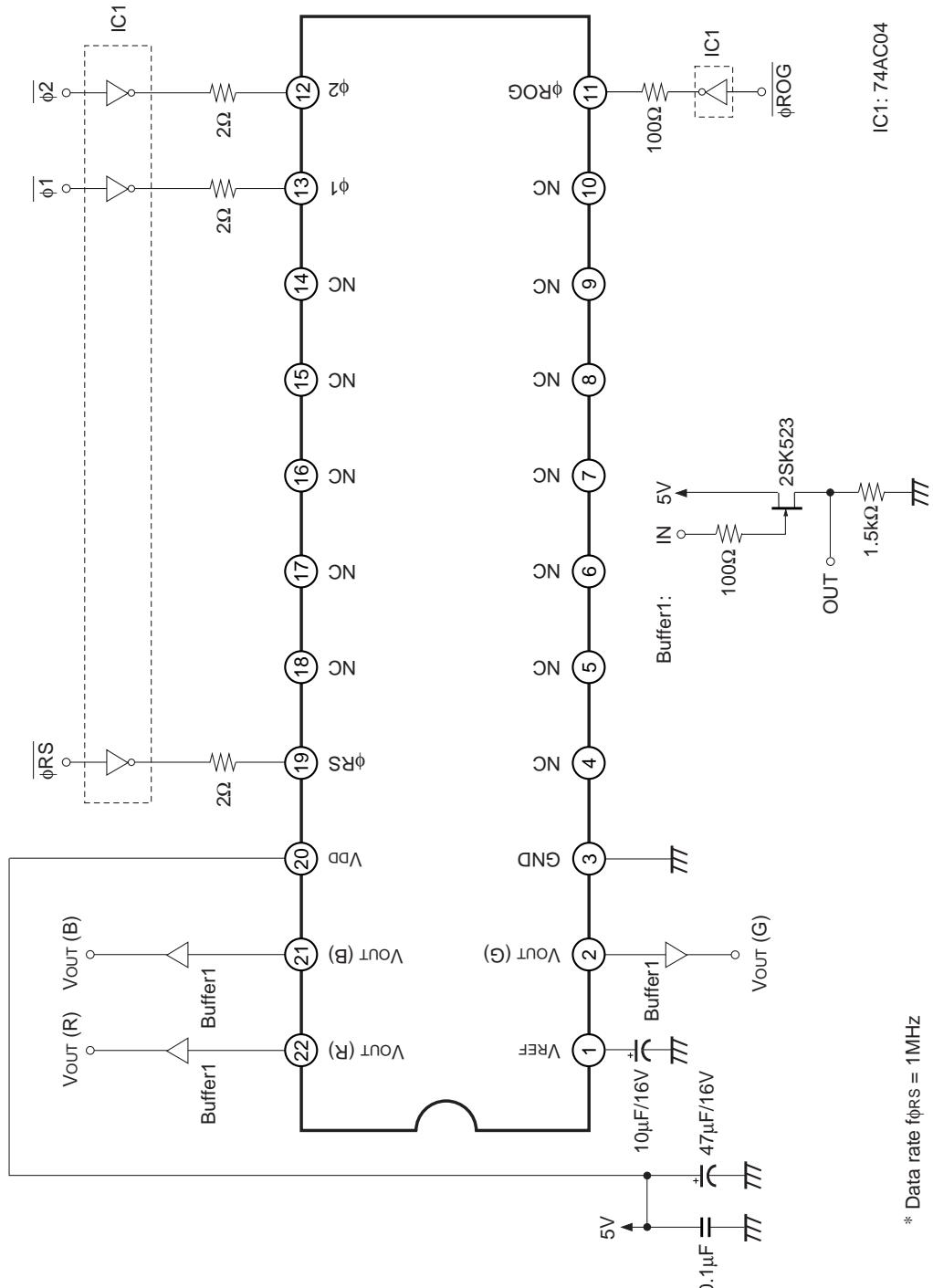
Clock Timing Chart 2**Clock Timing Chart 3**

Clock Pulse Recommended Timing

Item	Symbol	Min.	Typ.	Max.	Unit
φROG, φ1 pulse timing	t1	50	100	—	ns
φROG pulse high level period	t2	800	1000	—	ns
φROG, φ1 pulse timing	t3	800	1000	—	ns
φROG pulse rise time	t4	0	5	10	ns
φROG pulse fall time	t5	0	5	10	ns
φ1 pulse rise time/φ2 pulse fall time	t6	0	10	60	ns
φ1 pulse fall time/φ2 pulse rise time	t7	0	10	60	ns
φRS pulse high level period	t8	50	250*1	—	ns
φRS, φ1 pulse timing	t9	80	250*1	—	ns
φRS pulse rise time	t10	0	10	30	ns
φRS pulse fall time	t11	0	10	30	ns
Signal output delay time	t12	—	130	—	ns
	t13	—	40	—	ns

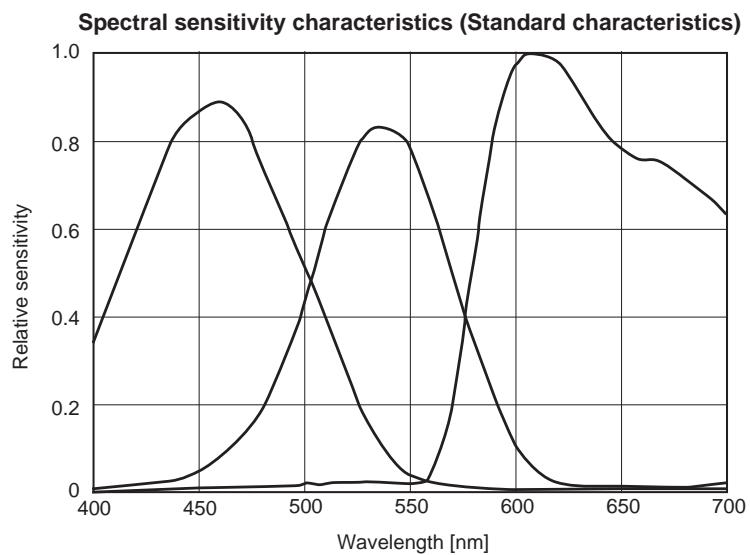
*1 These timing is the recommended condition under $f_{\phi RS} = 1\text{MHz}$.

Application Circuit*

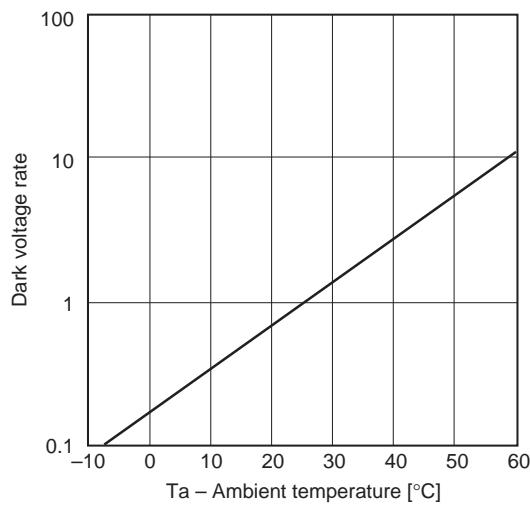


* Data rate $f_{\text{PRS}} = 1\text{MHz}$

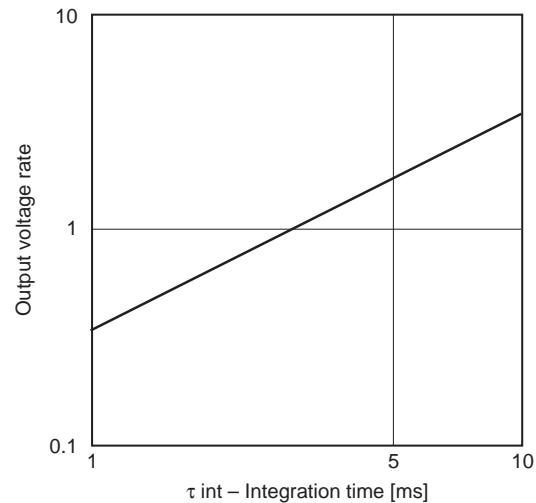
Application circuits shown are typical examples illustrating the operation of the devices. Sony cannot assume responsibility for any problems arising out of the use of these circuits or for any infringement of third party patent and other right due to same.

Example of Representative Characteristics (V_{DD} = 5V, Ta = 25°C)


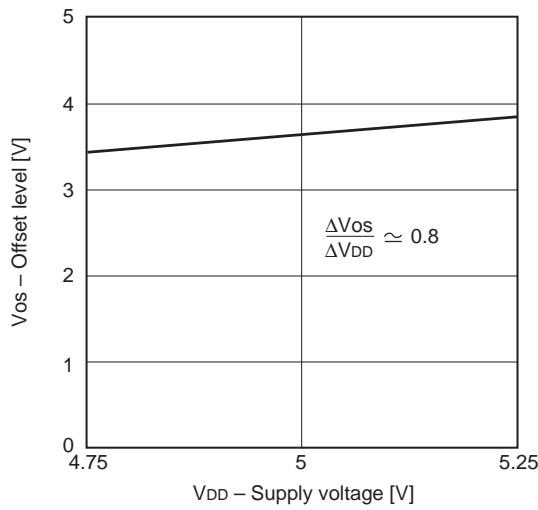
**Dark voltage rate vs. Ambient temperature
(Standard characteristics)**



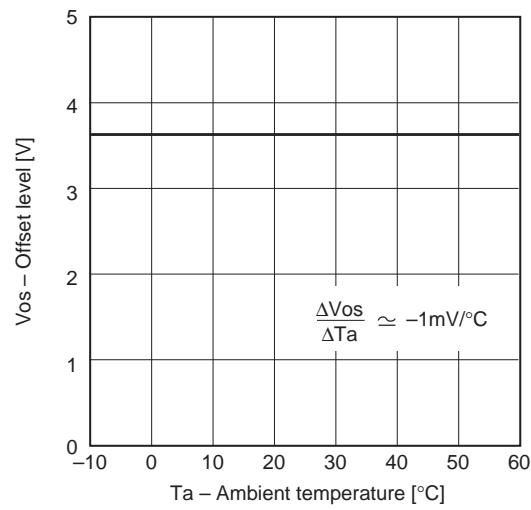
**Output voltage rate vs. Integration time
(Standard characteristics)**



**Offset level vs. Supply voltage
(Standard characteristics)**



**Offset level vs. Ambient temperature
(Standard characteristics)**



Notes of Handling

1) Static charge prevention

CCD image sensors are easily damaged by static discharge. Before handling be sure to take the following protective measures.

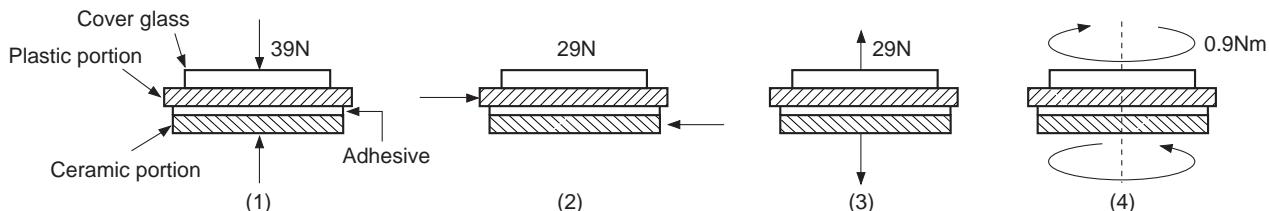
- Either handle bare handed or use non chargeable gloves, clothes or material. Also use conductive shoes.
- When handling directly use an earth band.
- Install a conductive mat on the floor or working table to prevent the generation of static electricity.
- Ionized air is recommended for discharge when handling CCD image sensor.
- For the shipment of mounted substrates, use boxes treated for prevention of static charges.

2) Notes on Handling CCD Packages

The following points should be observed when handling and installing packages.

a) Remain within the following limits when applying static load to the package:

- Compressive strength: 39N/surface (Do not apply load more than 0.7mm inside the outer perimeter of the glass portion.)
- Shearing strength: 29N/surface
- Tensile strength: 29N/surface
- Torsional strength: 0.9Nm



b) In addition, if a load is applied to the entire surface by a hard component, bending stress may be generated and the package may fracture, etc., depending on the flatness of the ceramic portion.

Therefore, for installation, either use an elastic load, such as a spring plate, or an adhesive.

c) Be aware that any of the following can cause the package to crack or dust to be generated.

- Applying repetitive bending stress to the external leads.
- Applying heat to the external leads for an extended period of time with soldering iron.
- Rapid cooling or heating.
- Prying the plastic portion and ceramic portion away at a support point of the adhesive layer.
- Applying the metal a crash or a rub against the plastic portion.

Note that the preceding notes should also be observed when removing a component from a board after it has already been soldered.

3) Soldering

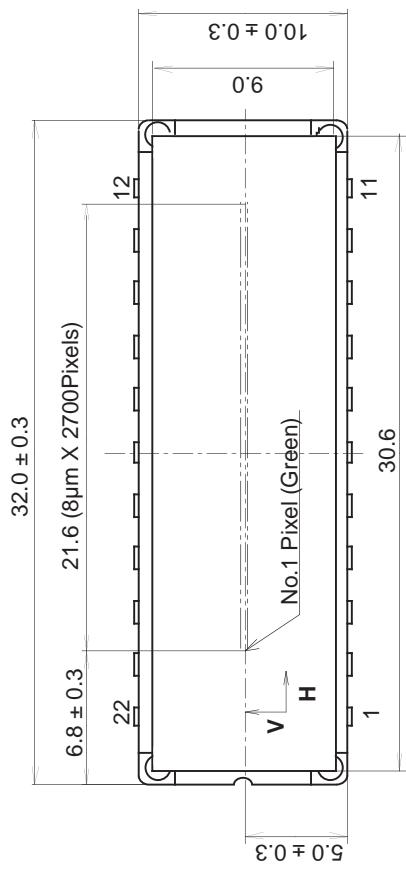
- Make sure the package temperature does not exceed 80°C.
- Solder dipping in a mounting furnace causes damage to the glass and other defects. Use a grounded 30W soldering iron and solder each pin in less than 2 seconds. For repairs and remount, cool sufficiently.
- To dismount an imaging device, do not use a solder suction equipment. When using an electric desoldering tool, ground the controller. For the control system, use a zero cross type.

- 4) Dust and dirt protection
 - a) Operate in clean environments.
 - b) Do not either touch glass plates by hand or have any object come in contact with glass surfaces. Should dirt stick to a glass surface, blow it off with an air blower. (For dirt stuck through static electricity ionized air is recommended.)
 - c) Clean with a cotton bud and ethyl alcohol if the glass surface is grease stained. Be careful not to scratch the glass.
 - d) Keep in a case to protect from dust and dirt. To prevent dew condensation, preheat or precool when moving to a room with great temperature differences.
- 5) Exposure to high temperatures or humidity will affect the characteristics. Accordingly avoid storage or usage in such conditions.
- 6) CCD image sensors are precise optical equipment that should not be subject to mechanical shocks.

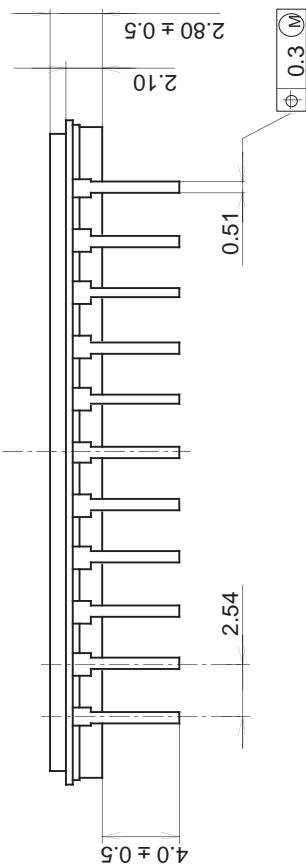
Package Outline

Unit: mm

22pin DIP(400mil)



A technical drawing of a U-shaped bracket. The top horizontal leg has a total width of 10.16. The bottom horizontal leg has a height of 0.25. The angle between the two legs is 0 to 90 degrees.



PACKAGE STRUCTURE

PACKAGE MATERIAL	Plastic , Ceramic
LEAD TREATMENT	GOLD PLATING
LEAD MATERIAL	42 ALLOY
PACKAGE MASS	2.21g
DRAWING NUMBER	LS-D8-01(E)

1. The height from the bottom to the sensor surface is $1.61 \pm 0.3\text{mm}$.
2. The thickness of the cover glass is 0.7mm , and the refractive index is 1.5 .