

IMH24

## Transistors

# Dual digital transistors

IMH24

## ● Features

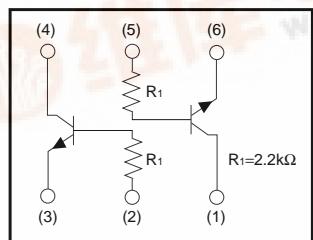
In addition to the features of regular digital transistors.

- 1) Low saturation voltage, typically  $V_{CE(\text{sat})} = 40\text{mV}$  at  $I_C / I_B = 50\text{mA} / 2.5\text{mA}$ , makes these transistors ideal for muting circuits.
- 2) These transistors can be used at high current levels,  $I_C = 600\text{mA}$ .
- 3) Two DTC623T chips in a SMT package.

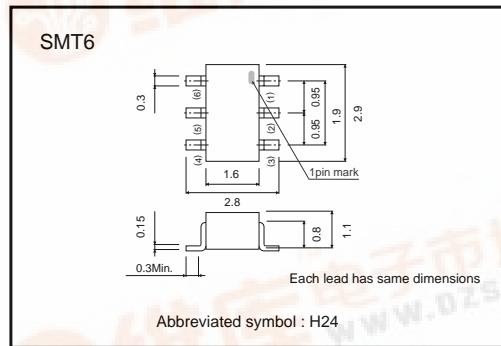
## ● Structure

## NPN digital transistor (Built-in resistor type)

### ● Equivalent circuit



● **External dimensions** (Unit : mm)



- **Absolute maximum ratings (Ta=25°C)**

| Parameter                   | Symbol           | Limits      | Unit |
|-----------------------------|------------------|-------------|------|
| Collector-base voltage      | V <sub>CBO</sub> | 20          | V    |
| Collector-emitter voltage   | V <sub>CEO</sub> | 20          | V    |
| Emitter-base voltage        | V <sub>EBO</sub> | 12          | V    |
| Collector current           | I <sub>C</sub>   | 600         | mA   |
| Collector power dissipation | P <sub>C</sub>   | 300(TOTAL)  | mW * |
| Junction temperature        | T <sub>j</sub>   | 150         | °C   |
| Storage temperature         | T <sub>stg</sub> | -55 to +150 | °C   |

\* 200mW per element must not be exceeded.

## Transistor

## ●Electrical characteristics (Ta=25°C)

| Parameter                            | Symbol               | Min. | Typ. | Max. | Unit       | Conditions                          |
|--------------------------------------|----------------------|------|------|------|------------|-------------------------------------|
| Collector-base breakdown voltage     | $BV_{CBO}$           | 20   | —    | —    | V          | $I_c=50\mu A$                       |
| Collector-emitter breakdown voltage  | $BV_{CEO}$           | 20   | —    | —    | V          | $I_c=1mA$                           |
| Emitter-base breakdown voltage       | $BV_{EBO}$           | 12   | —    | —    | V          | $I_e=50\mu A$                       |
| Collector cutoff current             | $I_{CBO}$            | —    | —    | 0.5  | $\mu A$    | $V_{CB}=20V$                        |
| Emitter cutoff current               | $I_{EBO}$            | —    | —    | 0.5  | $\mu A$    | $V_{EB}=12V$                        |
| Collector-emitter saturation voltage | $V_{CE}(\text{sat})$ | —    | 40   | 150  | mV         | $I_c / I_b=50mA / 2.5mA$            |
| DC current transfer ratio            | $h_{FE}$             | 820  | —    | 2700 | —          | $V_{CE}=5V, I_c=50mA$               |
| Input resistance                     | $R_i$                | 1.54 | 2.2  | 2.86 | k $\Omega$ | —                                   |
| Transition frequency                 | $f_T$                | —    | 150  | —    | MHz        | $V_{CE}=10V, I_e=-50mA, f=100MHz$ * |
| Output "ON" resistance               | $R_{on}$             | —    | 0.4  | —    | $\Omega$   | $V_I=5V, R_L=1k\Omega, f=1KHz$      |

\*Transition frequency of the device.

●Packaging specifications and  $h_{FE}$ 

|       |                              |        |
|-------|------------------------------|--------|
| Type  | Package                      | SMT6   |
|       | Packaging type               | Taping |
|       | Code                         | T110   |
|       | Basic ordering unit (pieces) | 3000   |
| IMH24 |                              | ○      |

## ●Electrical characteristic curves

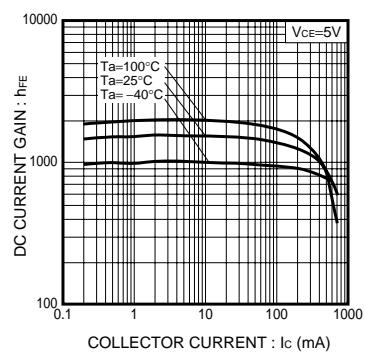


Fig.1 DC Current Gain vs.  
Collector Current

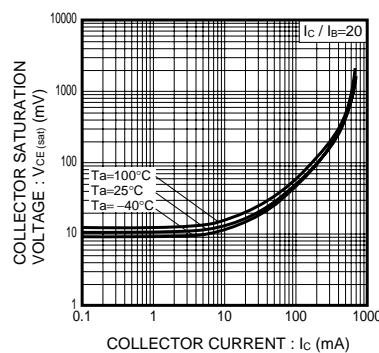


Fig.2 Collector-Emitter Saturation  
Voltage vs. Collector Current

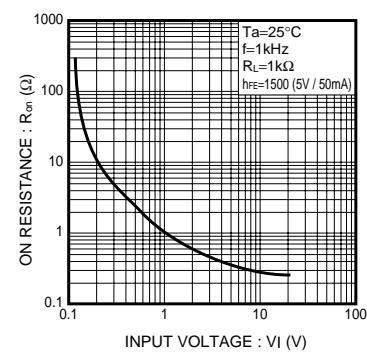


Fig.3 "ON" resistance vs. Input Voltage

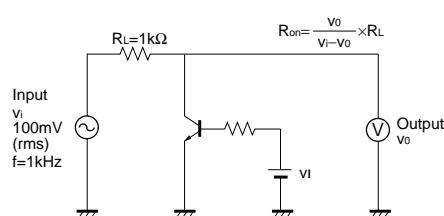
● $R_{on}$  measurement circuit

Fig.4 Output "ON" resistance ( $R_{on}$ )  
measurement circuit

## Appendix

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