General purpose (dual digital transistors) EMH2 / UMH2N / IMH2A

Features

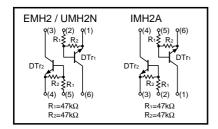
- 1) Two DTC144Es chips in a EMT or UMT or SMT package.
- 2) Mounting possible with EMT3 or UMT3 or SMT3 automatic mounting machines.
- 3) Transistor elements are independent, eliminating
- 4) Mounting cost and area can be cut in half.

●Structure

Epitaxial planar type NPN silicon transistor (Built-in resistor type)

The following characteristics apply to both DTr₁ and DTr₂.

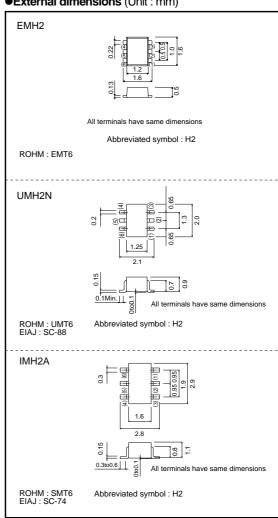
●Equivalent circuit



Packaging specifications

	Package	Taping		
	Code	T2R	TN	T110
Туре	Basic ordering unit (pieces)	8000	3000	3000
EMH2		0	-	-
UMH2N			0	-
IMH2A		_	_	0

●External dimensions (Unit : mm)



● Absolute maximum ratings (Ta = 25°C)

Parameter		Symbol	Limits	Unit	
Supply voltage		Vcc	50	V	
Input voltage		Vin	40	V	
		VIIV	-10		
Output current		lo	30	mA	
		Ic(Max.)	Ic(Max.) 100		
Power dissipation	EMH2,UMH2N	Pd	150 (TOTAL)	mW *1	
	IMH2A	Pu	300 (TOTAL)	*2	
Junction temperature		Tj	150	°C	
Storage temperature		Tstg	−55~+150	°C	

^{*1 120}mW per element must not be exceeded

●Electrical characteristics (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Innut voltage	VI(off)	-	-	0.5	V	Vcc=5V, Io=100μA	
Input voltage	VI(on)	3	-	-	V	Vo=0.3V, Io=2mA	
Output voltage	Vo(on)	-	0.1	0.3	٧	lo/l=10mA/0.5mA	
Input current	lı	-	-	0.18	mA	Vi=5V	
Output current	IO(off)	-	-	0.5	μΑ	Vcc=50V, VI=0V	
DC current gain	Gı	68	-	_	_	Vo=5V, Io=5mA	
Transition frequency	f⊤	-	250	-	MHz	Vc=10V, I=-5mA, f=100MHz *	
Input resistance	R ₁	32.9	47	61.1	kΩ	-	
Resistance ratio	R ₂ /R ₁	0.8	1	1.2	-	-	

^{*} Transition frequency of the device

•Electrical characteristic curves

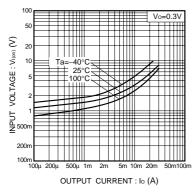


Fig.1 Input voltage vs. output current (ON characteristics)

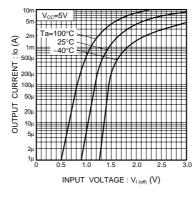


Fig.2 Output current vs. input voltage (OFF characteristics)

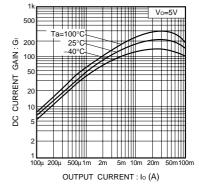


Fig.3 DC current gain vs. output current

^{*2 200}mW per element must not be exceeded.

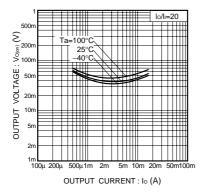


Fig.4 Output voltage vs. output

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