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HEXFET® TRANSISTOR

IRHM9160 P-CHANNEL RAD HARD

-100 Volt, 0.087Ω, RAD HARD HEXFET

International Rectifier's P-channel RAD HARD technology HEXFETs demonstrate excellent threshold voltage stability and breakdown voltage stability at total radiation doses as high as 10⁵ Rads (Si). Under identical pre- and post-radiation test conditions, International Rectifier's P-channel RAD HARD HEXFETs retain identical electrical specifications up to 1 x 105 Rads (Si) total dose. No compensation in gate drive circuitry is required. These devices are also capable of surviving transient ionization pulses as high as 1 x 10¹² Rads (Si)/Sec, and return to normal operation within a few microseconds. Single Event Effect, (SEE), testing of International Rectifier's P-channel RAD HARD HEXFETs has demonstrated virtual immunity to SEE failure. Since the P-channel RAD HARD process utilizes International Rectifier's patented HEXFET technology, the user can expect the highest quality and reliability in the industry.

P-channel RAD HARD HEXFET transistors also feature all of the well-established advantages of MOS-FETs, such as voltage control, very fast switching, ease of paralleling and temperature stability of the electrical parameters.

They are well-suited for applications such as switching power supplies, motor controls, inverters, choppers, audio amplifiers and high-energy pulse circuits in space and weapons environments.

Absolute Maximum Ratings

Product Summary

Part Number	BVDSS	RDS(on)	lD	
IRHM9160	-100V	0.087 Ω	-35*A	

专业PCB打样工厂,24小时加 Provisional Data Sheet No. PD-9.1415

Features:

- Radiation Hardened up to 1 x 10⁵ Rads (Si)
- Single Event Burnout (SEB) Hardened
- Single Event Gate Rupture (SEGR) Hardened
- Gamma Dot (Flash X-Ray) Hardened
- Neutron Tolerant
- Identical Pre- and Post-Electrical Test Conditions
- Repetitive Avalanche Rating
- Dynamic dv/dt Rating
- Simple Drive Requirements
- Ease of Paralleling
- Hermetically Sealed
- Electrically Isolated
- Ceramic Eyelets

Pre-Radiation

	Parameter	IRHM9160	Units
ID @ VGS = -12V, TC = 25°C	Continuous Drain Current	-35*	
$I_D @ V_{GS} = -12V, T_C = 100^{\circ}C$	Continuous Drain Current	-22	A
IDM	Pulsed Drain Current 1	-140	1
P _D @ T _C = 25°C	Max. Power Dissipation	250	W
Con T Del	Linear Derating Factor	2.0	W/K 5
VGS	Gate-to-Source Voltage	±20	V
EAS	Single Pulse Avalanche Energy 2	500	mJ
IAR	Avalanche Current ①	-35	A
EAR	Repetitive Avalanche Energy ①	25	mJ
dv/dt	Peak Diode Recovery dv/dt 3	-5.5	V/ns
。找 PDF	Operating Junction	-55 to 150	
HE TSTO	Storage Temperature Range		
odf dzsc com	Lead Temperature	300 (0.063 in. (1.6mm) from	°C
Spanazse.com		case for 10 sec.)	

IRHM9160 Device

Pre-Radiation

	Parameter	Min.	Тур.	Max.	Units	Test Conditions	
BVDSS	Drain-to-Source Breakdown Voltage	-100	—	—	V	VGS = 0V, ID = -1.0 mA	
ΔBV _{DSS} /ΔTJ	Temperature Coefficient of Breakdown Voltage	_	-0.13	—	V/°C	Reference to 25°C, ID = -1.0 mA	
RDS(on)	Static Drain-to-Source	—	_	0.087		VGS = 12V, ID = -22A	
	On-State Resistance	—	—	0.10	Ω	VGS = 12V, ID = -35A (4)	
VGS(th)	Gate Threshold Voltage	-2.0	_	-4.0	V	VDS = VGS, ID = -1.0 mA	
9fs	Forward Transconductance	10	_	—	S (ひ)	VDS > 15V, IDS = -22A ④	
IDSS	Zero Gate Voltage Drain Current	—	—	-25	A	VDS = 0.8 x Max Rating,VGS = 0V	
			—	-250	μA	VDS = 0.8 x Max Rating	
						VGS = 0V, TJ = 125°C	
IGSS	Gate-to-Source Leakage Forward	—	_	-100	nA	VGS = -20V	
IGSS	Gate-to-Source Leakage Reverse	—	—	100		VGS = +20V	
Qg	Total Gate Charge	—	—	200		VGS =12V, ID = -35A	
Qgs	Gate-to-Source Charge	—	—	50	nC	VDS = Max. Rating x 0.5	
Qgd	Gate-to-Drain ("Miller") Charge	—	—	90			
td(on)	Turn-On Delay Time	—	_	70		VDD = -50V, ID = -35A,	
tr	Rise Time	—	—	240	ns	RG = 2.35Ω	
td(off)	Turn-Off Delay Time	—	_	220	115		
tf	Fall Time	—	—	150			
LD	Internal Drain Inductance	—	8.7	—	nH	Measured from the drain lead, 6mm (0.25 in.) from package to center of die.	
LS	Internal Source Inductance	—	8.7	—		Measured from the source lead, 6mm (0.25 in.) from package to source bonding pad.	
C _{iss}	Input Capacitance		7000			$V_{GS} = 0V, V_{DS} = -25V$	
C _{OSS}	Output Capacitance	_	2000	—	pF	f = 1.0 MHz	
C _{rss}	Reverse Transfer Capacitance		500				

Electrical Characteristics @ Tj = 25°C (Unless Otherwise Specified)

Source-Drain Diode Ratings and Characteristics

	Parameter	Min.	Тур.	Max.	Units	Test Conditions		
١s	Continuous Source Current (Body Diode)	—	—	-35	Α	Modified MOSFET symbol showing the		
ISM	Pulse Source Current (Body Diode) ①		—	-140		integral reverse p-n junction rectifier.		
VSD	Diode Forward Voltage	—		-3.3	V	Tj = 25°C, IS = -35A, VGS = 0V ④		
trr	Reverse Recovery Time	—	—	775	ns	Tj = 25°C, IF = -35A, di/dt ≤ -100A/μs		
QRR	Reverse Recovery Charge	—		5.0	μC	V _{DD} ≤ -50V ④		
ton	Forward Turn-On Time Intrinsic turn-on time is negligible. Turn-on speed is substantially controlled by LS + LD.							

Thermal Resistance

	Parameter	Min.	Тур.	Max.	Units	Test Conditions
RthJC	Junction-to-Case			0.50	K/W5	
R _{th} JA	Junction-to-Ambient	_		48	N/10	

IRHM9160 Device

Radiation Performance of P-Channel Rad Hard HEXFETs

International Rectifier Radiation Hardened HEX-FETs are tested to verify their hardness capability. The hardness assurance program at International Rectifier uses two radiation environments.

Every manufacturing lot is tested in a low dose rate (total dose) environment per MIL-STD-750, test method 1019. International Rectifier has imposed a standard gate voltage of -12 volts per note 6 and a V_{DSS} bias condition equal to 80% of the device rated voltage per note 7. Pre- and post-radiation limits of the devices irradiated to 1 x 10⁵ Rads (Si) are identical and are presented in Table 1. The values in Table 1 will be met for either of the two low dose rate test circuits that are used.

Both pre- and post-radiation performance are tested and specified using the same drive circuitry and test conditions in order to provide a direct comparison. It should be noted that at a radiation level of 1×10^5 Rads (Si), no change in limits are specified in DC parameters.

High dose rate testing may be done on a special request basis, using a dose rate up to 1×10^{12} Rads (Si)/Sec.

International Rectifier radiation hardened P-Channel HEXFETs are considered to be neutron-tolerant, as stated in MIL-PRF-19500 Group D. International Rectifier P-Channel radiation hardened HEXFETs have been characterized in heavy ion Single Event Effects (SEE) environment and results are shown in Table 3.

Table 1.	Low Dose	Rate 6	\bigcirc
	D	4	

	Parameter		Rads (Si)	Units	Test Conditions 10		
		min.	max.				
BV _{DSS}	Drain-to-Source Breakdown Voltage	-100		V	$V_{GS} = 0V, I_D = -1.0 \text{ mA}$		
V _{GS(th)}	Gate Threshold Voltage ④	-2.0	-4.0		$V_{GS} = V_{DS}, I_{D} = -1.0 \text{ mA}$		
I _{GSS}	Gate-to-Source Leakage Forward	_	-100	nA	V _{GS} = -20V		
IGSS	Gate-to-Source Leakage Reverse	-	100		$V_{GS} = 20V$		
IDSS	Zero Gate Voltage Drain Current	-	-25	μA	$V_{DS} = 0.8 \text{ x} \text{ Max} \text{ Rating}, V_{GS} = 0 \text{ V}$		
R _{DS(on)1}	Static Drain-to-Source ④	—	0.087	Ω	VGS = -12V, I _D = -22A		
	On-State Resistance One						
V _{SD}	Diode Forward Voltage ④	—	-3.3	V	$T_{C} = 25^{\circ}C$, $I_{S} = -35A$, $V_{GS} = 0V$		

IRHM9160

Table 2. High Dose Rate ®

	10 ¹¹ Rads (Si)/sec 10 ¹² Rads (Si)/sec							
Parameter	Min.	Тур	Max.	Min.	Min. Typ. Max.		Units	Test Conditions
VDSS Drain-to-Source Voltage	—	—	-80	—	—	-80	V	Applied drain-to-source voltage
								during gamma-dot
IPP	—	-100	—	_	-100	—	A	Peak radiation induced photo-current
di/dt	—	-800	_	_	-160	—	A/µsec	Rate of rise of photo-current
L ₁	0.1	_	—	0.5	—	_	μH	Circuit inductance required to limit di/dt

Table 3. Single Event Effects (9)

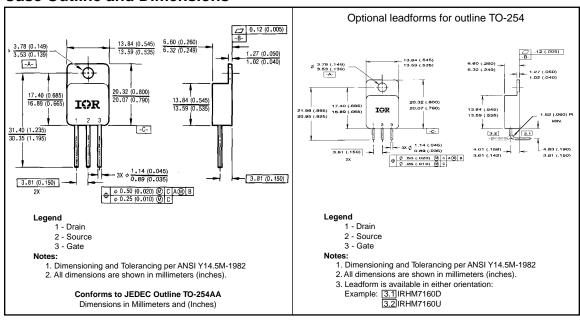
Demonstern	т	L la lta	lan	LET (Si)	Fluence	Range	V _{DS} Bias	V _{GS} Bias
Parameter	Тур.	Units	Ion	(MeV/mg/cm ²)	(ions/cm ²)	(μm)	(V)	(V)
BVDSS	-100	V	Ni	28	1 x 10⁵	~41	-100	5

IRHM9160 Device

- Repetitive Rating; Pulse width limited by maximum junction temperature. Refer to current HEXFET reliability report.
- ② @ V_{DD} = -25V, Starting T_J = 25°C, E_{AS} = [0.5 * L * (I_L²) * [BV_{DSS}/(BV_{DSS}-V_{DD})] Peak I_I = -35A, V_{GS} = -12V, 25 ≤ R_G ≤ 200Ω
- $\label{eq:ISD} \begin{array}{l} \$lines{1}{SD} \leq -35\text{A}, \mbox{ di/dt} \leq 170 \mbox{ A/}\mu\text{s}, \\ \mbox{VDD} \leq B\text{V}_{DSS}, \mbox{ TJ} \leq 150^{\circ}\text{C} \\ \mbox{Suggested RG} = 2.35\Omega \end{array}$
- ④ Pulse width \leq 300 µs; Duty Cycle \leq 2%
- ⑤ K/W = °C/W
- W/K = W/°C

Radiation Characteristics

- ⑥ Total Dose Irradiation with VGS Bias. -12 volt VGS applied and VDS = 0 during irradiation per MIL-STD-750, method 1019.
- O Total Dose Irradiation with V_{DS} Bias. V_{DS} = 0.8 rated BV_{DSS} (pre-radiation) applied and V_{GS} = 0 during irradiation per MIL-STD-750, method 1019.
- ⑧ This test is performed using a flash x-ray source operated in the e-beam mode (energy ~2.5 MeV), 30 nsec pulse.
- 9 Process characterized by independent laboratory.
- Ill Pre-Radiation and Post-Radiation test conditions are identical to facilitate direct comparison for circuit applications.



CAUTION BERYLLIA WARNING PER MIL-PRF-19500

Packages containing berylia shall not be ground, sandblasted, machined, or have other operations performed on them which will produce beryllia or beryllium dust. Furthermore, beryllium oxides packages shall not be placed in acids that will produce fumes containing beryllium.

International

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Case Outline and Dimensions