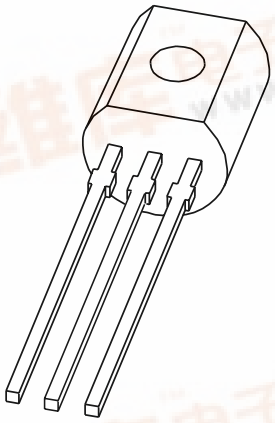
查询JC327供应商 捷多邦，专业PCB打样工厂，24小时加急出货

DISCRETE SEMICONDUCTORS

# DATA SHEET



# JC327
## PNP general purpose transistor

Product specification
Supersedes data of 1997 Mar 10

1999 Apr 27

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Philips Semiconductors

PHILIPS

# PNP general purpose transistor JC327

## FEATURES

- High current (max. 500 mA)
- Low voltage (max. 45 V).

## APPLICATIONS

- General purpose switching and amplification, e.g. driver and output stages of audio amplifiers.

## DESCRIPTION

PNP transistor in a TO-92; SOT54 plastic package.
NPN complement: JC337.

## PINNING

| PIN | DESCRIPTION |
|---|---|
| 1 | base |
| 2 | collector |
| 3 | emitter |

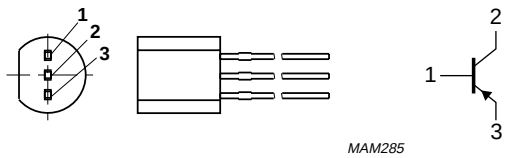



Fig.1 Simplified outline (TO-92; SOT54) and symbol.

## LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

| SYMBOL | PARAMETER | CONDITIONS | MIN. | MAX. | UNIT |
|---|---|---|---|---|---|
| $V_{CBO}$ | collector-base voltage | open emitter | – | –50 | V |
| $V_{CEO}$ | collector-emitter voltage | open base; $I_C$ = –10 mA | – | –45 | V |
| $V_{EBO}$ | emitter-base voltage | open collector | – | –5 | V |
| $I_C$ | collector current (DC) | | – | –500 | mA |
| $I_{CM}$ | peak collector current | | – | –1 | A |
| $I_{BM}$ | peak base current | | – | –200 | mA |
| $P_{tot}$ | total power dissipation | $T_{amb} \leq 25$ °C; note 1 | – | 625 | mW |
| $T_{stg}$ | storage temperature | | –65 | +150 | °C |
| $T_j$ | junction temperature | | – | 150 | °C |
| $T_{amb}$ | operating ambient temperature | | –65 | +150 | °C |

**Note**

1. Transistor mounted on an FR4 printed-circuit-board.

## THERMAL CHARACTERISTICS

| SYMBOL | PARAMETER | CONDITIONS | VALUE | UNIT |
|---|---|---|---|---|
| $R_{th\ j\text{-}a}$ | thermal resistance from junction to ambient | note 1 | 0.2 | K/mW |

**Note**

1. Transistor mounted on an FR4 printed-circuit-board.

## CHARACTERISTICS

$T_j$ = 25 °C unless otherwise specified.

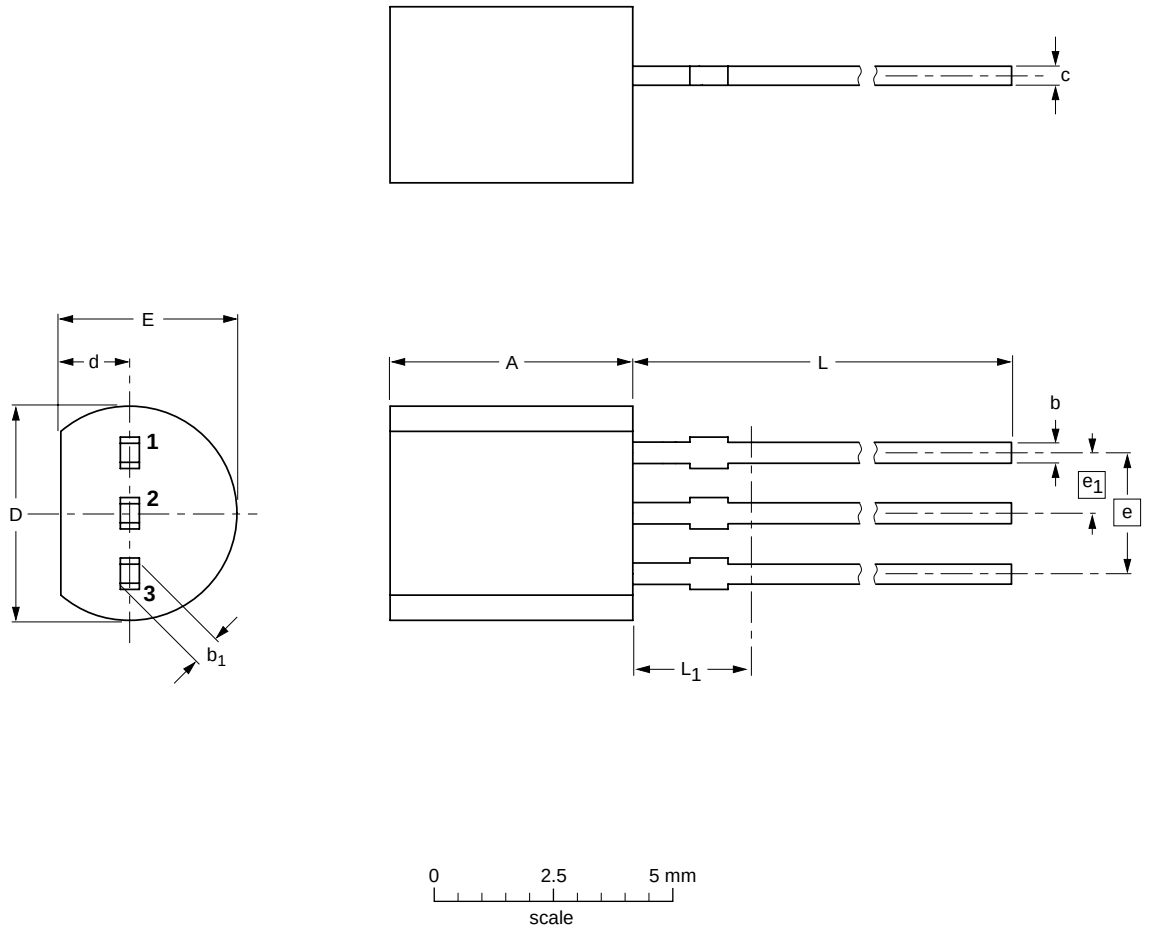
| SYMBOL | PARAMETER | CONDITIONS | MIN. | TYP. | MAX. | UNIT |
|---|---|---|---|---|---|---|
| $I_{CBO}$ | collector cut-off current | $I_E$ = 0; $V_{CB}$ = –20 V | – | – | –100 | nA |
| | | $I_E$ = 0; $V_{CB}$ = –20 V; $T_j$ = 150 °C | – | – | –5 | µA |
| $I_{EBO}$ | emitter cut-off current | $I_C$ = 0; $V_{EB}$ = –5 V | – | – | –100 | nA |
| $h_{FE}$ | DC current gain<br>JC327-25 | $I_C$ = –100 mA; $V_{CE}$ = –1 V | <br>160 | <br>– | <br>400 | |
| $h_{FE}$ | DC current gain | $I_C$ = –500 mA; $V_{CE}$ = –1 V | 40 | – | – | |
| $V_{CEsat}$ | collector-emitter saturation voltage | $I_C$ = –500 mA; $I_B$ = –50 mA | – | – | –700 | mV |
| $V_{BE}$ | base-emitter voltage | $I_C$ = –500 mA; $V_{CE}$ = –1 V; note 1 | – | – | –1.2 | V |
| $C_c$ | collector capacitance | $I_E$ = $i_e$ = 0; $V_{CB}$ = –10 V; f = 1 MHz | – | 8 | – | pF |
| $f_T$ | transition frequency | $I_C$ = –10 mA; $V_{CE}$ = –5 V;<br>f = 100 MHz | 80 | – | – | MHz |

**Note**

1. $V_{BE}$ decreases by about –2 mV/K with increasing temperature.

## PACKAGE OUTLINE

**Plastic single-ended leaded (through hole) package; 3 leads** **SOT54**




**DIMENSIONS (mm are the original dimensions)**

| UNIT | A | b | $b_1$ | c | D | d | E | e | $e_1$ | L | $L_1$(1) |
|---|---|---|---|---|---|---|---|---|---|---|---|
| mm | 5.2<br>5.0 | 0.48<br>0.40 | 0.66<br>0.56 | 0.45<br>0.40 | 4.8<br>4.4 | 1.7<br>1.4 | 4.2<br>3.6 | 2.54 | 1.27 | 14.5<br>12.7 | 2.5 |

**Note**

1. Terminal dimensions within this zone are uncontrolled to allow for flow of plastic and terminal irregularities.

| OUTLINE VERSION | REFERENCES | | | | EUROPEAN PROJECTION | ISSUE DATE |
|---|---|---|---|---|---|---|
| | IEC | JEDEC | EIAJ | | | |
| SOT54 | | TO-92 | SC-43 | | | 97-02-28 |

## DEFINITIONS

| | |
|---|---|
| **Data Sheet Status** | |
| Objective specification | This data sheet contains target or goal specifications for product development. |
| Preliminary specification | This data sheet contains preliminary data; supplementary data may be published later. |
| Product specification | This data sheet contains final product specifications. |
| **Limiting values** | |
| Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability. | |
| **Application information** | |
| Where application information is given, it is advisory and does not form part of the specification. | |

## LIFE SUPPORT APPLICATIONS

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**NOTES**

**NOTES**

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