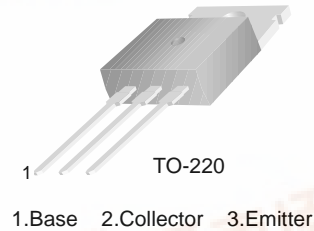




## MJE2955T

### General Purpose and Switching Applications

- DC Current Gain Specified to  $I_C = 10\text{ A}$
- High Current Gain Bandwidth Product :  $f_T = 2\text{MHz}$  (Min.)



### PNP Silicon Transistor

#### Absolute Maximum Ratings $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Value	Units
$V_{CBO}$	Collector-Base Voltage	- 70	V
$V_{CEO}$	Collector-Emitter Voltage	- 60	V
$V_{EBO}$	Emitter-Base Voltage	- 5	V
$I_C$	Collector Current	- 10	A
$I_B$	Base Current	- 6	A
$P_C$	Collector Dissipation ( $T_C = 25^\circ\text{C}$ )	75	W
$P_C$	Collector Dissipation ( $T_a = 25^\circ\text{C}$ )	0.6	W
$T_J$	Junction Temperature	150	$^\circ\text{C}$
$T_{STG}$	Storage Temperature	- 55 ~ 150	$^\circ\text{C}$

#### Electrical Characteristics $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Max.	Units
$BV_{CEO}$	Collector- Emitter Breakdown Voltage	$I_C = - 200\text{mA}$ , $I_B = 0$	-60		V
$I_{CEO}$	Collector Cut-off Current	$V_{CE} = - 30\text{V}$ , $I_B = 0$		-700	$\mu\text{A}$
$I_{CEX1}$	Collector Cut-off Current	$V_{CE} = - 70\text{V}$ , $V_{BE}(\text{off}) = 1.5\text{V}$		-1	mA
$I_{CEX2}$	Collector Cut-off Current	$V_{CE} = - 70\text{V}$ , $V_{BE}(\text{off}) = 1.5\text{V}$ @ $T_C = 150^\circ\text{C}$		-5	mA
$I_{EBO}$	Emitter Cut-off Current	$V_{EB} = - 5\text{V}$ , $I_C = 0$		-5	mA
$h_{FE}$	* DC Current Gain	$V_{CE} = - 4\text{V}$ , $I_C = - 4\text{A}$ $V_{CE} = - 4\text{V}$ , $I_C = - 10\text{A}$	20 5	100	
$V_{CE}(\text{sat})$	* Collector-Emitter Saturation Voltage	$I_C = - 4\text{A}$ , $I_B = - 0.4\text{A}$ $I_C = - 10\text{A}$ , $I_B = - 3.3\text{A}$		-1.1 -8	V V
$V_{BE}(\text{on})$	* Base-Emitter ON Voltage	$V_{CE} = - 4\text{V}$ , $I_C = - 4\text{A}$		-1.8	V
$f_T$	Current Gain Bandwidth Product	$V_{CE} = - 10\text{V}$ , $I_C = - 500\text{mA}$	2		MHz

\* Pulse test:  $PW \leq 300\mu\text{s}$ , duty cycle  $\leq 2\%$  Pulse

# Typical Characteristic

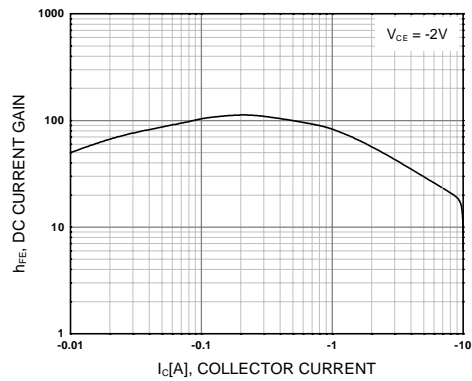


Figure 1. DC current Gain

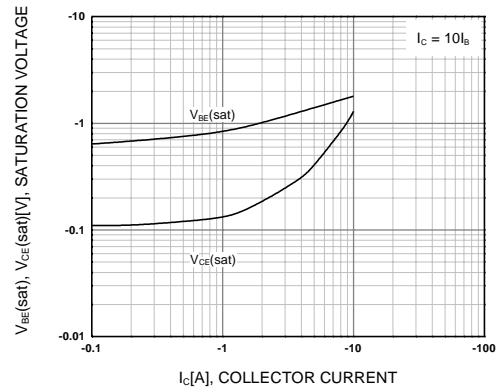


Figure 2. Base-Emitter Saturation Voltage  
Collector-Emitter Saturation Voltage

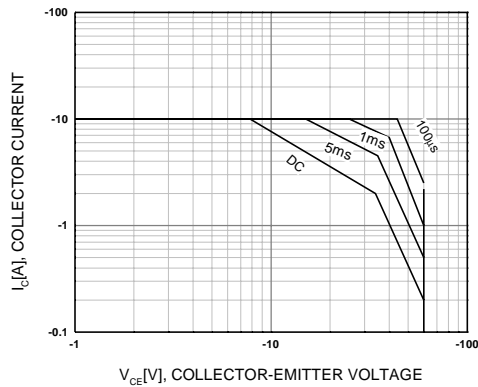


Figure 3. Safe Operating Area

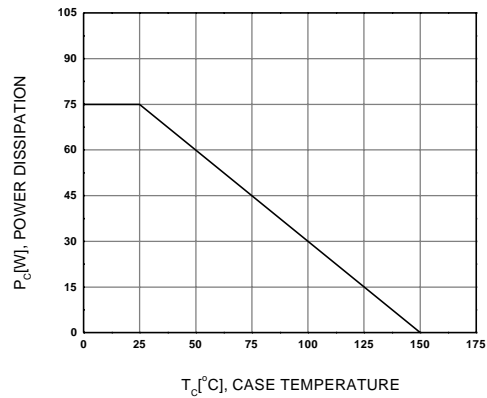
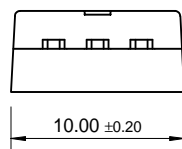
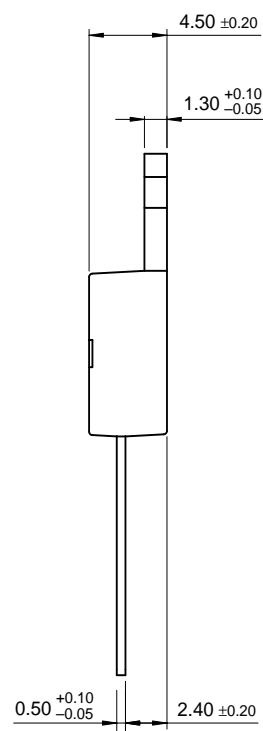


Figure 4. Power Derating

Technical drawing of a 1/2-18 UNF-2A x 1.50 inch Hex Flange Nut. The drawing shows the front and side views with the following dimensions:

- Overall width:  $9.90 \pm 0.20$
- Width of the hexagonal flange:  $(8.70)$
- Flange thickness:  $1.30 \pm 0.10$
- Flange hole diameter:  $\varnothing 3.60 \pm 0.10$
- Flange hole position from the side face:  $2.80 \pm 0.10$
- Hexagonal flange height:  $15.90 \pm 0.20$
- Hexagonal flange height (maximum):  $18.95 \text{ MAX.}$
- Hexagonal flange height (typical):  $(3.70)$
- Hexagonal flange height (typical):  $(3.00)$
- Hexagonal flange height (typical):  $(1.46)$
- Hexagonal flange height (typical):  $(1.00)$
- Hexagonal flange height (typical):  $(1.27 \pm 0.10)$
- Hexagonal flange height (typical):  $(1.52 \pm 0.10)$
- Hexagonal flange height (typical):  $(0.80 \pm 0.10)$
- Hexagonal flange height (typical):  $(45^\circ)$
- Hexagonal flange height (typical):  $13.08 \pm 0.20$
- Hexagonal flange height (typical):  $10.08 \pm 0.30$
- Hexagonal flange height (typical):  $2.54 \text{ TYP}$
- Hexagonal flange height (typical):  $[2.54 \pm 0.20]$
- Hexagonal flange height (typical):  $2.54 \text{ TYP}$
- Hexagonal flange height (typical):  $[2.54 \pm 0.20]$



Rev. A1, February 2001

## TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

ACE <sup>x</sup> ™	FAST <sup>r</sup> ™	PowerTrench <sup>®</sup>	SyncFET™
Bottomless™	GlobalOptoisolator™	QFET™	TinyLogic™
CoolFET™	GTO™	QS™	VCX™
CROSSVOLT™	HiSeC™	QT Optoelectronics™	UHC™
DOME™	ISOPLANAR™	Quiet Series™	
E <sup>2</sup> CMOS™	MICROWIRE™	LILENT SWITCHER <sup>®</sup>	
EnSigna™	OPTOLOGIC™	SMART START™	
FACT™	OPTOPLANAR™	SuperSOT™-3	
FACT Quiet Series™	PACMAN™	SuperSOT™-6	
FAST <sup>®</sup>	POP™	SuperSOT™-8	

## DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

## LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR INTERNATIONAL.

As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

## PRODUCT STATUS DEFINITIONS

### Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.