# Advance Information

# **SWITCHMODE™** Series

## **NPN Bipolar Power Transistor**

The MJE8503A transistor is designed for high voltage, high speed, power switching in inductive circuits where fall time is critical. They are suited for line operated WWW.DZSC.COM switchmode applications such as:

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- Switching Regulators
- Inverters
- Solenoid and Relay Drivers
- Motor Controls
- Deflection Circuits

#### **Featuring**

- 1500 Volt Collector-Base Breakdown Capability
- · Fast Switching:
  - 180 ns Typical Fall Times
  - 450 ns Typical Crossover Times
  - 1.2 µs Typical Storage Times
- Low Collector-Emitter Leakage Current 100 μA Max @ 1500 VCES

# **MJE8503A\***

\*Motorola Preferred Device

**POWER TRANSISTORS 5.0 AMPERES** 1500 VOLTS — BVCES 80 WATTS



### MAXIMUM RATINGS (T<sub>C</sub> = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	V <sub>CEO(sus)</sub>	700	Vdc
Collector-Emitter Voltage	V <sub>CES</sub>	1500	Vdc
Collector-Base Voltage	VCBO	1500	Vdc
Emitter-Base Voltage	V <sub>EBO</sub>	5.0	Vdc
Collector Current — Continuous — Peak (1)	IC	5.0 10	Adc
Collector Current — Continuous — Peak	I <sub>B</sub>	4.0 4.0	Adc
Total Power Dissipation @ T <sub>C</sub> = 25°C @ T <sub>C</sub> = 100°C Derate above 25°C	PD	80 21 0.8	Watts W/°C
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-65 to +125	°C

## THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	$R_{ heta JC}$	1.25	°C/W
Maximum Lead Temperature for Soldering Purposes 1/8" from Case for 5 sec.	TL	275	°C

<sup>(1)</sup> Pulse Test: Pulse Width = 5.0 ms, Duty Cycle < 10%.

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is document contains information on a new product. Specifications and information herein are subject to change without notice.

ferred devices are Motorola recommended choices for future use and best overall value.

## **MJE8503A**

## **ELECTRICAL CHARACTERISTICS** ( $T_C = 25^{\circ}C$ unless otherwise noted)

	Symbol	Min	Тур	Max	Unit	
OFF CHARACTERISTICS						
Collector-Emitter Sustaining Voltage (IC = 100 mAdc, IB = 0)		VCEO(sus)	700	_	_	Vdc
Collector Cutoff Current (VCE = 1500 Vdc, VBI (VCE = 1500 Vdc, VBI	ICES		_ _	0.1 2.0	mAdc	
Collector Cutoff Current (VCE = 1500 Vdc, R <sub>BI</sub>	= = 50 Ohms, T <sub>C</sub> = 100°C)	ICER	_	_	5.0	mAdc
Emitter Cutoff Current (VEB = 6.0 Vdc, IC = 0	))	<sup>I</sup> EBO	_	_	1.0	mAdc
SECOND BREAKDOWN				•		•
Second Breakdown Colle	ector with Base Forward Biased	I <sub>S/b</sub>	See Figure 2			
ON CHARACTERISTICS						
DC Current Gain (I <sub>C</sub> = 1.0 Adc, V <sub>CE</sub> = 5.0 Vdc) (I <sub>C</sub> = 4.5 Adc, V <sub>CE</sub> = 5.0 Vdc)		h <sub>FE</sub>	7.5 2.25			
Base-Emitter Saturation Voltage ( $I_C = 2.5$ Adc, $I_B = 1.0$ Vdc) ( $I_C = 4.5$ Adc, $I_B = 2.0$ Vdc)		VBE(sat)	_ _	_	1.5 1.5	Vdc
Collector-Emitter Saturation Voltage (I <sub>C</sub> = 2.5 Adc, I <sub>B</sub> = 1.0 Vdc) (I <sub>C</sub> = 4.5 Adc, I <sub>B</sub> = 2.0 Vdc)		VCE(sat)		_	2.0 3.0	Vdc
DYNAMIC CHARACTERIS	STICS					
Current-Gain — Bandwidth Product (IC = 0.1 Adc, VCE = 5.0 Vdc, f <sub>test</sub> = 1.0 MHz)		fΤ	_	7.0	_	MHz
Output Capacitance (V <sub>CB</sub> = 10 Vdc, I <sub>E</sub> = 0, f <sub>test</sub> = 0.1 MHz)		C <sub>ob</sub>	_	125	_	pF
SWITCHING CHARACTE	RISTICS					
Resistive Load (Table 1)						
Delay Time		t <sub>d</sub>	_	0.06	0.2	μs
Rise Time	(I <sub>C</sub> = 2.5 Adc, I <sub>B</sub> = 1.0 Adc, V <sub>CC</sub> = 500 Vdc	t <sub>r</sub>	_	0.08	2.0	- - -
Storage Time	$V_{BE(off)} = 5.0 \text{ Vdc}, t_p = 50 \mu s)$	t <sub>S</sub>	_	1.2	4.0	
Fall Time		t <sub>f</sub>		0.7	2.0	
Inductive Load (Table 1)						
Storage Time		t <sub>SV</sub>	_	1.2		μs
Crossover Time $ (I_{C} = 2.5 \text{ Adc}, I_{B} = 1.0 \text{ Adc}, V_{clamp} = 500 \text{ Vdc} $ $V_{BE(off)} = 5.0 \text{ Vdc}, t_{p} = 50  \mu\text{s}) $ Fall Time		t <sub>C</sub>	_	0.45	_	
		t <sub>fi</sub>		0.18	_	

<sup>(1)</sup> Pulse Test: Pulse Width = 300 µs, Duty Cycle ≤ 2%

## **MJE8503A**

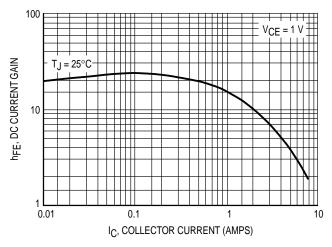


Figure 1. DC Current Gain

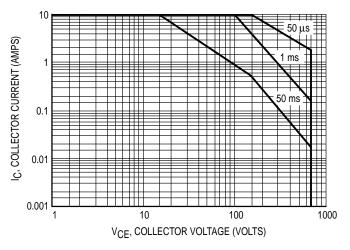
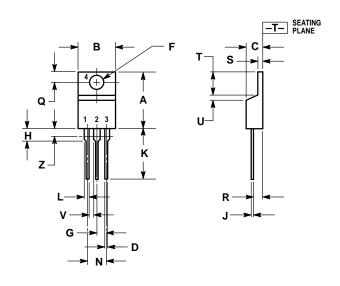


Figure 2. Forward Bias Safe Operating Area (FBSOA)

#### PACKAGE DIMENSIONS



- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- CONTROLLING DIMENSION: INCH.
  DIMENSION Z DEFINES A ZONE WHERE ALL
  BODY AND LEAD IRREGULARITIES ARE
  ALLOWED.

	INCHES		MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.570	0.620	14.48	15.75	
В	0.380	0.405	9.66	10.28	
С	0.160	0.190	4.07	4.82	
D	0.025	0.035	0.64	0.88	
F	0.142	0.147	3.61	3.73	
G	0.095	0.105	2.42	2.66	
Н	0.110	0.155	2.80	3.93	
J	0.018	0.025	0.46	0.64	
K	0.500	0.562	12.70	14.27	
L	0.045	0.060	1.15	1.52	
N	0.190	0.210	4.83	5.33	
Q	0.100	0.120	2.54	3.04	
R	0.080	0.110	2.04	2.79	
S	0.045	0.055	1.15	1.39	
Т	0.235	0.255	5.97	6.47	
U	0.000	0.050	0.00	1.27	
٧	0.045		1.15		
Z		0.080		2.04	

PIN 1. BASE

- 2. COLLECTOR
- EMITTER
- COLLECTOR

**CASE 221A-06** TO-220AB **ISSUE Y** 

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