REMOTE-CONTROL INTERFACE IC

M GENERAL DESCRIPTION

The NJM2129 is a remote-control interface for television, VCR, receiver, and others.

The signal flow of IN1 to OUT1 and IN2 to OUT2 is a first priority. When no signal is input from the IN2, a signal which is input from the IN1 is output to the OUT2 through the OUT1. Also when no signal is input from IN1 and IN2, a signal which is input from the OUT1 is output to the OUT2. An internal regulator can operate a LED.

PACKAGE OUTLINE





NJM2129D

NJM2129M

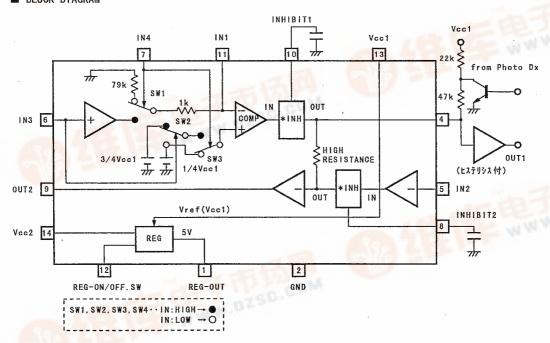
FEATURES

[INTERFACE BLOCK]

- IN4 switches One-Way or Two-Way communication [REGUIATOR BLOCK]
- Internal Current Limit Circuit
- Internal Output Short Protection
- ●ON/OFF Control
- Bipolar Technology
- Package Outline

DIP14, DMP14

■ BLOCK DIAGRAM



*The output of !NH becomes high impedance when its input is keeping over about 40 msec.



■ ABSOLUTE MAXIMUM RATINGS (Ta=25°C)

PARAMETER	SYMBOL	RATINGS	UNIT	
Supply Voitage	Vcc1, 2	15		
Input Voltage	VIN	15	V	
Power Dissipation	Po	DIP8 700 DMP8 300	mW	
Operating Temperature Range	Торг	-20 ~ +75	℃	
Storage Temperature Range	Tets	-40 ~ +125	℃	

■ ELECTRICAL CHARACTERISTICS (Vcc1=5V, Ta=25°C)

PARAMETER	SYMBOL			TEST	CON	OITIC	ON			MIN.	TYP.	MAX.	UNIT
[INTERFACE]		INPUT CONDITION						IRCU	ΙT				
[INTERPACE]		OUT1	IN1	1N2	1 N3	1 N4	SW1	SW2	SW3				
Operating Supply Voltage1	Vcc1	-	_	_	1	ı				4. 75	5.0	5. 25	٧
Operating Current1	lcc1	-	L	L	ᆜ	ا ا					2	4	mA
Operating Current2	Icc2	_		н	Н	Η	3	2	3		4. 5	7	mA
1N2/3/4-Vth	IN2/3/4-Vth	ı	_	-		1				2. 0	2. 5	3. 0	V
IN1-Vth (note 1)	IN1-Vth	_		_	L	Н				1. 0	1. 3	2. 0	٧
		_			H/L	_l				1. 0	1. 3	2. 0	٧
		_		-	Н	Н				3. 0	3.6	4. 0	٧
OUT1 (Low)	OUT1-L		Н	-	-	-		2		0	_	1. 5	٧
OUT1 (High)	OUT1-H		*L	_		_		,1		3. 5		5. 0	٧
OUT1(Hi-Imp)	OUT1-Hi-Imp		L		_	_		1		0		1. 5	٧
			L	-	_			2		3. 5	_	5. 0	٧
OUT2 (Low)	OUT2-L	L	Н	*L	_	_		2	1				
		Н	*L	*L	_	_		1	1			<u>'</u>	
		L/H	L	*L	_	_		1/2	1	0	_	1. 5	ν
		Н	*L	L	_	_		1	1				
			L	L	_	_		2	1				
OUT2(Hsgt)	OUT2-H	L	Н	Н	_	-		2	2				
·		Н	*L	Н	-	_		1	2				
		L/H	L	Н	_	_		1/2	2	3. 5	-	5.0	V
		L	Н	L	_	_		2	2				
			L	L	-			1	2				1

(note 1): The Vth of IN1 is changed by condition of IN3 and IN4. \star :For INHIBIT.

NJM2129

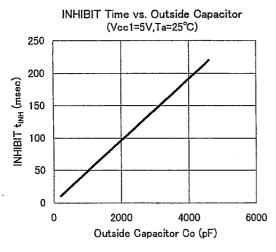
■ ELECTRICAL CHARACTERISTICS (Vcc1=5V, Ta=25°C)

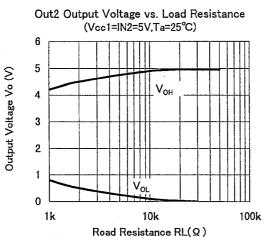
PARAMETER	SYMBOL	TEST CONDITION								MIN.	TYP.	MAX.	UNIT
[INTERFACE]		I N	PUT (COND	TION	ų .	С						
[INTERFACE]		OUT1	IN1	IN2	1 N3	IN4	SW1	SW2	SW3				
IN1 Input Impedance	IN1-Rin	-		-	-	_	1			47	80	120	kΩ
IN1-OUT (Low)	IN1-Lout			-	L	н	2			2	2. 5	3	٧
				-	L	Н	3			0	_	1.0	٧
IN1-OUT (High)	IN1-Hout	_		-	Н	Н	2			3. 5	_	5. 0	٧
		_		_	Н	Н	3			2	2. 5	3	٧
IN1-OPEN	IN1-Open	_		-	Н	Н	1			4. 0	_	5.0	V
INHIBIT1 Time	INH1-time	_	*L	_	_	L				20	40	80	ms
INHIBIT2 Time	INH2-time	-	-	*L	-	_		1		20	40	80	ms
Slew Switch1(IN1→0UT2)		Vcc1:0FF, IN1=3.5V 3							3. 0	_	_	٧	
[POWER SUPPLY]	(note 3)												
Operating Power Supply2	Vcc2									5. 75	5. 9	12 (note4)	٧
Operating Current2	l cc2	1 o=0	l o=OmA						-	2	3	mA	
	j	lo=	50mA							_	20	30	mA
Output Voltage	Vout	Voc	Vcc2=5. 9V, Io=60mA								5. 0	5, 3	٧
Line Regulation	△Vo-Vcc2	Vcc2=5. 75V~12V, lo=50mA							_		300	mA	
Load Regulation	∆Vo-lo	Vcc2=5. 9V, lo=0∼50mA							_	_	300	mA	
REG-SW (ON)	Reg-ON									3. 0		5. 0	٧
REG-SW(OFF)	Reg-OFF	,								0	_	2.0	٧

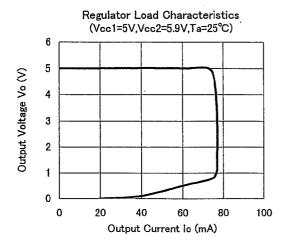
⁽note 3) The Vref in Power Supply block is the Vcc1, so that its specification is guaranteed at Vcc1=5V.

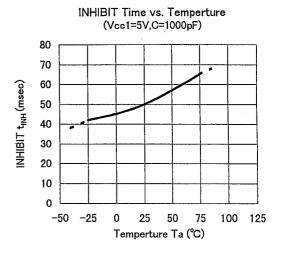
⁽note 4) The Supply voltage of Vcc2 must be chose less then power dissipation.

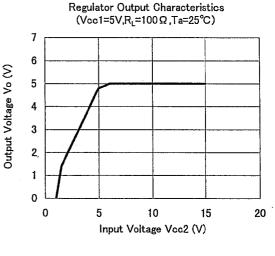
■ TYPICAL CHARACTERISTICS

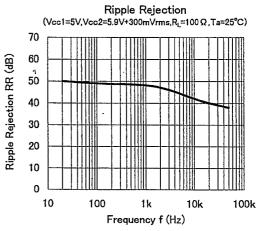












NJM2129

MEMO

[CAUTION]
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