

C-MOS QUAD ANALOG SWITCH

■ GENERAL DESCRIPTION

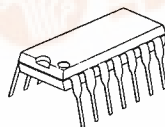
The NJU4066B is a quad bilateral analog switches, which are controlled by independent control signals.

The analog switch is ON during the control signal is "H", and OFF during it is "L".

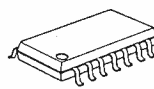
The low on-state resistance and superior transfer characteristics permit input of wide voltage range, consequently it is suitable for analog and digital signal switching, chopper-modulator-demodulator and others.

The NJU4066B is functionally and pin-to-pin compatible with RCA CD4066B and Motorola MC14066B.

■ PACKAGE OUTLINE



NJU4066BD



NJU4066BM



NJU4066BV

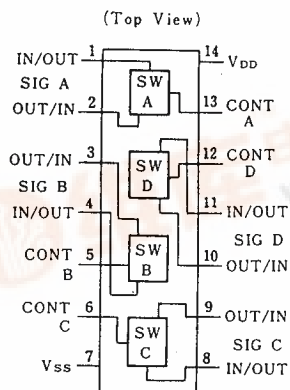
■ FEATURES

- 4 Independent Bilateral Analog Switches
- Low On-state Resistance
- Package Outline -- DIP/DMP/SSOP 14
- C-MOS Technology

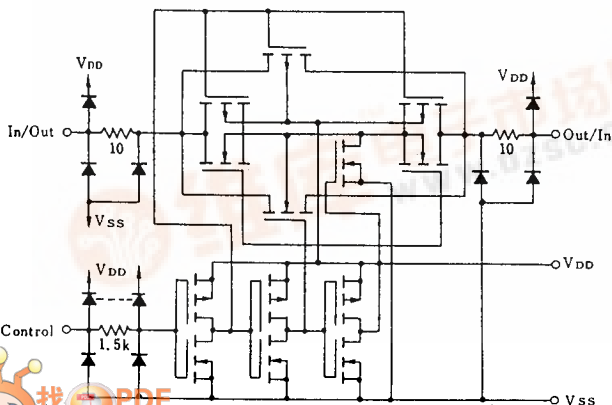
■ TERMINAL DESCRIPTION

NO.	SYMBOL	FUNCTION
13,5,6,12	A,B,C,D	Control Inputs
1,2,3,4 8,9,10,11	IN/OUT (OUT/IN)	Signal Input/Output (Output/Input)
14	V _{DD}	Power Supply
7	V _{SS}	Ground

■ PIN CONFIGURATION



■ EQUIVALENT CIRCUIT



■ TRUTH TABLE

Control Signal	Switch
V _{DD} (1)	ON
V _{SS} (0)	OFF

■ ABSOLUTE MAXIMUM RATINGS

(Ta=25°C)

P A R A M E T E R	S Y M B O L	R A T I N G S	U N I T
Supply Voltage	V_{DD}	- 0.5 ~ +20	V
Input Voltage	V_{IN}	- 0.5 ~ $V_{DD}+0.5$ *	V
Output Voltage	V_O	- 0.5 ~ $V_{DD}+0.5$ *	V
Input Current	I_{IN}	± 10	mA
Output Current	I_O	± 10	mA
Power Dissipation	P_D	500 { DIP } 200 { DMP } 300 { SSOP }	mW
		100 (Per Gate)	
Operating Temperature	T_{opr}	- 40 ~ + 85	°C
Storage Temperature	T_{stg}	- 60 ~ + 150	°C

 * $V_{DD}+0.5V$ must be 20V or less.

■ ELECTRICAL CHARACTERISTICS

• DC Characteristics

 ($V_{SS}=0V$)

P A R A M E T E R	S Y M B O L	C O N D I T I O N S	V_{DD} (V)	Ta=-40°C		Ta=25°C			Ta=85°C		U N I T
				MIN	MAX	MIN	TYP	MAX	MIN	MAX	
Quiescent Current	I_{DD}	$V_{IN}=V_{SS}$ or V_{DD}	5 10 15 20	0.25 0.50 1.0 5.0		0.25 0.50 1.0 5.0			7.5 15 30 150		μA
Low Level Control Input Voltage	V_{ILC}	$I_O < 1\mu A$, $V_O=0.5V$ or 4.5V $V_O=1V$ or 9V $V_O=1.5V$ or 13.5V	5 10 15	1 2 2		1 2 2			1 2 2		V
High Level Control Input Voltage	V_{IHC}	$I_O < 1\mu A$, $V_O=0.5V$ or 4.5V $V_O=1V$ or 9V $V_O=1.5V$ or 13.5V	5 10 15	3.5 7 11		3.5 7 11			3.5 7 11		V
Input Current	I_{IN}	$V_{IN}=0$ or 18V	18	± 0.1		± 0.1			± 1		μA
Operating Voltage				3	18	3		18	3	18	V
On-State Resistance	R_{ON}	$V_{SS}=0V$, $V_{IS}=V_{SS}$ to V_{DD}	5 10 15	850 330 210		300 150 100	1050 400 240		1200 500 300		Ω
Off-Channel Leakage Current		$V_{SS}=0V$, $V_{IS}=V_{DD}$, $V_O=V_{SS}$	18	0.1		0.1			1		μA
SW to SW On-State Resistance Difference	ΔR_{ON}	$V_{SS}=0V$, $V_{IS}=V_{SS}$ to V_{DD}	5 10 15			15 10 5					Ω

SWITCHING CHARACTERISTICS

 ($T_a=25^{\circ}\text{C}$, $V_{SS}=0\text{V}$)

P A R A M E T E R		SYMBOL	C O N D I T I O N S	V _{DD} (V)	MIN	TYP	MAX	UNIT
Propagation Delay Time	In - Out	t _{PHL}	R _L =10kΩ C _L =50pF V _C =V _{DD}	5 10 15	20 10 7	40 20 15	ns	
	Out - In	t _{PLH}		5 10 15	20 10 7	40 20 15		
Cut-Off Frequency (ON)			R _L =1kΩ, 20log(V _{OUT} /V _{IN})=-3dB V _C =V _{DD} , V _{IS} =5V _{P-P} , V _{SS} =-5V	5	40		MHz	
Propagation Delay Time	Control-Out	t _{PZH}	R _L =10kΩ, C _L =50pF V _{IS} =V _{DD} , R _L →V _{SS}	5 10 15	35 20 15	70 40 30	ns	
	Control-Out	t _{PZL}	R _L =10kΩ, C _L =50pF V _{IS} =V _{SS} , R _L →V _{DD}	5 10 15	35 20 15	70 40 30	ns	
Sine-Wave Distortion			R _L =10kΩ, V _{SS} =-5V V _C =V _{DD} , f=1kHz, V _{IS} =5V _{P-P}	5	0.05		%	
Crosstalk	SW A to B		R _L =1kΩ, 20log(V _{OUT} /V _{IN})=-50dB V _C =V _{SS} =-5V, V _{IS} =5V _{P-P} , R _{IN} =10kΩ	5	8		MHz	
	Control-Out		R _L =1kΩ, V _{SS} =0V V _C =10V _{P-P}	10	50		mV	
Feedthrough All Channels Off			R _L =1kΩ, 20log(V _{OUT} /V _{IN})=-50dB V _C =V _{SS} =-5V, V _{IS} =5V _{P-P}	5	1		MHz	
Input Capacitance		C _{IN}			7.5		pF	

NJU4066B

MEMO

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