查询SF16JZ51供应商 TOSHIBA

SF16GZ51,SF16JZ51

Unit: mm

TOSHIBA THYRISTOR SILICON PLANAR TYPE

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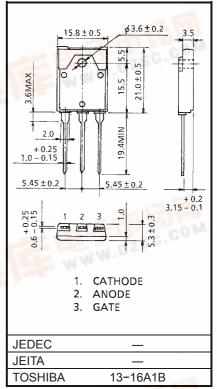
MEDIUM POWER CONTROL APPLICATIONS

Repetitive Peak Off-State Voltage : V_{DRM} = 400,600V
 Repetitive Peak Reverse Voltage : V_{RRM} = 400,600V

- Average On–State Current : I_T (AV) = 16A
- Isolation Voltage : VIsol = 1500V AC

MAXIMUM RATINGS

-150.							
CHARACTERISTIC		SYMBOL	RATING	UNIT			
Repetitive Peak Off-State Voltage and	SF16GZ51	V _{DRM}	400	V			
Repetitive Peak Reverse Voltage	SF16JZ51	V _{RRM}	600	v			
Non-Repetitive Peak Reverse Voltage	SF16GZ51	V _{RSM}	500	V			
(Non−Repetitive <5ms, T _j = 0~125°C)	SF16JZ51	¥RSM	720				
Average On-State Current (Half Sine Waveform)		I _{T(AV)}	16	A			
R.M.S On-State Current		I _{T(RMS)}	25	А			
Peak One Cycle Surge On-State Current (Non-Repetitive)		ITSM	250 (50Hz)	А			
			275 (60Hz)	~			
I ² t Limit Value		l ² t	312	A ² s			
Critical Rate of Rise of On-State Curret (Note)		di / dt	100	A / µs			
Peak Gate Power Dissipation		P _{GM}	5	W			
Average Gate Power Dissipation		PG (AV)	0.5	W			
Peak Forward Gate Voltage		VFGM	10	V			
Peak Reverse Gate Voltage		V _{RGM}	-5	V			
Peak Forward Gate Current		I _{GM}	2	А			
Junction Temperature		Тj	-40~125	°C			
Storage Temperature Range		T _{stg}	-40~125	°C			
Isolation Voltage (AC, t = 1min.)		V _{Isol}	1500	V			



Weight: 5.9g

Note : di / dt Test Condition, i_G = 30mA, t_{gw} = 10 μ s, t_{gr} ≤ 250ns



1

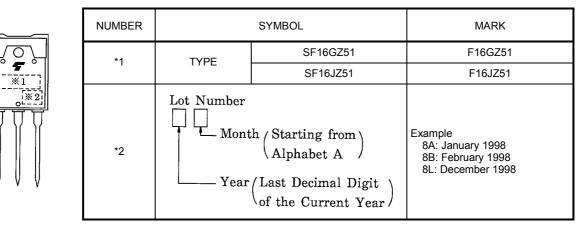
<u>TOSHIBA</u>

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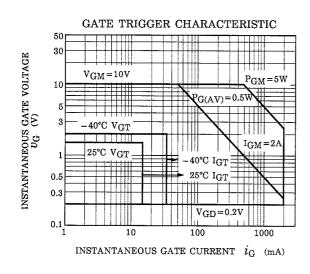
ELECTRICAL CHARACTERISTICS (Ta = 25°C)

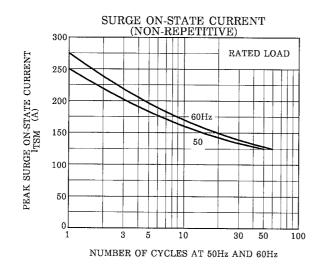
CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Repetitive Peak Off-State Current and Repetitive Peak Reverse Current	I _{DRM} I _{RRM}	V _{DRM} = V _{RRM} = Rated	_	_	20	μA
Peak On-State Voltage	V _{TM}	I _{TM} = 50A	_	_	1.5	V
Gate Trigger Voltage	V _{GT}	- V _D = 6V, R _L = 10Ω	_	_	1.5	V
Gate Trigger Current	I _{GT}	$v_{\rm D} = 0v, R_{\rm L} = 10\Omega$	_	_	15	mA
Holding Current	Ι _Η	V _D = 6V, I _{TM} = 500mA	_	_	50	mA
Critical Rate of Rise of Off-State Voltage	dv / dt	V _{DRM} = Rated, Tc = 125°C Exponential Rise	_	50	_	V / µs
Thermal Resistance	R _{th (j−c)}	Junction to Case	_	_	1.5	°C/W

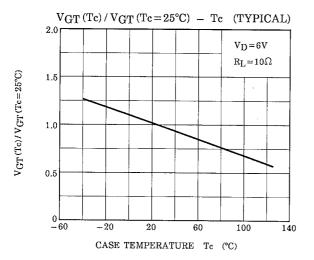
MARKING

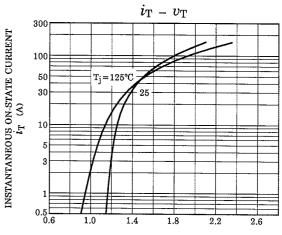


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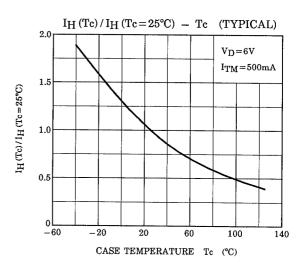


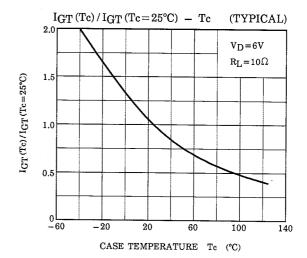






INSTANTANEOUS ON-STATE VOLTAGE $v_{\rm T}$ (V)



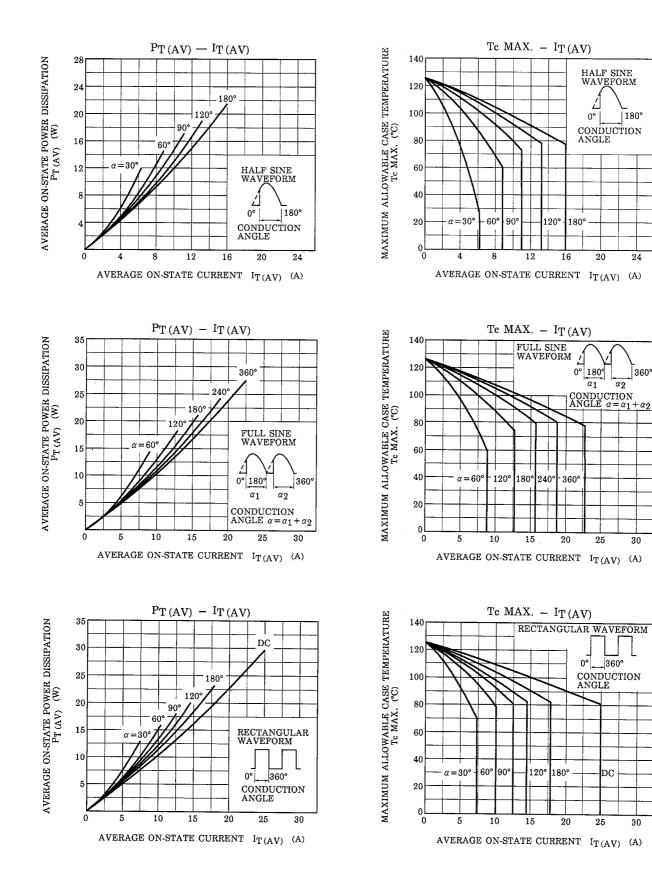


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24

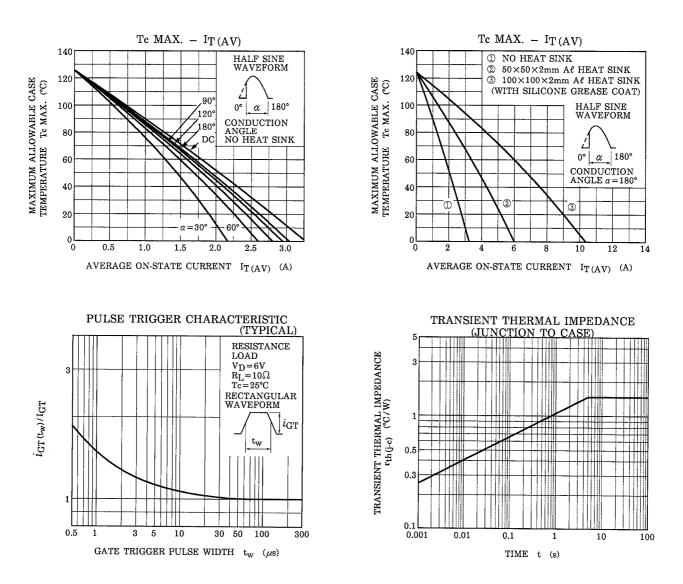
360°

30



30

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