

TOSHIBA FIELD EFFECT TRANSISTOR SILICON N CHANNEL MOS TYPE ( $\pi$ -MOSVI)

# 2SK3128

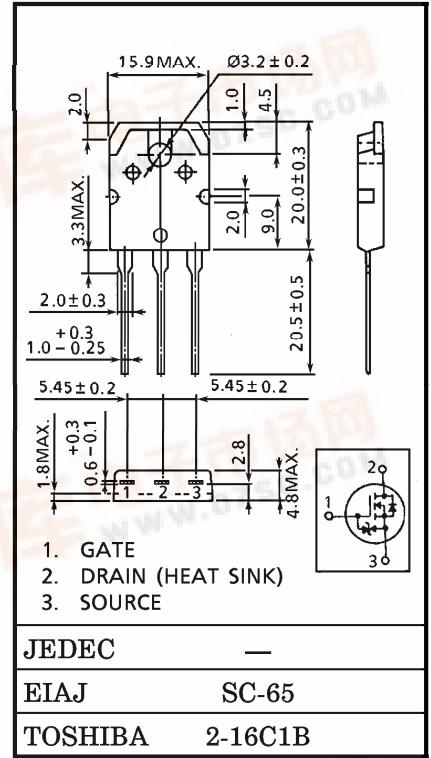
HIGH SPEED, HIGH CURRENT SWITCHING APPLICATIONS  
 CHOPPER REGULATOR, DC-DC CONVERTER AND MOTOR DRIVE APPLICATIONS

INDUSTRIAL APPLICATIONS  
 Unit in mm

- Low Drain-Source ON Resistance :  $R_{DS(ON)} = 9.5 \text{ m}\Omega$  (Typ.)
- High Forward Transfer Admittance :  $|Y_{fs}| = 40 \text{ S}$  (Typ.)
- Low Leakage Current :  $I_{DSS} = 100 \mu\text{A}$  (Max.) ( $V_{DS} = 30 \text{ V}$ )
- Enhancement-Mode :  $V_{th} = 1.5 \sim 3.0 \text{ V}$   
 ( $V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA}$ )

MAXIMUM RATINGS ( $T_a = 25^\circ\text{C}$ )

CHARACTERISTIC	SYMBOL	RATING	UNIT
Drain-Source Voltage	$V_{DSS}$	30	V
Drain-Gate Voltage ( $R_{GS} = 20 \text{ k}\Omega$ )	$V_{DGR}$	30	V
Gate-Source Voltage	$V_{GSS}$	$\pm 20$	V
Drain Current	DC	$I_D$	60 A
	Pulse	$I_{DP}$	180 A
Drain Power Dissipation ( $T_c = 25^\circ\text{C}$ )	$P_D$	150	W
Single Pulse Avalanche Energy**	$E_{AS}$	411	mJ
Avalanche Current	$I_{AR}$	60	A
Repetitive Avalanche Energy*	$E_{AR}$	1.5	mJ
Channel Temperature	$T_{ch}$	150	$^\circ\text{C}$
Storage Temperature Range	$T_{stg}$	$-55 \sim 150$	$^\circ\text{C}$



THERMAL CHARACTERISTICS

CHARACTERISTIC	SYMBOL	MAX.	UNIT
Thermal Resistance, Channel to Case	$R_{th(ch-c)}$	1.0	$^\circ\text{C}/\text{W}$
Thermal Resistance, Channel to Ambient	$R_{th(ch-a)}$	50	$^\circ\text{C}/\text{W}$

Note ;

\* Repetitive rating ; Pulse Width Limited by Max. junction temperature.

\*\*  $V_{DD} = 25 \text{ V}, T_{ch} = 25^\circ\text{C}$  (initial),  $L = 82 \mu\text{H}, R_G = 25 \Omega, I_{AR} = 60 \text{ A}$

**This transistor is an electrostatic sensitive device.  
 Please handle with caution.**

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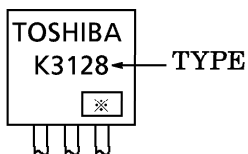
ELECTRICAL CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC		SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Gate Leakage Current		IGSS	VGS = ±16 V, VDS = 0 V	—	—	±10	μA
Drain Cut-off Current		IDSS	VDS = 30 V, VGS = 0 V	—	—	100	μA
Drain-Source Breakdown Voltage		V(BR)DSS	ID = 10 mA, VGS = 0 V	30	—	—	V
Gate Threshold Voltage		Vth	VDS = 10 V, ID = 1 mA	1.5	—	3.0	V
Drain-Source ON Resistance		RDS(ON)	VGS = 10 V, ID = 30 A	—	9.5	12	mΩ
Forward Transfer Admittance		Yfs	VDS = 10 V, ID = 30 A	20	40	—	S
Input Capacitance		Ciss	VDS = 10 V, VGS = 0 V f = 1 MHz	—	2300	—	pF
Reverse Transfer Capacitance		Crss		—	380	—	
Output Capacitance		Coss		—	1100	—	
Switching Time	Rise Time	tr	<p> <math>I_D = 30\text{ A}</math>  <math>V_{GS} = 10\text{ V}</math>  <math>0\text{ V}</math>  <math>4.7\Omega</math>  <math>R_L = 1.0\Omega</math>  <math>V_{DD} \cong 30\text{ V}</math> </p>	—	12	—	ns
	Turn-on Time	ton		—	25	—	
	Fall Time	tf		—	75	—	
	Turn-off Time	t <sub>off</sub>		$V_{IN} : t_r, t_f < 5\text{ ns},$ $\text{Duty} \leq 1\%, t_w = 10\ \mu\text{s}$	—	200	
Total Gate Charge (Gate-Source Plus Gate-Drain)		Qg	VDD ≅ 24 V, VGS = 10 V	—	66	—	nC
Gate-Source Charge		Qgs	ID = 60 A	—	45	—	
Gate-Drain (“Miller”) Charge		Qgd		—	21	—	

SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Continuous Drain Reverse Current	IDR	—	—	—	60	A
Pulse Drain Reverse Current	IDRP	—	—	—	180	A
Diode Forward Voltage	VDSF	IDR = 60 A, VGS = 0 V	—	—	-1.5	V
Reverse Recovery Time	t <sub>rr</sub>	IDR = 60 A, VGS = 0 V	—	150	—	ns
Reverse Recovery Charge	Q <sub>rr</sub>	dIDR / dt = 50 A / μs	—	0.26	—	μC

MARKING



※ Lot Number

□ □ — Month (Starting from Alphabet A)

— Year (Last Number of the Christian Era)