2SK3192

Silicon N-channel power MOSFET

■ Features

- Avalanche energy capability guaranteed
- High-speed switching
- Low ON resistance Ron
- No secondary breakdown

■ Applications

- PDP
- Switching mode regulator

■ Absolute Maximum Ratings $T_C = 25$ °C

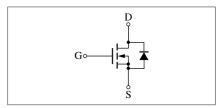
Parameter	Symbol	Rating	Unit	
Drain-source surrender voltage	V _{DSS}	250	V	
Gate-source surrender voltage	V _{GSS}	±30	V	
Drain current	I_D	±30	A	
Peak drain current	I_{DP}	±120	A	
Avalanche energy capability *	EAS	925	mJ	
Power dissipation	P_{D}	100	W	
$T_a = 25^{\circ}C$		3		
Channel temperature	T _{ch}	150	°C	
Storage temperature	T _{stg}	-55 to +150	°C	

Note) *: L = 1.74 mH, $I_L = 30 \text{ A}$, $V_{DD} = 50 \text{ V}$, 1 pulse, $T_a = 25^{\circ}\text{C}$

Unit: mm 15.0±0.3 (3.2)11.0±0.2 φ 3.2±0.1 15.0±0.2 2.0±0.1 2.0±0.2 (3.2) 1.1±0.1 0.6±0.2 10.9±0.5 1: Gate 2: Drain 3: Source EIAJ: SC-92 TOP-3F-B1 Package

Marking Symbol: K3192

Internal Connection



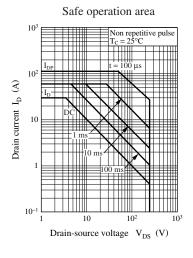
■ Electrical Characteristics $T_C = 25$ ° $C \pm 3$ °C

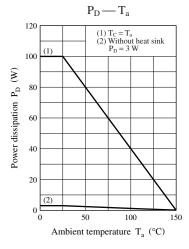
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Drain-source surrender voltage	V _{DSS}	$I_D = 1 \text{ mA}, V_{GS} = 0$	250			V
Drain-source cutoff current	I_{DSS}	$V_{DS} = 200 \text{ V}, V_{GS} = 0$			10	μΑ
Gate-source cutoff current	I_{GSS}	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0$			±1	μΑ
Gate threshold voltage	V _{th}	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$	2		4	V
Drain-source ON resistance	R _{DS(on)}	$V_{GS} = 10 \text{ V}, I_D = 15 \text{ A}$		50	68	mΩ
Forward transfer admittance	Y _{fs}	$V_{DS} = 10 \text{ V}, I_{D} = 15 \text{ A}$	8	15		S
Short-circuit forward transfer capacitance (Common source)	C _{iss}	$V_{DS} = 10 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$		4200		pF
Short-circuit output capacitance (Common source)	C _{oss}			1600		pF
Reverse transfer capacitance (Common source)	C _{rss}			650		pF
Turn-on delay time	t _{d(on)}	$V_{DD} = 100 \text{ V}, I_D = 15 \text{ A}, R_L = 6.7 \Omega$		45		ns
Rise time	t _r	$V_{GS} = 10 \text{ V}$		115		ns
Turn-off delay time	t _{d(off)}			330		ns
Fall time	$t_{\rm f}$			130		ns

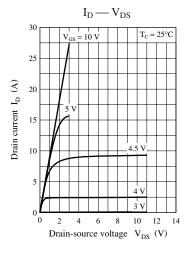
■ Electrical Characteristics (continued) $T_C = 25$ °C ± 3 °C

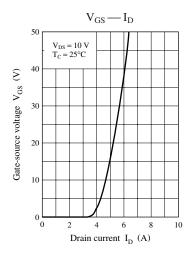
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Diode forward voltage	V _{DSF}	$I_{DR} = 30 \text{ A}, V_{GS} = 0$			-1.5	V
Reverse recovery time	t _{rr}	$L = 230 \mu H, V_{DD} = 100 V$		260		ns
Reverse recovery charge	Q _{rr}	$I_{DR} = 15 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s}$		1.6		μС
Gate charge load	Q_g	$V_{DD} = 100 \text{ V}, I_D = 15 \text{ A}$		95		nC
Gate-source charge	Q_{gs}	$V_{GS} = 10 \text{ V}$		34		nC
Gate-drain charge	Q_{gd}			12		nC
Thermal resistance (ch-c)	R _{th(ch-c)}				1.25	°C/W
Thermal resistance (ch-a)	R _{th(ch-a)}				41.7	°C/W

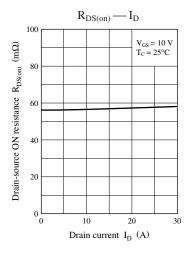
Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

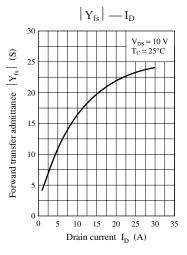


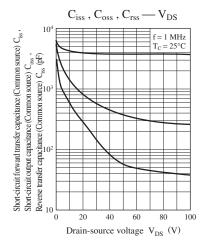












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