

STK4274

Thick Film Hybrid IC

Current Amp

TENTATIVE

Case Outline : 15 pins (See attached case outline drawing.)

Function : Current amp

Use : Video projectors

Feature : 2 channels/1 package for convergence use

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Notes	Value	Unit
Maximum Supply Voltage	V _{CC}	Note 1	±38	V
Maximum Collector Current	I _c	Tr6,13 DC 1sec	+2.0	A
		Tr7,14 DC 1sec	-2.0	A
Thermal Resistance	θ _{j-c}	Tr6,7,13,14 (Per power Tr)	3.0	°C/W
Junction Temperature	T _j		150	°C
Operating Case Temperature	T _c		105	°C
Storage Temperature	T _{stg}		-30 to +105	°C

Note 1. If the supply voltage is not balanced between +V_{CC} and -V_{CC}, the maximum rating of +V_{CC} - (-V_{CC}) must be 76V. Further, |±V_{CC} max| < 42.5V must be met.

Operating Characteristics at Ta = 25°C, R_g = 50Ω, See attached Test Circuit.

Parameter	Symbol	Conditions	min	typ	max	unit
Output Noise Voltage	V _{NO}	V _{CC} = ±24V				0.2 mVrms
Quiescent Current	I _{cco}	V _{CC} = ±24V		15	25	mA
Midpoint Voltage	V _N	V _{CC} = ±24V	-50	0	+50	mV
Output Delay Time	t _D	V _{CC} = ±20.5V, f = 15.75kHz, triangular wave input V _{P-P} = 1.5V				1 μsec

Remarks

- For power supply at the time of test, use a constant-voltage power supply unless otherwise specified.
- The output noise voltage is represented by the peak value on rms scale (VTVM) of average value indicating type.

The application circuit diagrams and circuit constants herein are included as an example and provide no guarantee for designing equipment to be mass-produced. The information herein is believed to be accurate and reliable. However, no responsibility is assumed by SANYO for its use; nor for any infringements of patents or other rights of third parties which may result from its use.



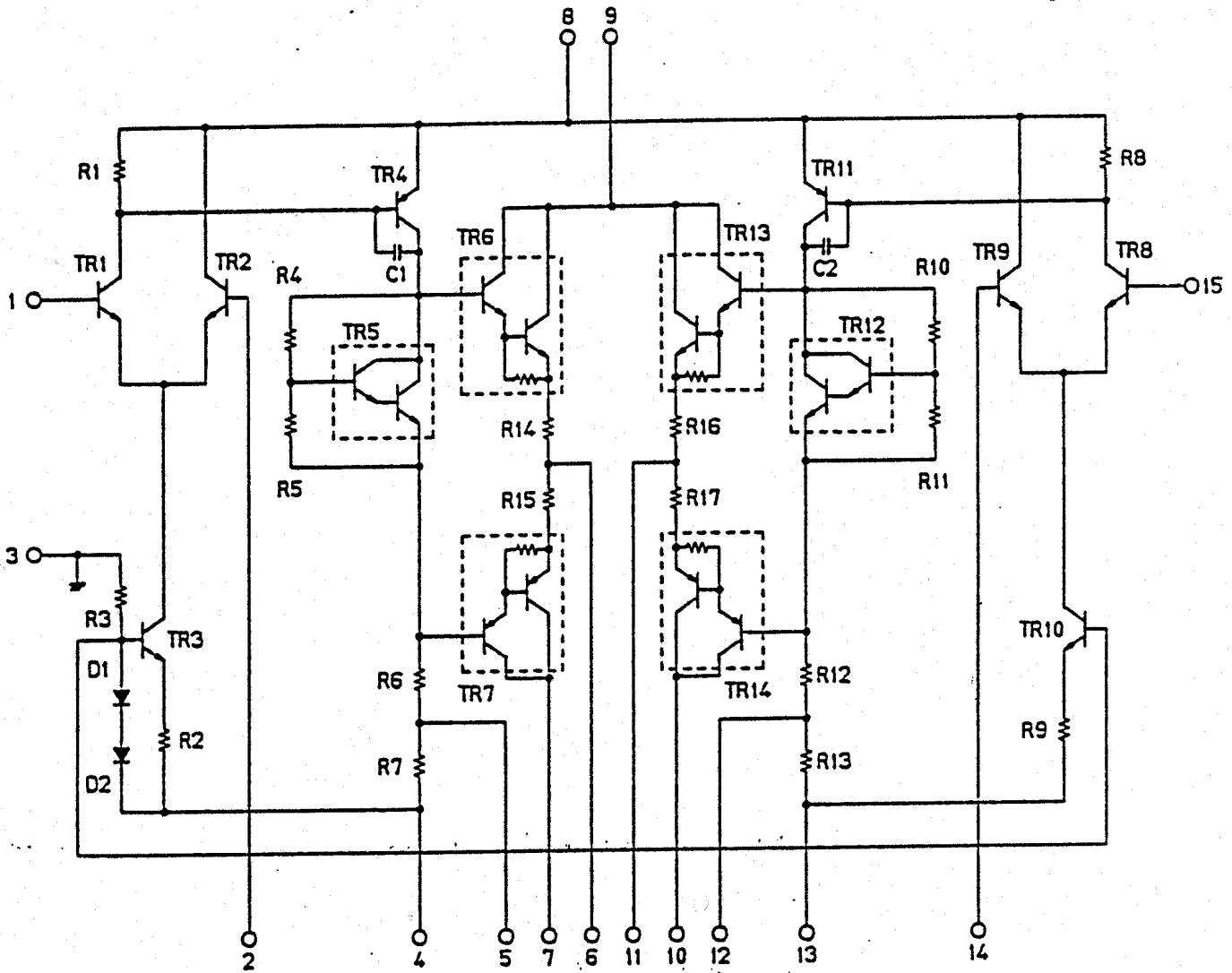
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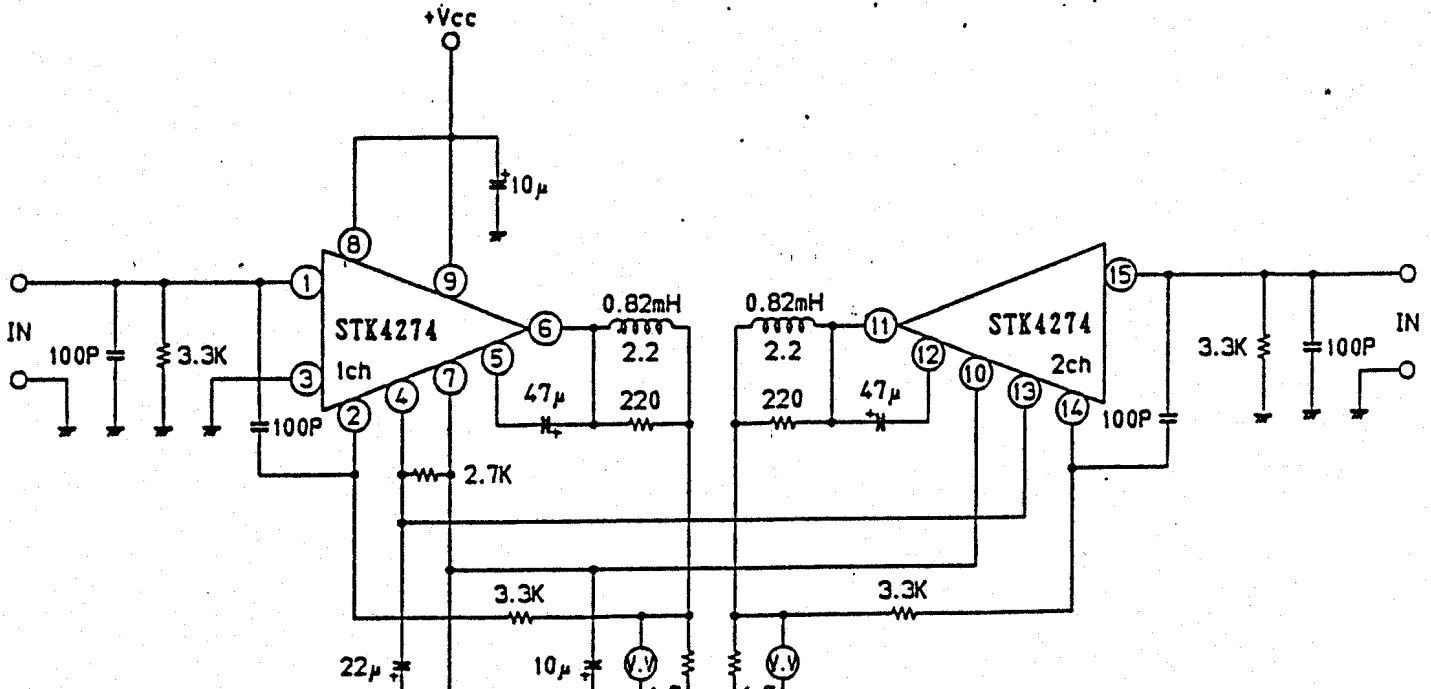
Musashi-ku, 1-6-2, Chomei Yushima, Bunkyo-ku, TOKYO 113, JAPAN

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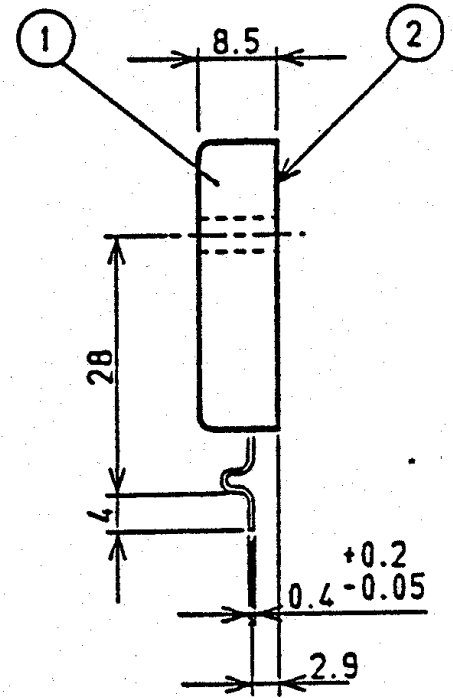
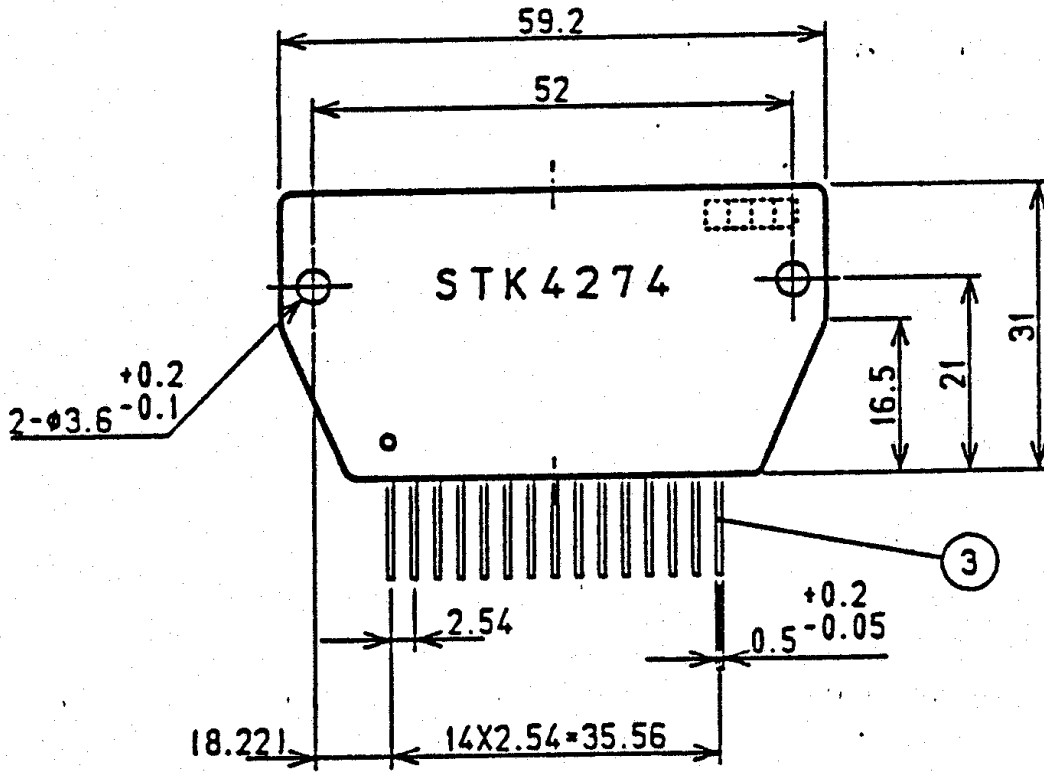
Internal Equivalent Circuit (STK4274)



Test Circuit (STK4274)



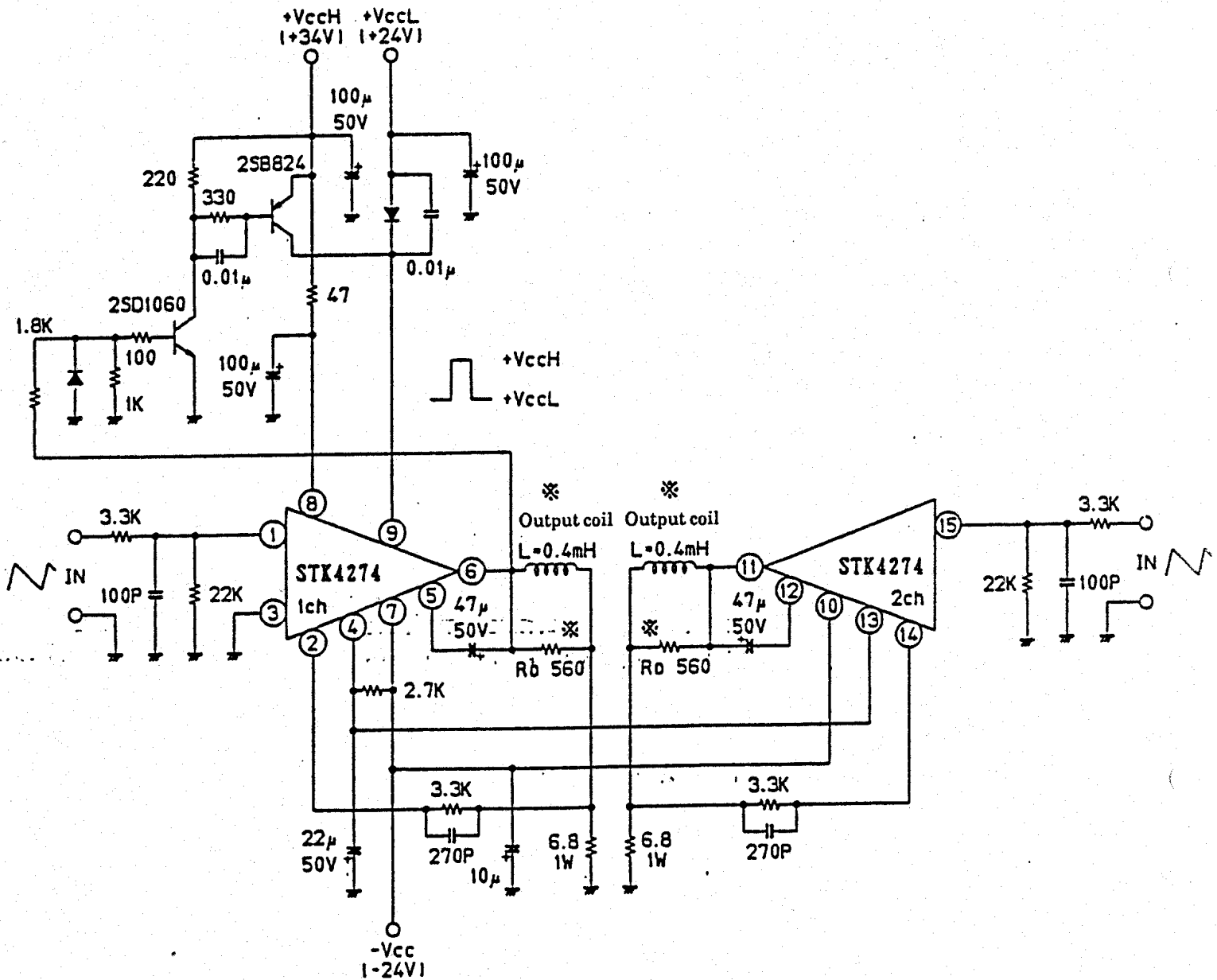
Case Outline (unit: mm)



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Sample Application Circuit (STK4274)

Video projector convergence correction circuit
 $f_H = 15.75\text{kHz}$
 Sample application to power supply selector



Design notes

1. Avoid pin-to-pin short, otherwise the IC will break down.
2. For mark ※, the constant is specified (tolerance $\pm 10\%$). If you want to change the constant, consult us.