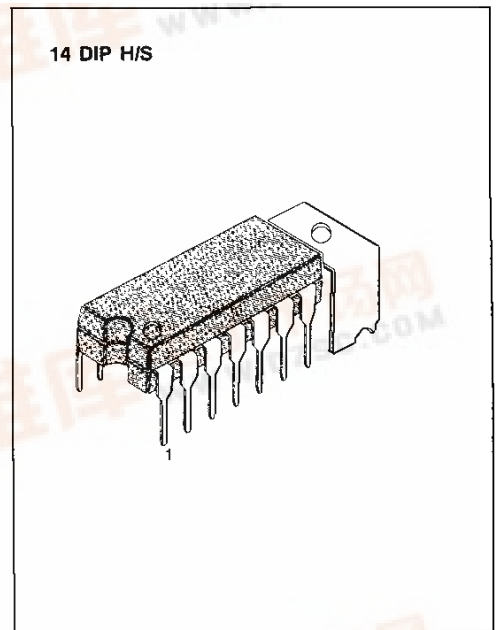


VIDEO IF PROCESSOR FOR B/W TVs

The KA2912 is a silicon monolithic integrated circuit designed for the VIF stage in B/W television receivers. This IC has all functions, including a video IF amplifier, video low-level detector, RF AGC, IF AGC and noise canceller. This IC is encapsulated a 14-pin dual in-line package with heat sink.

FEATURES

- High input sensitivity: Typ 30 dB μ .
- It can be used both of keyed type AGC and peak type AGC.
- It can be operated with the power supply voltage above 7V.
- Since the video detector has a wide bandwidth, it's suitable for the sound carrier frequency of 4.5, 5.5, 6.0, 6.5 MHz.
- As input is differential mode, it can be used with an SAW filter.
- All functions of the VIF stage are provided by this single chip IC. This will realize reduction of assembly costs as well as reduction of the number of external components.



ORDERING INFORMATION

Device	Package	Operating Temperature
KA2912	14 DIP H/S	-20 ~ +75°C

BLOCK DIAGRAM

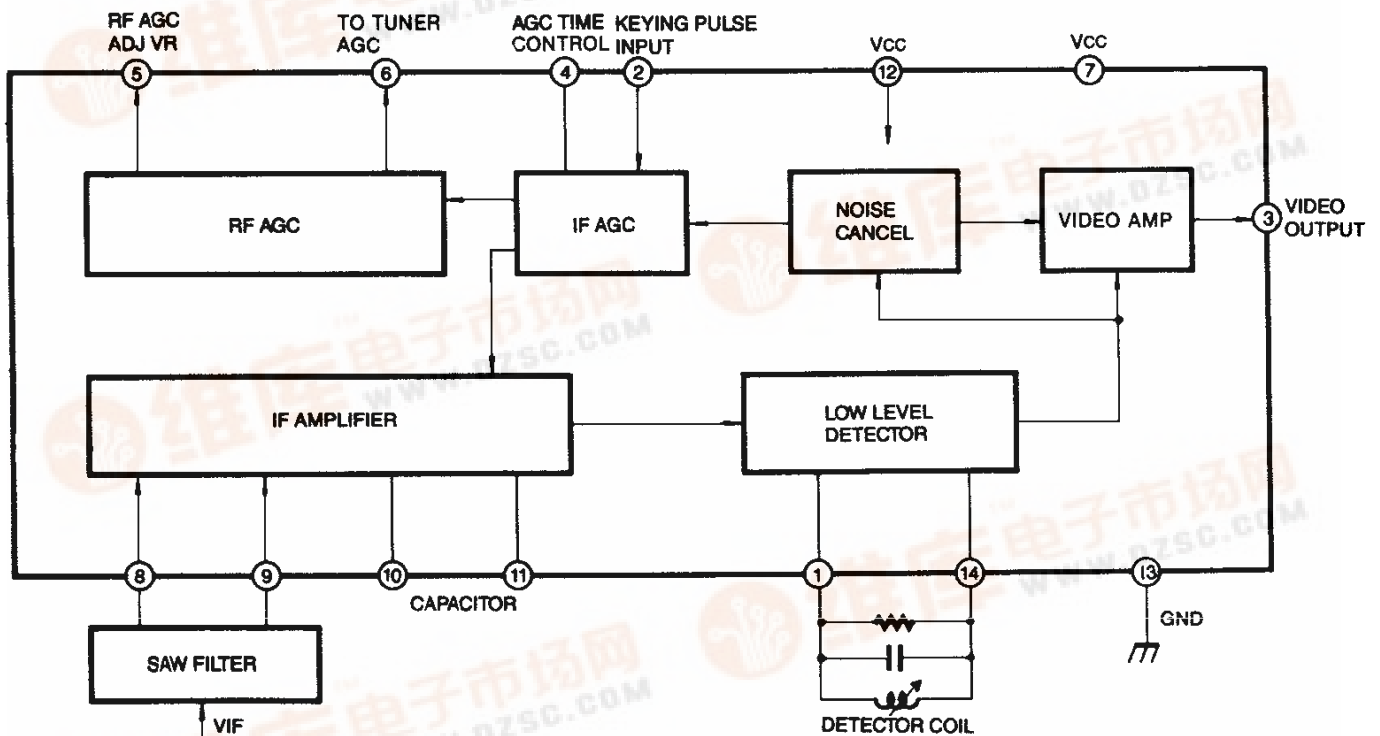


Fig. 1

ABSOLUTE MAXIMUM RATINGS ($T_a = 25^\circ\text{C}$)

Characteristic	Symbol	Value	Unit
Supply Voltage	V_{CC} (Pin 7)	15	V
Input Singnal Voltage	V_8, V_9	3	V_{P-P}
Power Dissipation	P_D	875 ($T_a = 75^\circ\text{C}$) free air	mW
Operating Temperature	T_{OPR}	- 20 ~ + 70	$^\circ\text{C}$
Storage Temperature	T_{STG}	- 40 ~ + 125	$^\circ\text{C}$

ELECTRICAL CHARACTERISTICS

($T_a = 25^\circ\text{C}$, $V_{CC} = 12\text{V}$, $f_p = 45.75\text{MHz}$, $f_m = 400\text{Hz}$)

Characteristic	Symbol	Test Conditions	Min	Typ	Max	Unit
Total Supply Current	I_{CC}	$I_7 + I_{12}$, $R_A = 150\Omega$	40	50	60	mA
Input Sensitivity	S_{VI}	MOD=80%, $v_O = 1.4V_{P-P}$	—	30	35	$\text{dB}\mu$
Maximum Input Voltage	$V_{I(MAX)}$	MOD=80%, -1dB Point	100	—	—	$\text{dB}\mu$
Video Output Voltage	V_O	MOD=80%, $v_i = 3\text{mVrms}$	1.0	1.4	1.7	V_{P-P}
Video Output DC Voltage	$V_{O(DC)}$	No Signal	3.3	3.8	4.3	V
Signal to Noise Ratio	S/N	MOD=80%-0% $v_i = 3\text{mVrms}$	40	50	—	dB
RF AGC Voltage (High)	V_{6H}	$V_5 = 0\text{V}$	8	9	11	V
RF AGC Voltage (Low)	V_{6L}	$V_5 = 7\text{V}$	—	0	0.5	V
Differential Gain	D.G.	Stair step $F_M = 3.58\text{MHz}$	—	—	10	%
Differential Phase	D.P.	Stair step $F_M = 3.58\text{MHz}$	—	—	10	deg
Video Detector Band Width	$G_V(F)$	-3dB Point	5.5	—	—	MHz
Input Resistance	R_{IN}		—	1.5	—	$\text{K}\Omega$
Input Capacitance	C_{IN}		—	3.3	—	pF

TYPICAL APPLICATION CIRCUIT

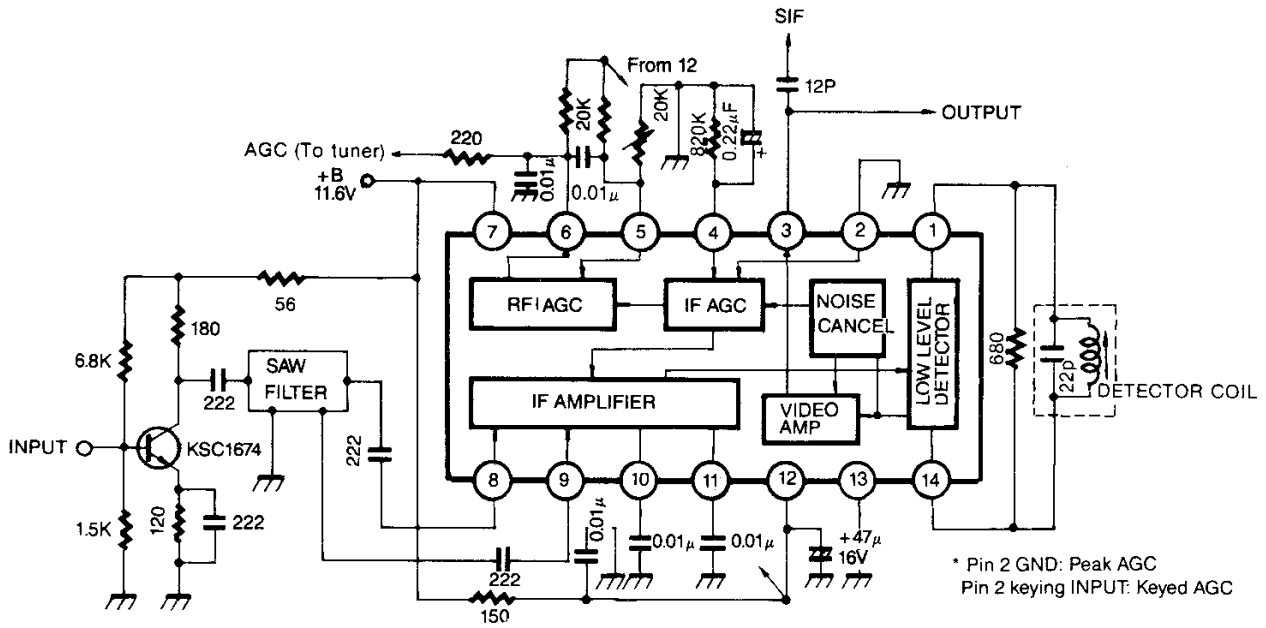


Fig. 2

TEST CIRCUIT

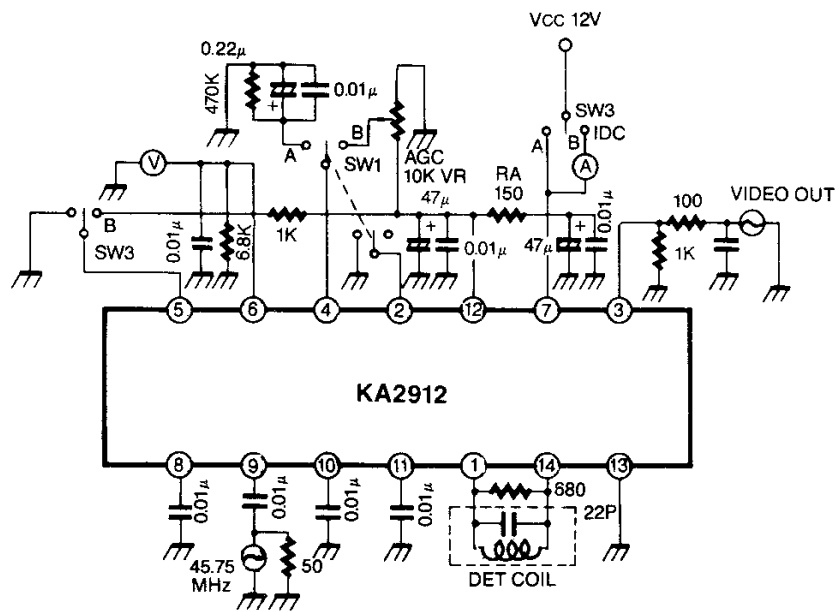
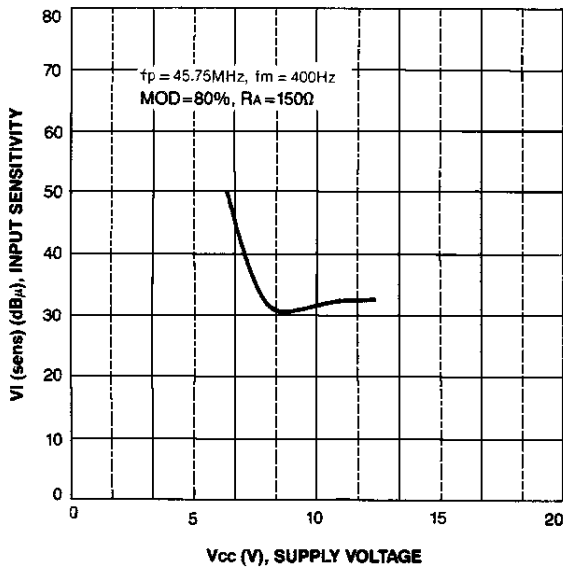
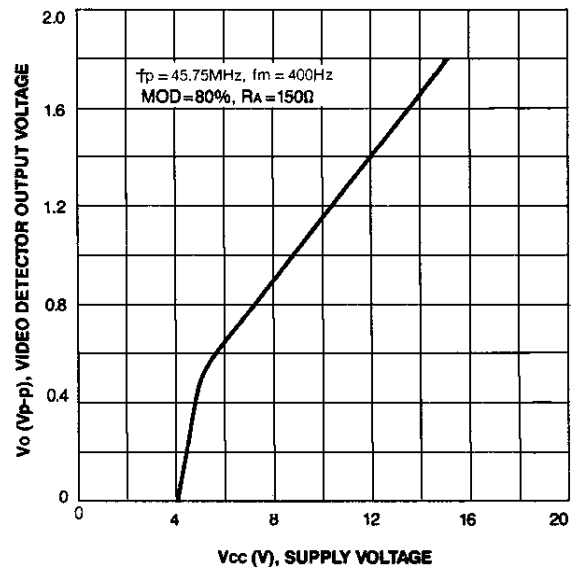


Fig. 3

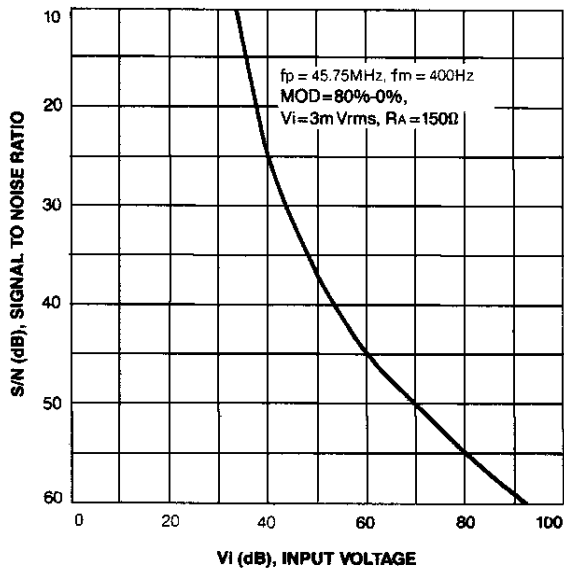
INPUT SENSITIVITY



VIDEO DETECTOR OUTPUT VOLTAGE Vs SUPPLY VOLTAGE



S/N Vs INPUT VOLTAGE



VIDEO DETECTOR OUTPUT VOLTAGE ATTENUATION Vs FREQUENCY

