FAIRCHILD SEMICONDUCTOR TM

www.fairchildsemi.com

KA2S1265

Fairchild Power Switch(SPS)

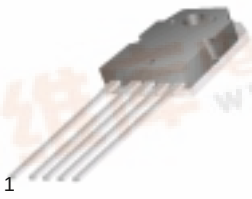
Features

- Wide operatimg frequency range up to (150kHz)
- Pulse by pulse over current limiting
- Over load protection
- Over voltage protecton (Min. 23V)
- Internal thermal shutdown function
- Under voltage lockout
- Internal high voltage sense FET
- External sync terminal
- Latch up Mode

Description

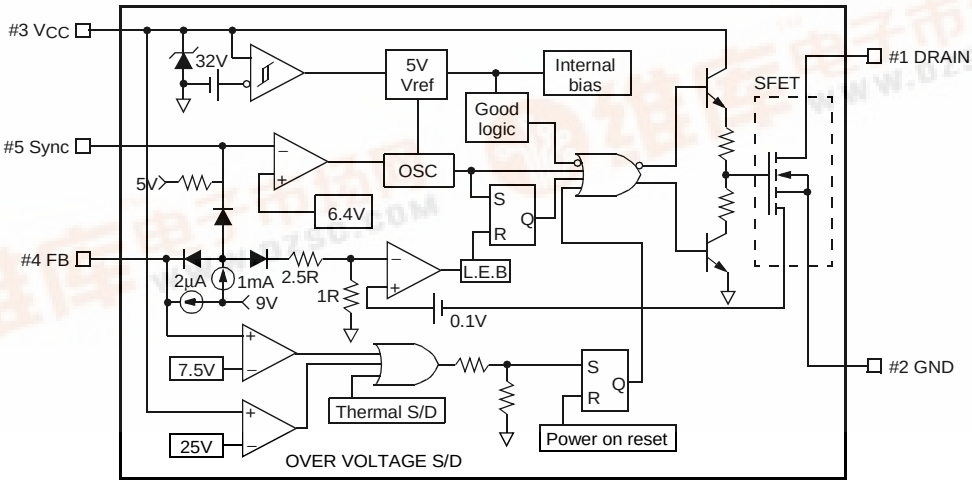
The SPS product family is specially designed for an offline SMPS with minimal external components. The SPS consist of high voltage power SenseFET and current mode PWM Controller IC. control IC features a trimmed oscillator, under voltage lock-out, leading edge blanking, optimized gate turn-on/turn-off driver, thermal shut down protection, over voltage protection, temperature compensated precision current sources for loop compensation and fault protection circuit. Compared to discrete MOSFET and controller or RCC switching converter solution, a SPS can reduce total component count, design size, weight and at the same time increase & efficiency, productivity, and system reliability. It has a basic platform well suited for cost-effective Monitor power supply.

TO-3P-5L



1. DRAIN 2. GND 3. Vcc 4. FB 5. Sync

Internal Block Diagram



Absolute Maximum Ratings

Characteristic	Symbol	Value	Unit
Maximum Drain voltage (1)	$V_{D,MAX}$	650	V
Drain-Gate voltage (R_{GS}=1MΩ)	V_{DGR}	650	V
Gate-source (GND) voltage	V_{GS}	±30	V
Drain current pulsed (2)	I_{DM}	48.0	A_{DC}
Single pulsed avalanche energy (3)	E_{AS}	785	mJ
Continuous drain current (T_C=25°C)	I_D	12	A_{DC}
Continuous drain current (T_C=100°C)	I_D	8.4	A_{DC}
Maximum Supply voltage	$V_{CC,MAX}$	30	V
Input voltage range	V_{FB}	–0.3 to V_{SD}	V
Total power dissipation	P_D	269	W
	Derating	2.17	W/°C
Operating ambient temperature	T_A	–25 to +85	°C
Storage temperature	T_{STG}	–55 to +150	°C

Notes:

1.Tj=25°C to 150°C

2.Repetitive rating: Pulse width limited by maximum junction temperature

3.L=10mH, V_{DD}=50V, R_G=27Ω, starting Tj=25 °C

Electrical Characteristics (SFET part)

(Ta = 25°C unless otherwise specified)

Characteristic	Symbol	Test condition	Min.	Typ.	Max.	Unit
Drain-source breakdown voltage	BV_{DSS}	V_{GS}=0V, I_D=50μA	650	-	-	V
Zero gate voltage drain current	I_{DSS}	V_{DS}=Max., Rating, V_{GS}=0V	-	-	50	μA
		V_{DS}=0.8Max., Rating, V_{GS}=0V, T_C=125°C	-	-	200	mA
Static drain-source on resistance (note)	$R_{DS(ON)}$	V_{GS}=10V, I_D=6.0A	-	0.72	-	W
Forward transconductance (note)	gfs	V_{DS}=50V, I_D=6.0A	5.7	-	-	S
Input capacitance	Ciss	V_{GS}=0V, V_{DS}=25V, f=1MHz	-	2700	-	pF
Output capacitance	Coss		-	300	-	
Reverse transfer capacitance	Crss		-	61	-	
Turn on delay time	$t_{d(on)}$	V_{DD}=0.5BV_{DSS}, I_D=12.0A (MOSFET switching time are essentially independent of operating temperature)	-	18	-	nS
Rise time	tr		-	37	-	
Turn off delay time	$t_{d(off)}$		-	88	-	
Fall time	tf		-	36	-	
Total gate charge (gate-source+gate-drain)	Qg	V_{GS}=10V, I_D=12.0A, V_{DS}=0.5BV_{DSS} (MOSFET switching time are essentially independent of operating temperature)	-	-	140	nC
Gate-source charge	Qgs		-	20	-	
Gate-drain (Miller) charge	Qgd		-	69	-	

Note:

Pulse test: Pulse width ≤ 300μS, duty cycle ≤ 2%

$S = \frac{1}{R}$

Electrical Charcteristics (CONTROL part)

(Ta = 25°C unless otherwise specified)

Characteristic	Symbol	Test condition	Min.	Typ.	Max.	Unit
UVLO SECTION						
Start threshold voltage	V_{START}	-	14	15	16	V
Stop operating voltage	V_{STOP}	After turn on	9	10	11	V
OSCILLATOR SECTION						
Initial accuracy	F_{OSC}	Ta=25°C	18	20	22	kHz
Frequency change with temperature (2)	$\Delta F/\Delta T$	–25°C≤Ta≤+85°C	-	±5	±10	%
Maximum duty cycle	Dmax	-	92	95	98	%
FEEDBACK SECTION						
Feedback source current	I_{FB}	Ta=25°C, Vfb=GND	0.8	1	1.2	mA
Shutdown Feedback voltage	V_{SD}	-	6.9	7.5	8.1	V
Shutdown delay current	I_{delay}	Ta=25°C, 5V≤Vfb≤V_{SD}	1.4	1.8	2.2	µA
SYNC. & SOFT START SECTION						
Soft start voltage	V_{SS}	V_{FB}=2V	4.7	5.0	5.3	V
Soft start current	I_{SS}	Sync & S/S=GND	0.8	-	-	mA
Sync threshold voltage(3)	V_{SYTH}	Vfb=5V	6.0	6.4	6.8	V
REFERENCE SECTION						
Output voltage (1)	V_{ref}	Ta=25°C	4.80	5.00	5.20	V
Temperature Stability (1)(2)	$V_{ref}/\Delta T$	–25°C≤Ta≤+85°C	-	0.3	0.6	mV/°C
CURRENT LIMIT (SELF-PROTECTION) SECTION						
Peak Current Limit	I_{OVER}	Max. inductor current	7.04	8.00	8.96	A
PROTECTION SECTION						
Thermal shutdown temperature (Tj) (1)	T_{SD}	-	140	160	-	°C
TOTAL DEVICE SECTION						
Start Up current	I_{START}	V_{CC}=14V	0.1	0.3	0.55	mA
Operating supply current (control part only)	I_{OP}	Ta=25°C	6	12	18	mA
V_{CC} zener voltage	V_Z	I_{CC}=20mA	30	32.5	35	V

NOTE:

1. These parameters, although guaranteed, are not 100% tested in production
2. These parameters, although guaranteed, are tested in EDS(water test) process
3.The amplitude of the sync. pulse is recommended to be between 2V and 3V for stable sync. function.

Typical Performance Characteristics

(These characteristic graphs are normalized at Ta = 25°C)

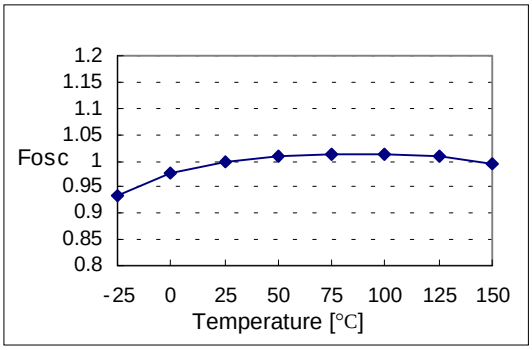


Figure 1. Operating Frequency

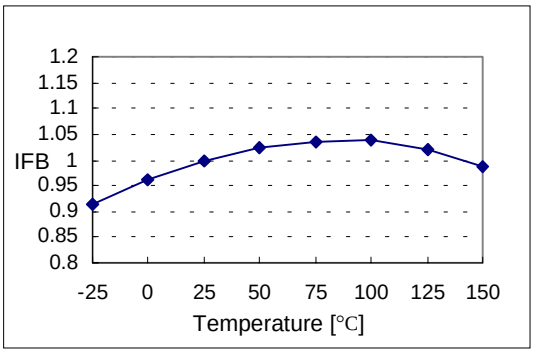


Figure 2. Feedback Source Current

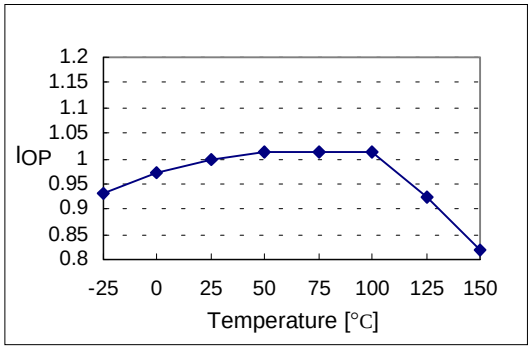


Figure 3. Operating Supply Current

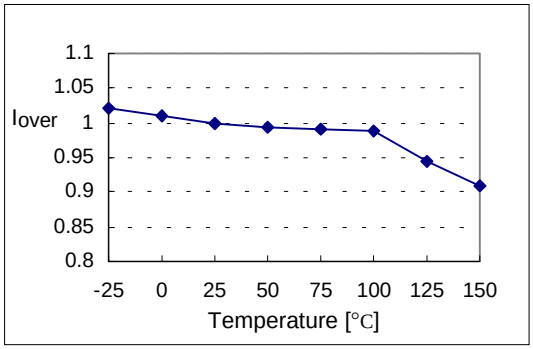


Figure 4. Peak Current Limit

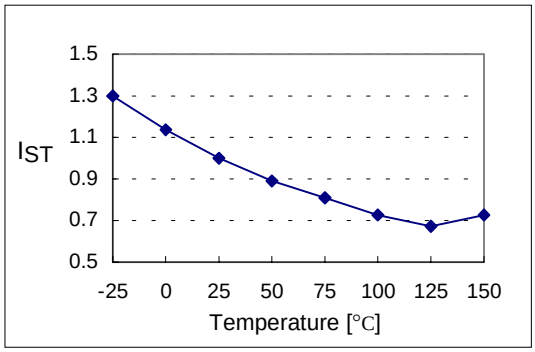


Figure 5. Start up Current

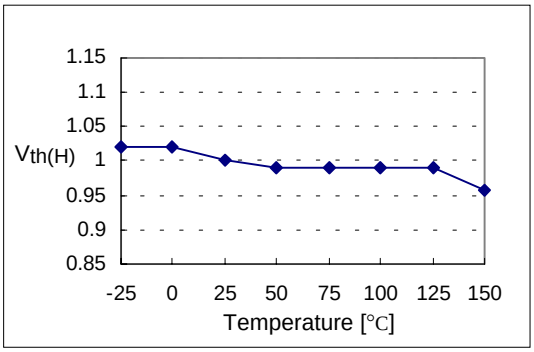


Figure 6. Start Threshold Voltage

Typical Performance Characteristics (Continued)

(These characteristic graphs are normalized at Ta = 25°C)

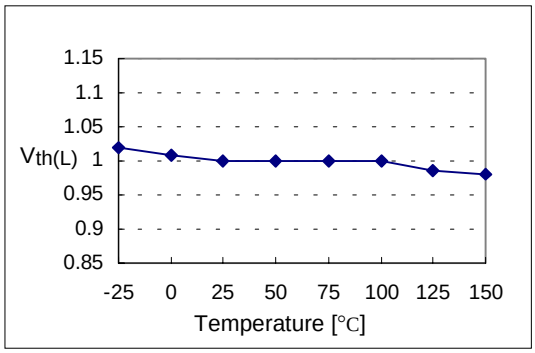


Figure 7. Stop Threshold Voltage

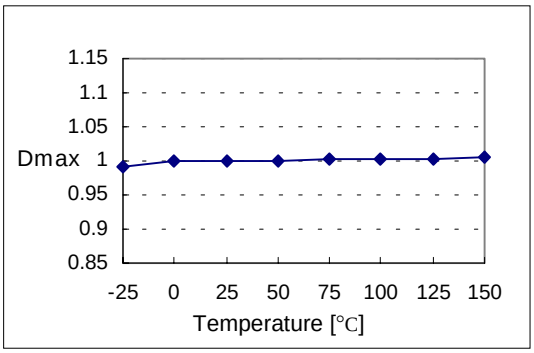


Figure 8. Maximum Duty Cycle

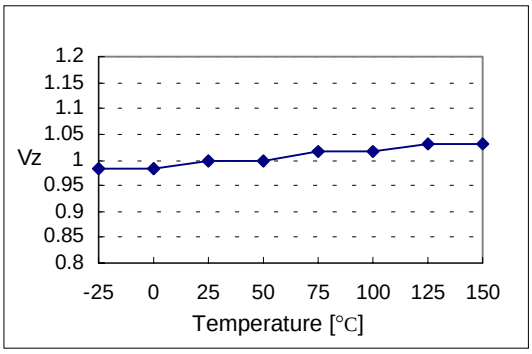


Figure 9. V_{CC} Zener Voltage

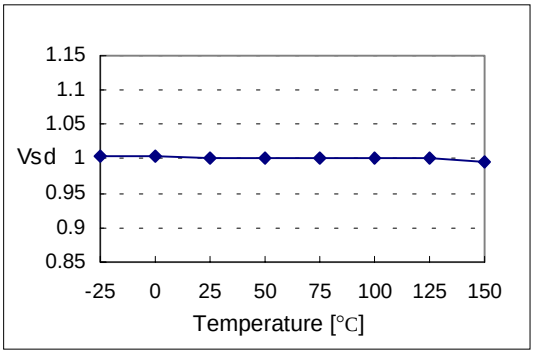


Figure 10. Shutdown Feedback Voltage

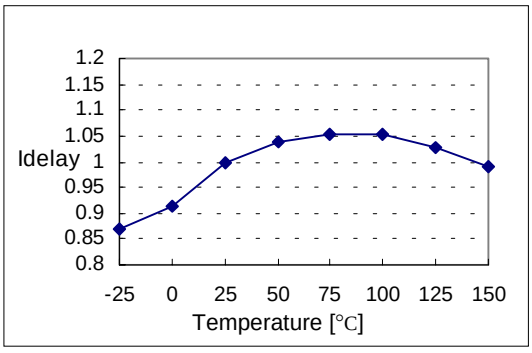


Figure 11. Shutdown Delay Current

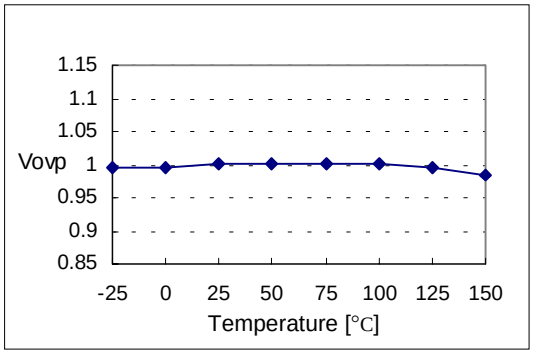


Figure 12. Over Voltage Protection

Typical Performance Characteristics (Continued)

(These characteristic graphs are normalized at Ta = 25°C)

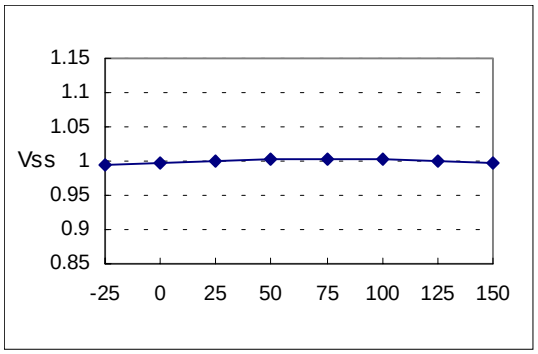


Figure13. Soft Start Voltage

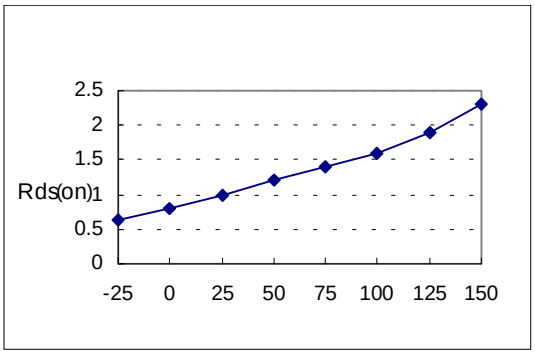
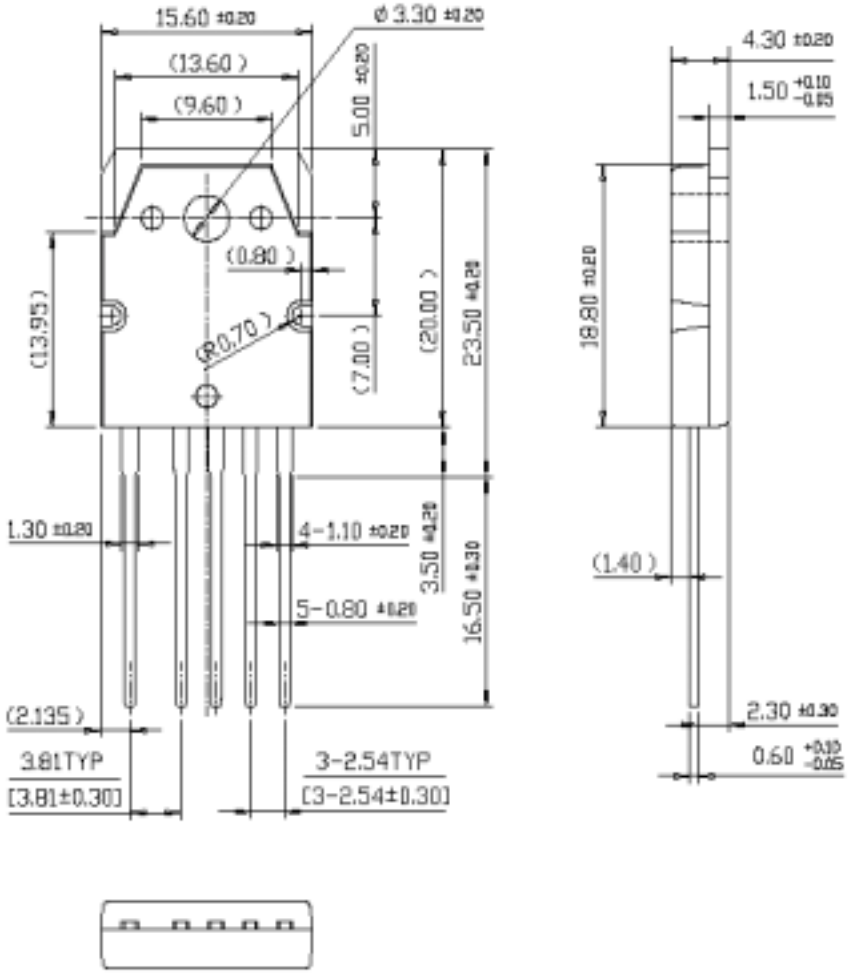


Figure 14. Static Drain-Source on Resistance

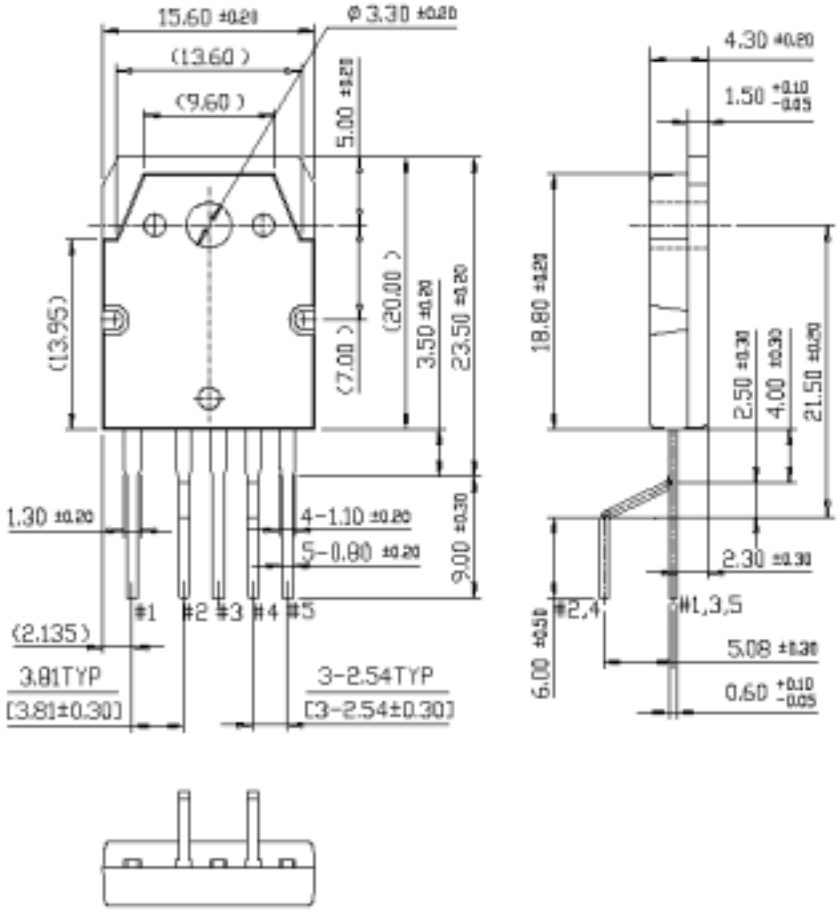
Package Dimensions

TO-3P-5L



Package Dimensions (Continued)

TO-3P-5L (Forming)



Ordering Information

Product Number	Package	Rating	Operating Temperature
KA2S1265-TU	TO-3P-5L	650V,12A	-25°C to +85°C
KA2S1265-YDTU	TO-3P-5L(Forming)		

TU : Non Forming Type
YDTU : Forming Type

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF THE PRESIDENT OF FAIRCHILD SEMICONDUCTOR CORPORATION. As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury of the user.
2. A critical component in any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.