

KA301A

Single Operational Amplifier

Features

- Short circuit protection and latch free operation
- Slew rate of $10V/\mu s$ as a summing amplifier
- Class AB output provides excellent linearity
- Low bias current

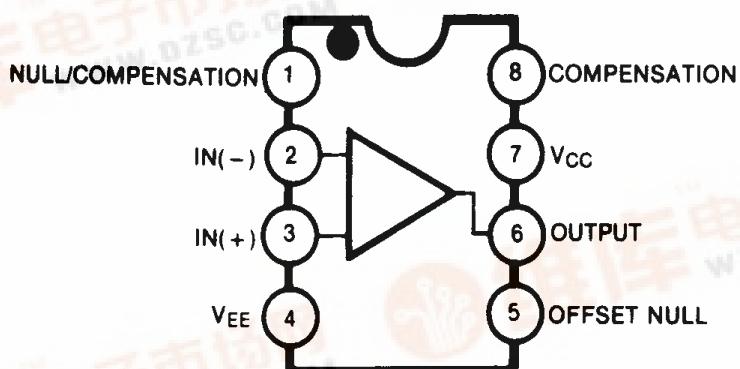
Description

The KA301A is a general purpose operational amplifier which is externally phase compensated, permit a choice of operation for optimum high frequency performance at a selected gain: unity gain compensation can be obtained with a single capacitor.

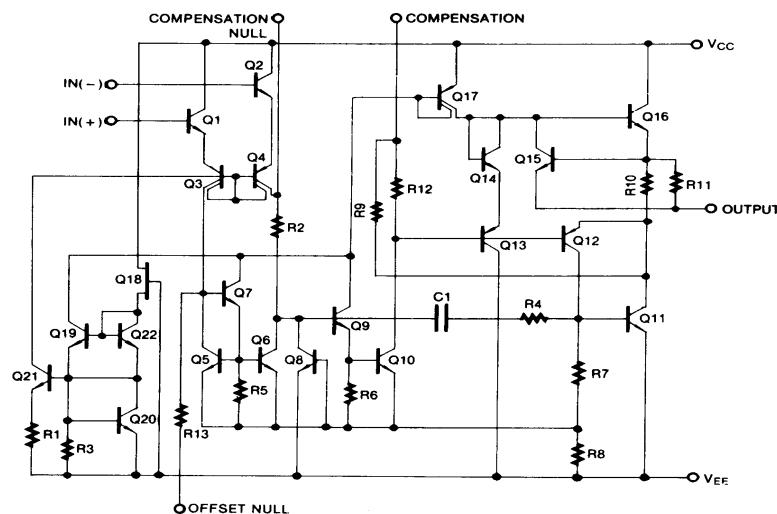
8-DIP



Internal Block Diagram



Schematic Diagram



Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Supply Voltage	V _{CC}	±18	V
Differential Input Voltage	V _{I(DIFF)}	30	V
Input Voltage	V _I	±15	V
Output short Circuit Duration	-	Continuous	-
Power Dissipation	P _D	500	mW
Operating Temperature Range	T _{OPR}	0 ~ +70	°C
Storage Temperature Range	T _{STG}	- 65 ~ + 150	°C

Electrical Characteristics

(TA = +25°C, VCC = +15V, VEE = -15V, unless otherwise specified)

Parameter	Symbol	Conditions	KA301A			Unit	
			Min.	Typ.	Max.		
Input Offset Voltage	VIO	$R_S \leq 50\text{K}\Omega$	-	2.0	7.5	mV	
			Note 1	-	-	10	
Input Offset Current	IIO		-	4.5	50	nA	
			Note 1	-	-	70	
Input Bias Current	IBIAS		-	60	250	nA	
			Note 1	-	-	300	
Supply Current	ICC	VCC = $\pm 20\text{V}$	-	-	-	mA	
		VCC = $\pm 15\text{V}$	-	2.0	3.0	mA	
		VCC = $\pm 20\text{V}$, TA = TA(MAX)	-	-	-	mA	
Large Signal Voltage Gain	GV	VCC = $\pm 15\text{V}$, $R_L \geq 2\text{K}\Omega$, VO(P-P) = $\pm 10\text{V}$	25	160	-	V/mV	
			Note 1	15	-	V/mV	
Average Temperature Coefficient of Input Offset Voltage (NOTE2)	$\Delta V_{IO}/\Delta T$	Note 1	-	6.0	30	$\mu\text{V}/^\circ\text{C}$	
Average Temperature Coefficient of Input Offset Current (NOTE2)	$\Delta I_{IO}/\Delta T$	25 °C ≤ TA ≤ TA(MAX)	-	0.01	0.3	nA/°C	
		TA(MIN) ≤ TA ≤ 25 °C	-	0.02	0.6	nA/°C	
Input Voltage Range	VI(R)	VCC = $\pm 20\text{V}$	Note 1	-	-	V	
		VCC = $\pm 15\text{V}$	Note 1	± 12	-	V	
Common-Mode Rejection Ratio	CMRR	$R_S \leq 50\text{K}\Omega$	Note 1	70	95	-	dB
Power Supply Rejection Ratio	PSRR	$R_S \leq 50\text{K}\Omega$	Note 1	70	100	-	dB
Output Voltage Swing	VO(P.P)	VCC = $\pm 15\text{V}$	RL = 10KΩ	± 12	± 14	-	V
			RL = 2.0KΩ	± 10	± 13	-	V
Input Resistance (NOTE2)	RI	-	-	0.5	2.0	-	MΩ

Note:

1. KA301A: $0 \leq TA \leq +70\text{ }^\circ\text{C}$
2. Guaranteed by design.

Typical Performance Characteristics

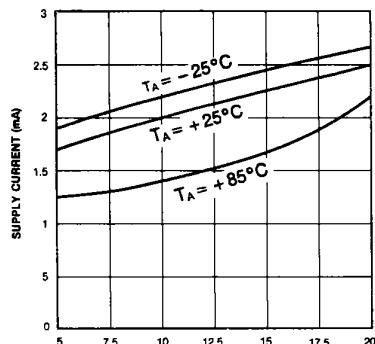


Figure 1. Supply Current

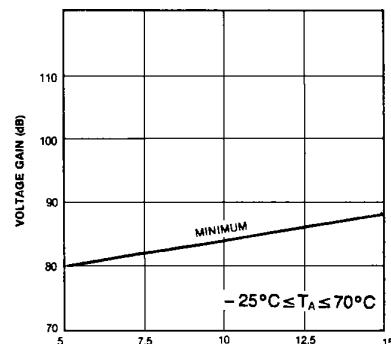


Figure 2. Voltage Gain

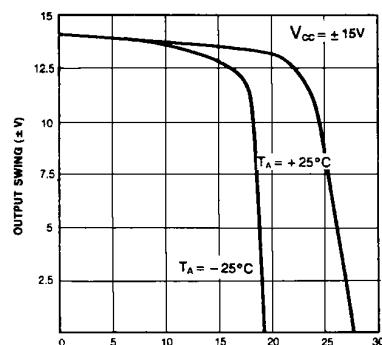


Figure 3. Current Limiting

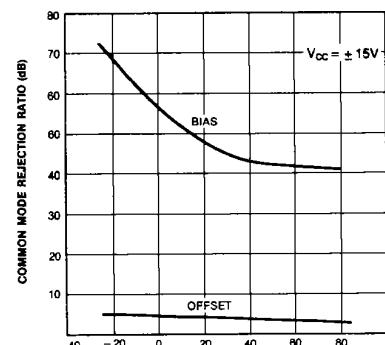


Figure 4. Input Current

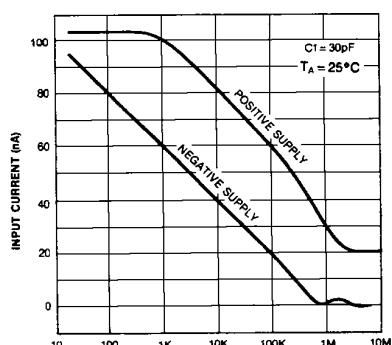


Figure 5. Power Supply Rejection

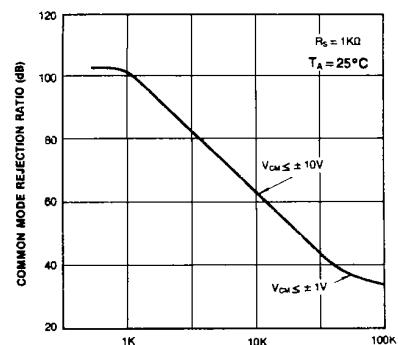


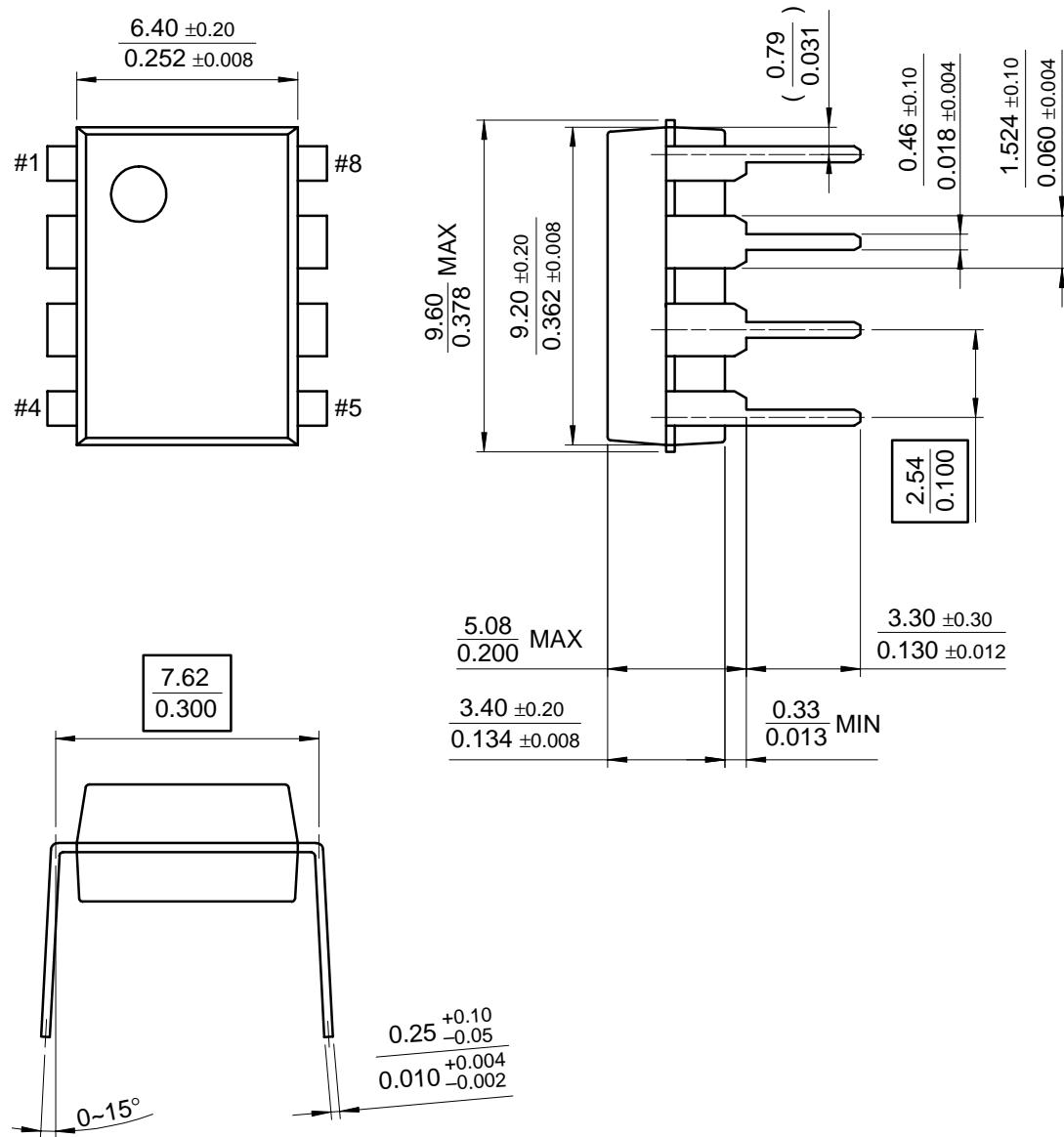
Figure 6. Common Mode Rejection

Mechanical Dimensions

Package

Dimensions in millimeters

8-DIP



Ordering Information

Product Number	Package	Operating Temperature
KA301A	8-DIP	0 ~ + 70 °C

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