

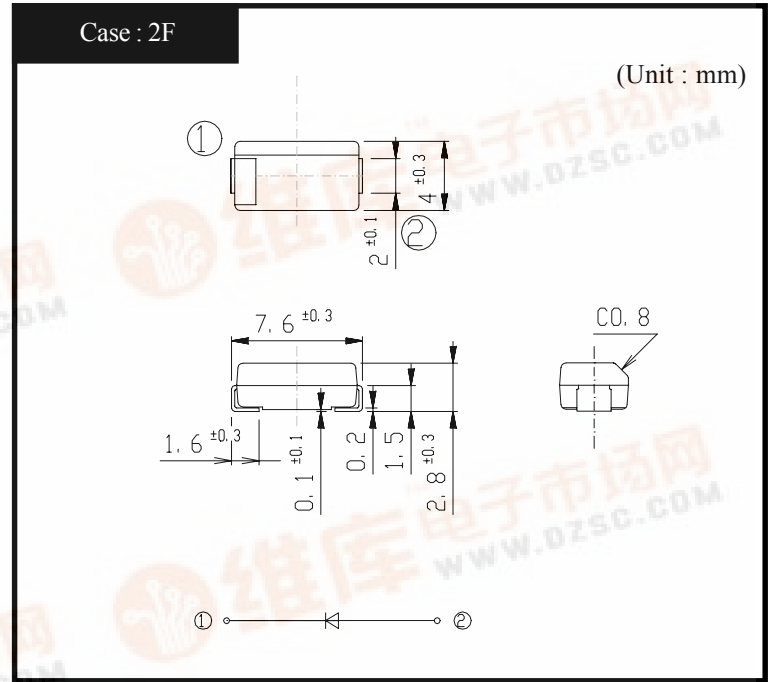
# SHINDENGEN

TSS KP Series

SMD

**KP4L07**

## OUTLINE DIMENSIONS



## RATINGS

### ●Absolute Maximum Ratings

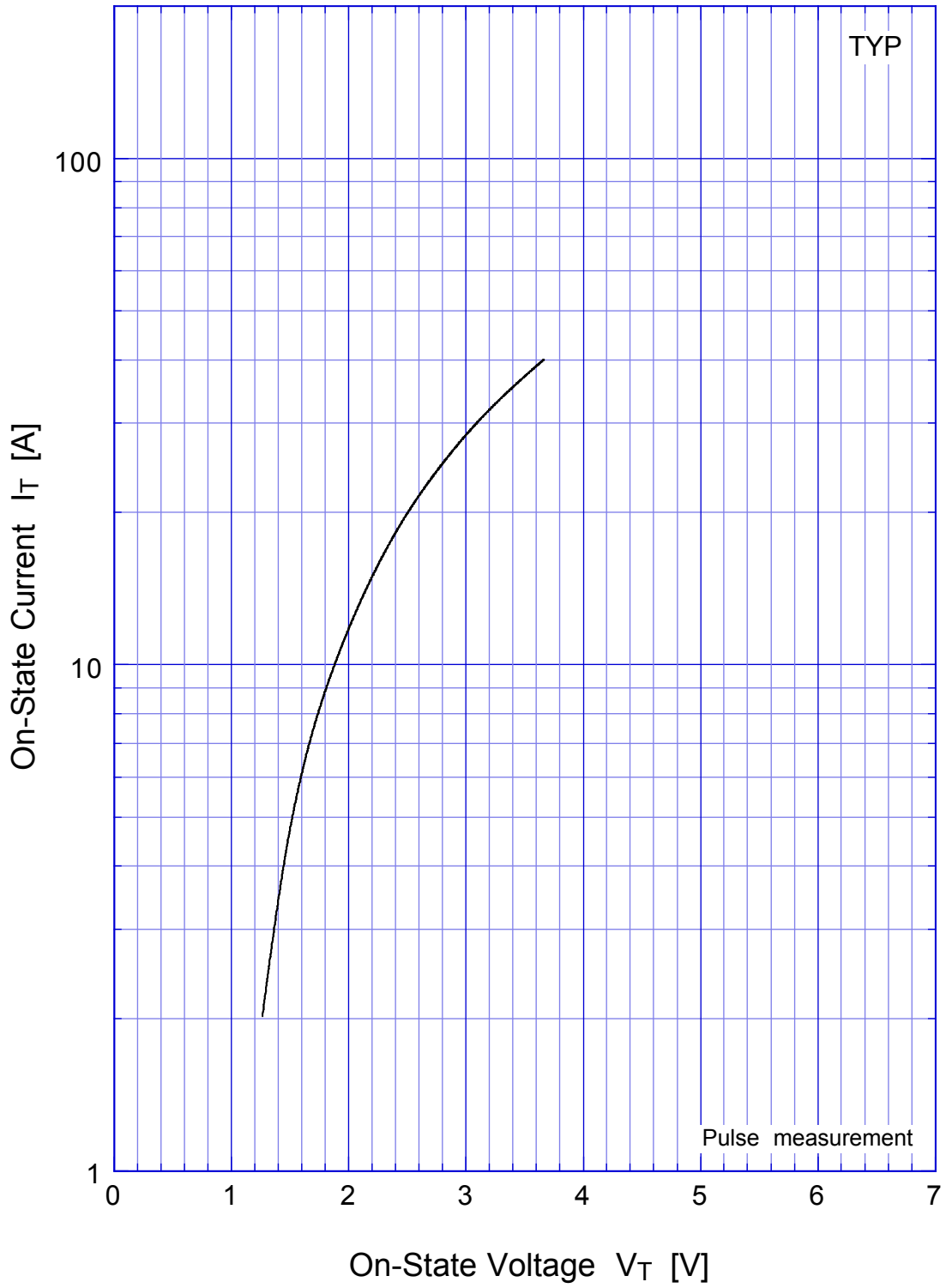
Item	Symbol	Conditions	Ratings	Unit
Storage Temperature	Tstg		-40~125	°C
Junction Temperature	Tj		125	°C
Maximum Off-State Voltage	V <sub>DRM</sub>		58	V
Surge On-State Current	I <sub>TSM</sub>	10/1000 μs, Non-repetitive	40	A
		10/200 μs, Non-repetitive	60	
		8/20 μs, Non-repetitive	150	

### ●Electrical Characteristics (Tl=25°C)

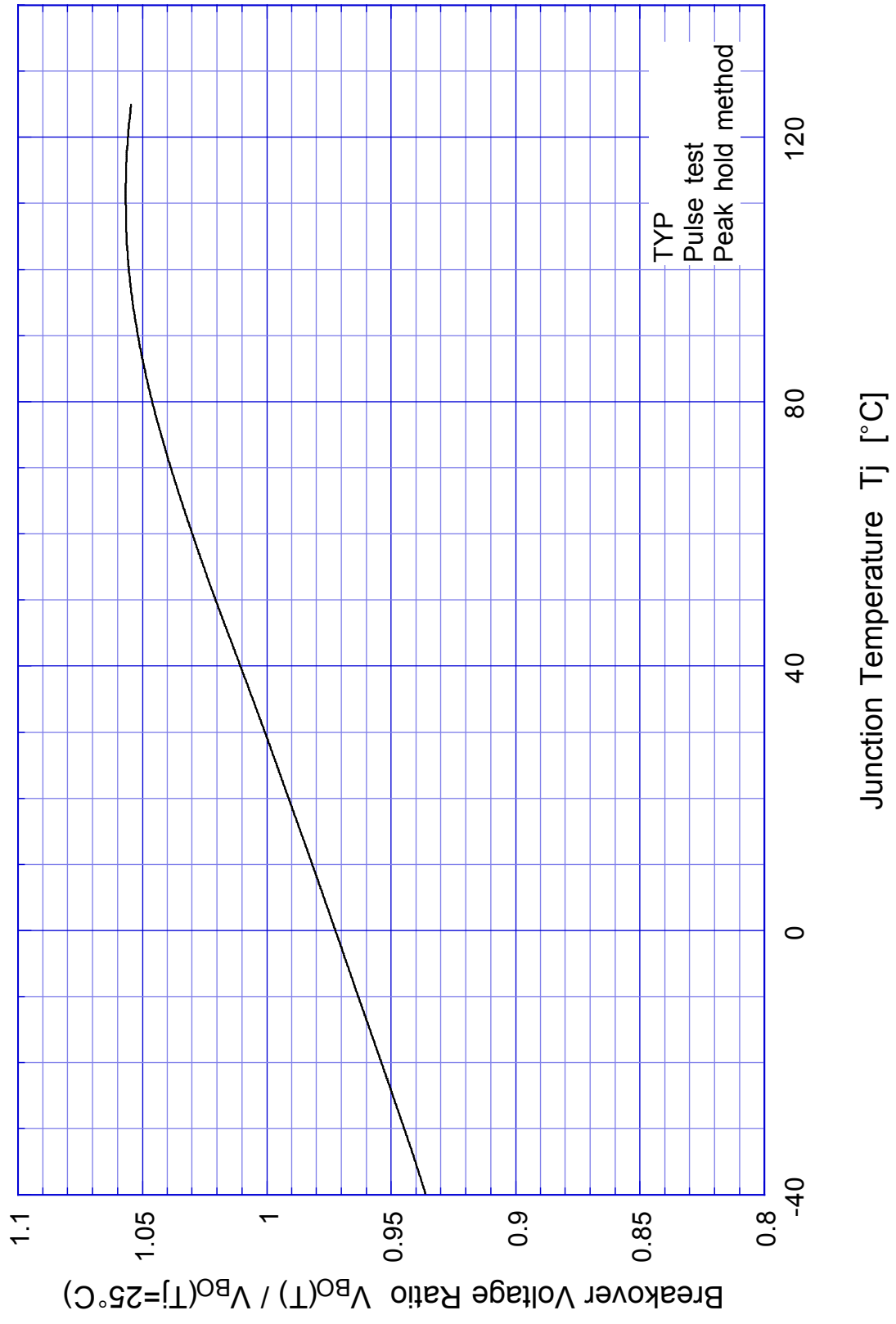
Item	Symbol	Conditions	Ratings	Unit
Breakover Voltage	V <sub>BO</sub>	Pulse measurement (Peak hold)	Min 65	V
Off-State Current	I <sub>DRM</sub>	V <sub>D</sub> = V <sub>DRM</sub>	Max 10	μA
Holding Current	I <sub>H</sub>	Pulse measurement	Min 100	mA
On-State Voltage	V <sub>T</sub>	I <sub>T</sub> = 2A	TYP 1.25	V
Junction Capacitance	C <sub>j</sub>	f = 1kHz OSC = 1V, V <sub>D</sub> = 50V	Max 90	pF
Clamping Voltage	V <sub>CL</sub>	dv/dt = 100V/μs	Max 80	V



# KP4L07 On-State Voltage - On-State Current

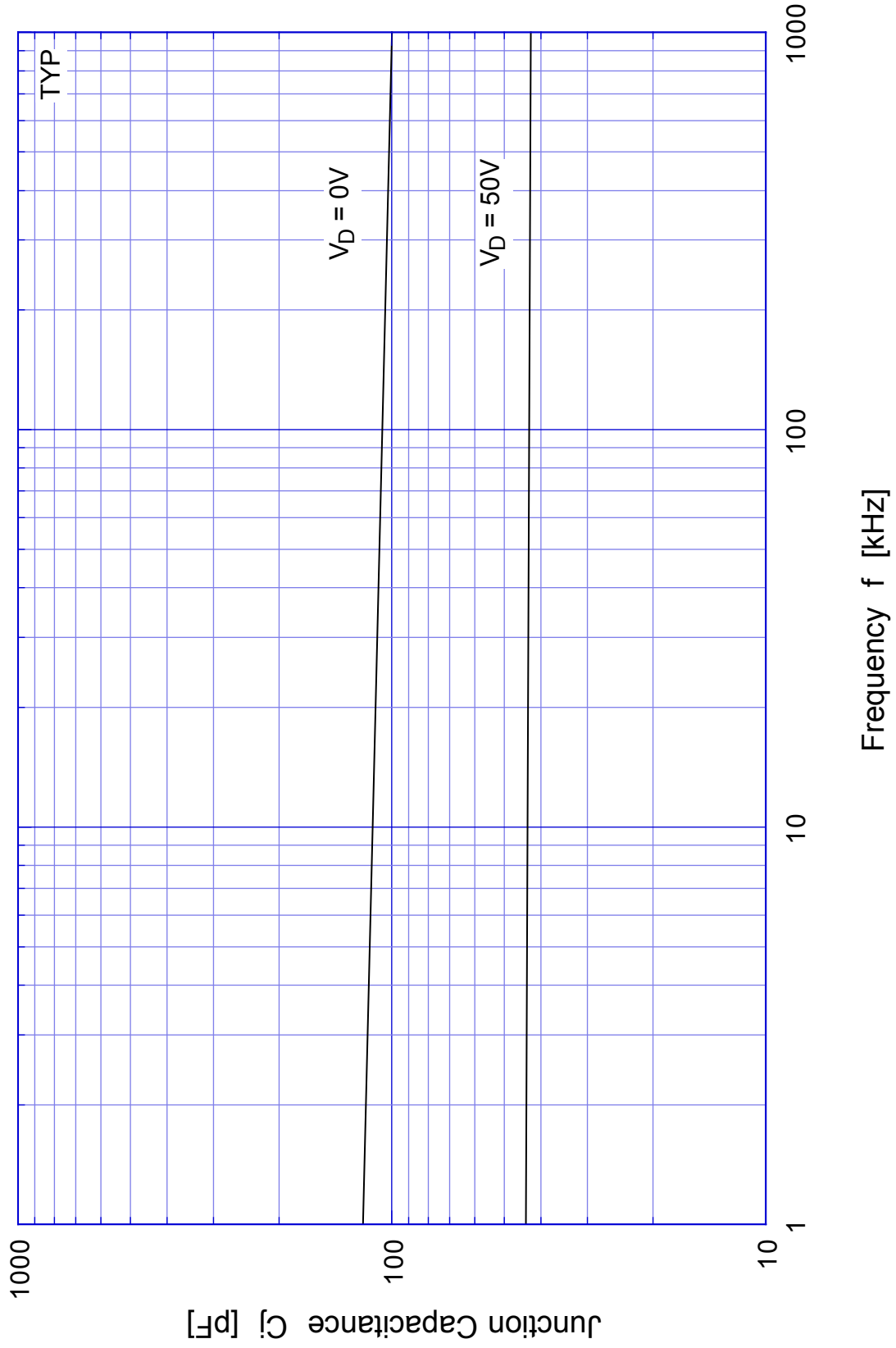


# KP4L07 Breakover Voltage - Junction Temperature

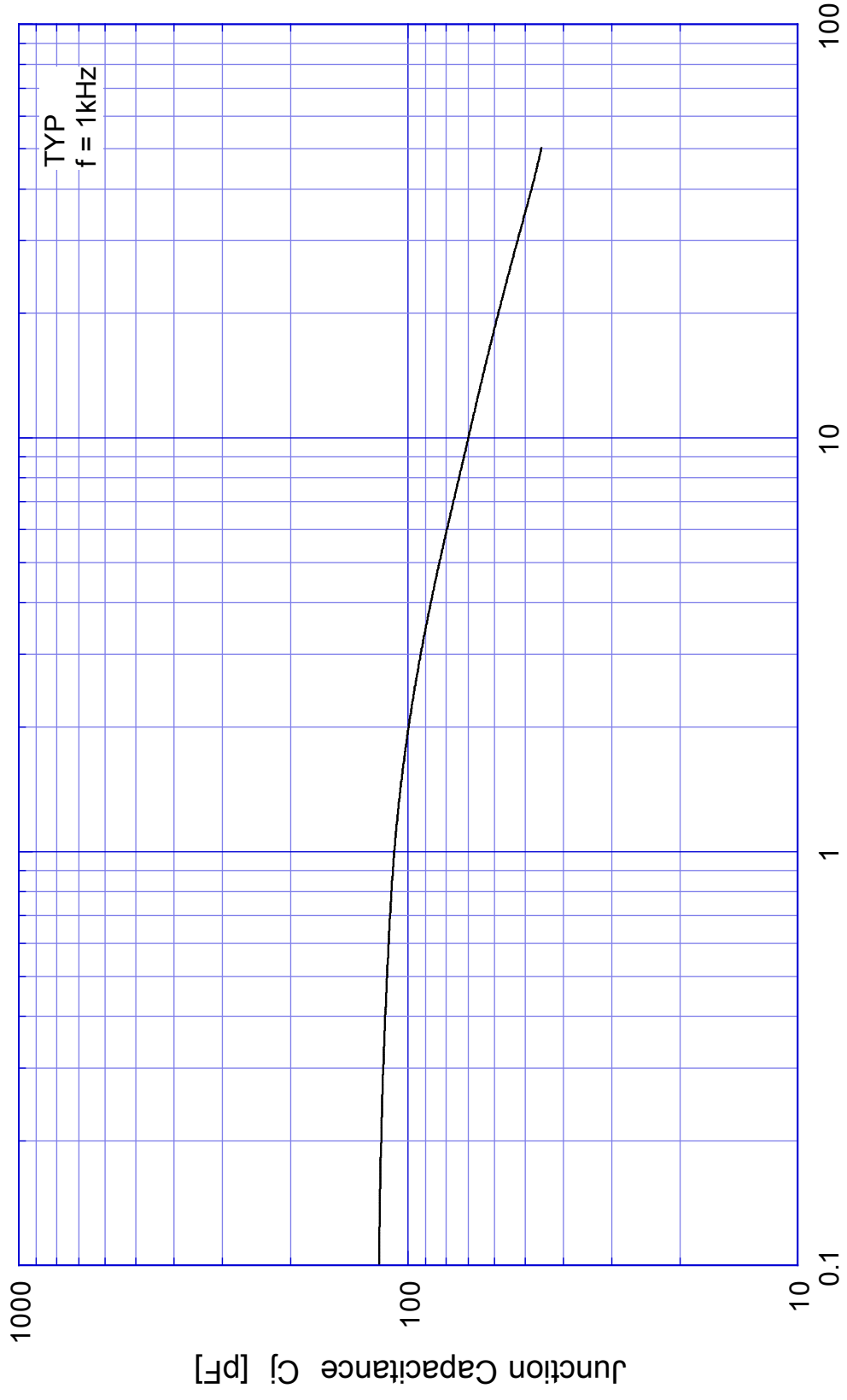


KP4L07

Junction Capacitance



# KP4L07 Junction Capacitance



Off-State Applied Voltage  $V_D$  [V]

# KP4L07 Holding Current - Junction Temperature

