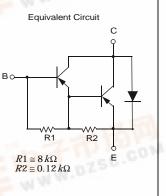




PNP Silicon Darlington Transistor

Absolute Maximum Ratings T_C=25°C unless otherwise noted

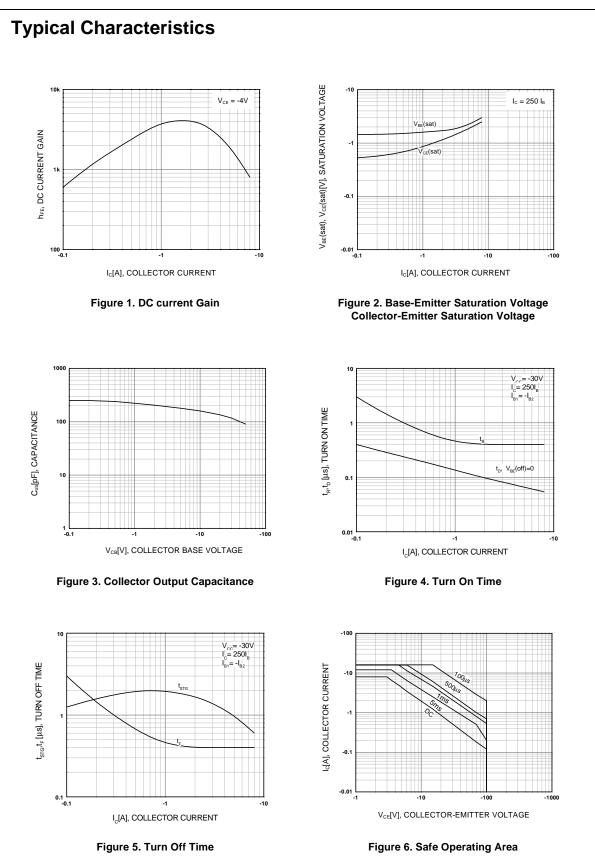
Symbol	Parameter	Value	Units
V _{CBO}	Collector-Base Voltage	- 100	V
V _{CEO}	Collector-Emitter Voltage	- 100	V
V _{EBO}	Emitter-Base Voltage	- 5	V
I _C	Collector Current (DC)	- 8	A
ICP	Collector Current (Pulse)	- 16	A
I _B	Base Current	- 120	mA
P _C	Collector Dissipation (T _C =25°C)	20	W
	Collector Dissipation (T _a =25°C)	1.75	W
TJ	Junction Temperature	150	°C
T _{STG}	Storage Temperature	- 65 ~ 150	°C



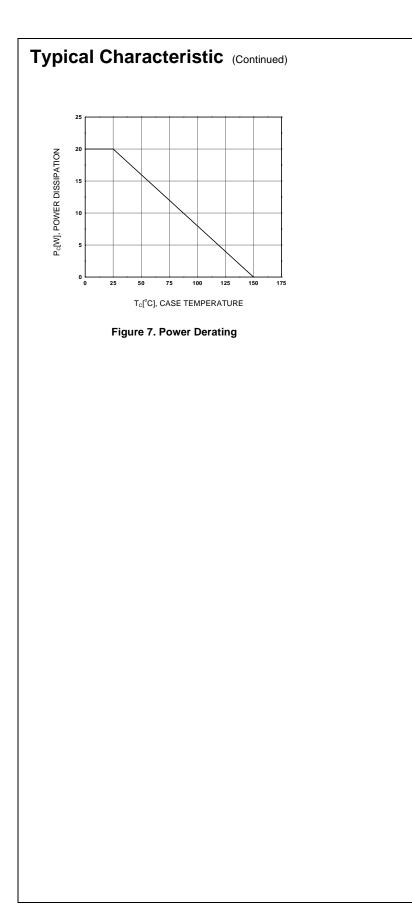
Electrical Characteristics T_C=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Max.	Units
V _{CEO} (sus)	*Collector-Emitter Sustaining Voltage	I _C = - 30mA, I _B = 0	- 100		V
ICEO	Collector Cut-off Current	$V_{CE} = -50V, I_{B} = 0$		- 10	μΑ
I _{CBO}	Collector Cut-off Current	V _{CB} = - 100V, I _E = 0		- 10	μA
I _{EBO}	Emitter Cut-off Current	$V_{EB} = -5V, I_{C} = 0$		- 2	mA
h _{FE}	*DC Current Gain	$V_{CE} = -4V, I_{C} = -4A$ $V_{CE} = -4V, V_{EB} = -8A$	1000 100	12K	C.C
V _{CE} (sat)	*Collector-Emitter Saturation Voltage	$I_{C} = -4A, I_{B} = -16mA$ $I_{C} = -8A, I_{B} = -80mA$	100	- 2 - 4	V V
V _{BE} (sat)	*Base-Emitter Saturation Voltage	$I_{\rm C} = -8A, I_{\rm B} = -80mA$		- 4.5	V
V _{BE} (on)	*Base-Emitter On Voltage	$V_{CE} = -4V, I_{C} = -4A$		- 2.8	V
C _{ob}	Output Capacitance	$V_{CB} = -10V, I_E = 0$ f= 0.1MHz		300	pF

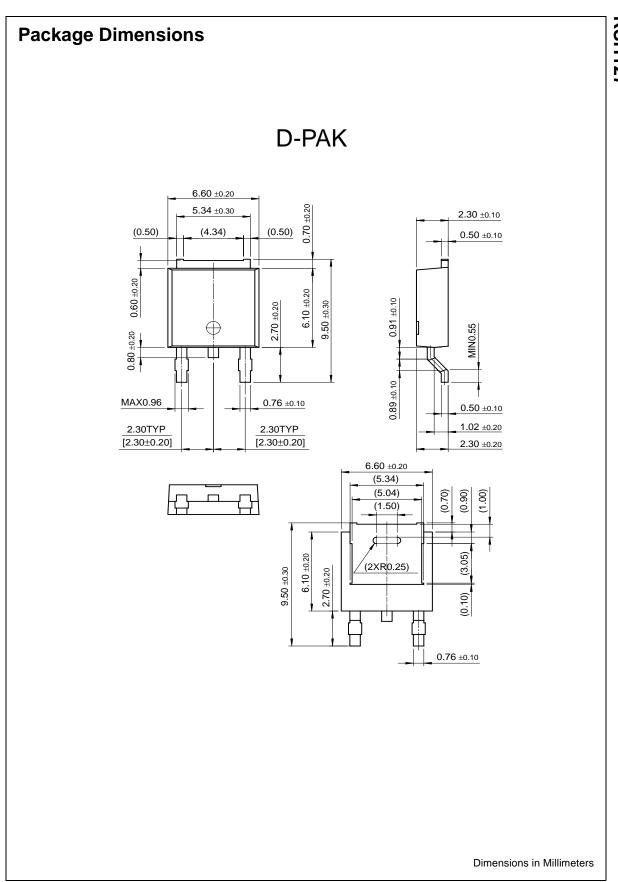
* Pul<mark>se Test: PW</mark>≤300μ<mark>s, Duty C</mark>ycle≤2%



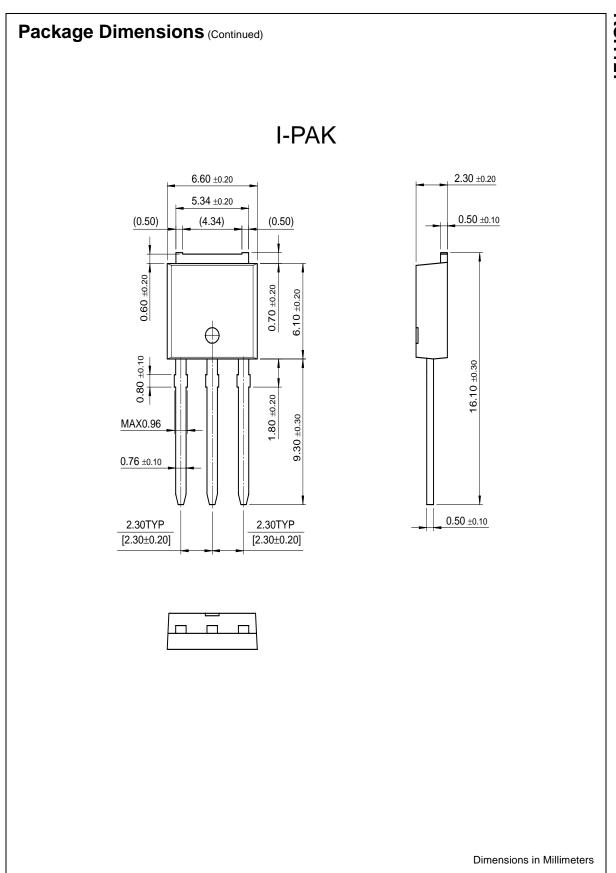
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