

KSR1005

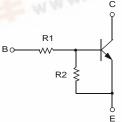
Switching Application (Bias Resistor Built In)

- Switching circuit, Inverter, Interface circuit, Driver Circuit
- Built in bias Resistor (R_1 =4.7 $K\Omega$, R_2 =10 $K\Omega$)
- Complement to KSR2005



1. Emitter 2. Collector 3. Base

Equivalent Circuit



NPN Epitaxial Silicon Transistor

Absolute Maximum Ratings Ta=25°C unless otherwise noted

Symbol	Parameter	Value	Units
V _{CBO}	Collector-Base Voltage	50	V
V _{CEO}	Collector-Emitter Voltage	50	V
V _{EBO}	Emitter-Base Voltage	10	V
I _C	Collector Current	100	mA
Pc	Collector Power Dissipation	300	mW
TJ	Junction Temperature	150	°C
T _{STG}	Storage Temperature	-55 ~ 150	°C

Electrical Characteristics Ta=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BV _{CBO}	Collector-Base Breakdown Voltage	I _C =10μA, I _E =0	50			V
BV _{CEO}	Collector-Emitter Breakdown Voltage	I _C =100μA, I _B =0	50	-80		V
I _{CBO}	Collector Cut-off Current	V _{CB} =40V, I _E =0			0.1	μΑ
h _{FE}	DC Current Gain	V _{CE} =5V, I _C =5mA	30			
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C =10mA, I _B =0.5mA			0.3	V
C _{ob}	Output Capacitance	V _{CE} =10V, I _C =5mA f=1MHz		3.7		pF
f _T	Current Gain Bandwidth Product	V _{CE} =10V, I _C =5mA		250		MHz
V _I (off)	Input Off Voltage	V _{CE} =5V, I _C =100μA	0.3			V
V _I (on)	Input On Voltage	V _{CE} =0.3V, I _C =20mA			2.5	V
R ₁	Input Resistor		3.2	4.7	6.2	ΚΩ
R ₁ /R ₂	Resistor Ratio		0.42	0.47	0.52	

Typical Characteristics

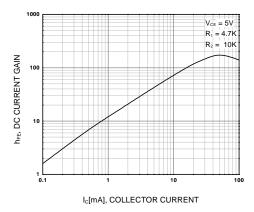


Figure 1. DC current Gain

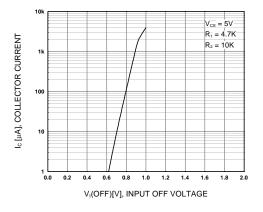


Figure 3. Input Off Voltage

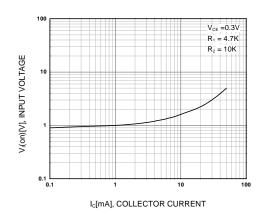


Figure 2. Input On Voltage

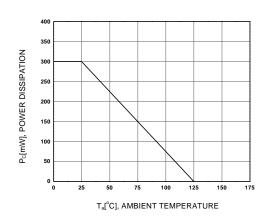
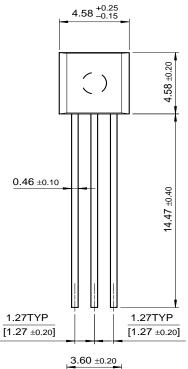


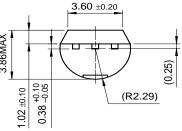
Figure 4. Power Derating

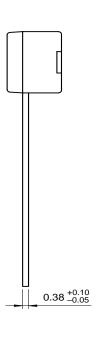
©2002 Fairchild Semiconductor Corporation

Package Dimensions

TO-92







Dimensions in Millimeters

TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

$ACEx^{TM}$	FACT™	ImpliedDisconnect™	PACMAN™	SPM TM
ActiveArray™	FACT Quiet series™	ISOPLANAR™	POP™	Stealth™
Bottomless™	FAST [®]	LittleFET™	Power247™	SuperSOT™-3
CoolFET™	FASTr™	MicroFET™	PowerTrench [®]	SuperSOT™-6
CROSSVOLT™	FRFET™	MicroPak™	QFET™	SuperSOT™-8
DOME™	GlobalOptoisolator™	MICROWIRE™	QS^{TM}	SyncFET™
EcoSPARK™	GTO™	MSX™	QT Optoelectronics™	TinyLogic™
E ² CMOS™	HiSeC™	MSXPro™	Quiet Series™	TruTranslation™
EnSigna™	I^2C^{TM}	OCX^{TM}	RapidConfigure™	UHC™ _
Across the board.	. Around the world.™	OCXPro™	RapidConnect™	UltraFET [®]
The Power Franc	hise™	OPTOLOGIC [®]	SILENT SWITCHER®	VCX™
Programmable Ad	ctive Droop™	OPTOPLANAR™	SMART START™	

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

As used herein:

- 1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.
- 2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

PRODUCT STATUS DEFINITIONS

Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.