## $\square \longrightarrow$ LA1061M <br> Antenna Switching Controller

## Overview

The LA1061M is an antenna switching controller for mobile radio equipment．
The LA1061M uses a number of inputs from the receiver circuitry to select the main antenna or sub－antenna according to signal strength and quality．Weak and strong signals are detected with the S－meter DC voltage and F．E． AGC voltage，respectively．Multi－path distortion is detected from the AC component of the IF output，using the same high－sensitivity counter circuit as in Sanyo＇s earlier LA1060 device．An auxiliary circuit keeps the main antenna selected for a fixed time period when reception conditions outside a moving vehicle are changing rapidly． The LA1061M is available in surface－mount 8 －pin DIPs， facilitating construction of compact equipment．It operates from a single 7 to 12 V power supply．

## Package Dimensions

unit：mm
3111－MFP14S


## Features

－Uses Sanyo＇s proprietary AGC amplifier and detector，providing accurate detection of multi－path distortion．
－High－current Main and Sub－antenna switching outputs．
－Antenna switching frequency limiting circuit．
－On－board comparators for F．E AGC（strong signal）and S－meter DC（weak signal）detection．
－Surface－mount 14－pin MFP．

## Specifications

Maximum Ratings at $\mathrm{Ta}=25^{\circ} \mathrm{C}$

| Parameter | Symbol | Conditions | Ratings | Unit |
| :---: | :---: | :---: | :---: | :---: |
| Maximum supply voltage | $V_{\text {CC }}$ max |  | 14 | V |
| Allowable power dissipation | Pd max |  | 182 | mW |
| Operating temperature | Topr |  | -30 to +80 | ${ }^{\circ} \mathrm{C}$ |
| Storage temperature | Tstg |  | -40 to +125 | ${ }^{\circ} \mathrm{C}$ |
| Maximum flow－out current | 14 | Pin 4 | 1 | mA |
|  | $\mathrm{I}_{5}$ | Pin 5 | 10 | mA |
|  | 16 | Pin 6 | 10 | mA |
|  | I9 | Pin 9 | 2 | mA |
|  | $\mathrm{l}_{10}$ | Pin 10 | 5 | mA |
|  | 112 | Pin 12 | 2 | mA |
| Maximum apply voltage$\text { 拔 }- \text { PDF }$ | $\mathrm{V}_{13}$ | Pin 13 | $\mathrm{V}_{\mathrm{CC}}$ | V |
|  | $\mathrm{V}_{14}$ | Pin 14 | $\mathrm{V}_{\mathrm{CC}}$ | V |

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Operating Conditions at $\mathrm{Ta}=25^{\circ} \mathrm{C}$

| Parameter | Symbol | Conditions | Ratings | Unit |
| :--- | :--- | :--- | :--- | :---: |
| Recommended supply voltage | $\mathrm{V}_{\mathrm{CC}}$ |  | 8 | V |
| Operating voltage range | $\mathrm{V}_{\mathrm{CC}}$ op |  | 7 to 12 | V |

Operation Characteristics at $\mathrm{Ta}=25^{\circ} \mathrm{C}, \mathrm{v}_{\mathrm{CC}}=8 \mathrm{~V}, \mathrm{f}=100 \mathrm{kHz}$ sine wave

| Parameter | Symbol | Conditions | Ratings |  |  | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | min | typ | max |  |
| Current drain | ICC | No input, $\mathrm{I}_{5}$ and $\mathrm{I}_{6}$ are not included. | 4.5 | 7 | 12 | mA |
| Pin 5 'H'-level voltage | $\mathrm{V}_{5}$ | $1 \mathrm{k} \Omega$ to ground | 5.0 | 6.0 | 7.0 | V |
| Pin 6 ' H '-level voltage | $\mathrm{V}_{6}$ | $1 \mathrm{k} \Omega$ to ground | 5.0 | 6.0 | 7.0 | V |
| Noise amp gain | $\mathrm{G}_{\mathrm{v}} 1$ | $\mathrm{V}_{\text {IN }}=3 \mathrm{mVrms}, \mathrm{f}=100 \mathrm{kHz}$ | 33 | 36 | 39 | dB |
|  | $\mathrm{Gv}_{\mathrm{v}} 2$ | VIN $=100 \mathrm{mVrms}, \mathrm{f}=100 \mathrm{kHz}$ | 10 | 13 | 16 | dB |
| Noise detection sensitivity | NDS | Noise AGC off | 9 | 12 | 15 | mVrms |
| Noise count number | NCN | $\mathrm{V}_{\text {IN }}=30 \mathrm{mVrms}, \mathrm{f}=100 \mathrm{kHz}$, sine wave |  | 10 |  |  |
| Gate time 1 | tG1 | Noise AGC off | 120 | 150 | 180 | $\mu \mathrm{s}$ |
| Pin 9 voltage | V9 | $\begin{aligned} & \mathrm{V}_{\mathrm{IN}}=100 \mathrm{mVrms}, \mathrm{f}=100 \mathrm{kHz}, \\ & \mathrm{Rg}=300 \mathrm{k} \Omega \end{aligned}$ | 7.0 | 7.7 | 8.0 | V |
| Strong signal comparator threshold | $\mathrm{V}_{\text {th14 }}$ |  | 0.8 | 1.0 | 1.2 | V |
| Weak signal comparator threshold | $V_{\text {th13 }}$ |  | 1.8 | 2.0 | 2.2 | V |
| Gate time 2 | tG2 |  | 2 | 4 | 6 | ms |
| Gate time3 | tG3 |  | 13 | 23 | 40 | ms |
| Switching frequency limit maximum count | HCN |  |  | 15 |  |  |

## Equivalent Circuit Block Diagram



## LA1061M

## Operating Characteristic Test Circuit



## Sample Printed Circuit Pattern



## Functional Description

## General Operation

The LA1061M has two inputs for weak and strong signal detection, and one for multi-path distortion detection. It has two outputs for Main and Sub-antenna selection, one of which is selected according to the state of the inputs. Both outputs can directly drive an antenna switching pin diode.

## Signal Strength Detection

Each signal strength detection input has a built-in comparator. The weak signal comparator (pin 13) is driven by the S-meter DC voltage and has a threshold of approximately 2 V . The antenna switching logic selects the Main antenna when the voltage on this pin is lower than the threshold, regardless of the state of the other inputs.
The strong signal detector (pin 14) is driven by the F.E. AGC voltage and has a threshold of approximately 1 V . The antenna switching logic selects the Sub-antenna if the voltage on this pin is lower than the threshold, the weak signal comparator is off, and the multi-path distortion detector is not already on.

## Multi-path Distortion Detection

The IF output signal is high-pass filtered and the resulting noise signal input through a coupling capacitor to pin 1, the LA1061M amplifies this signal and applies AGC to it. The AGC amplifier is designed to detect multi-path distortion without amplifying noise due to a weak IF signal. The number of noise pulses within the period set by the time constant on pin 4 is counted. If it exceeds a certain limit, and the strong signal detector is not already on, the antenna switching logic selects the Main antenna.

## Switching Frequency Detection

The LA1061M counts the number of antenna changes within the time interval set by the time constant on pin 12. The internal D/A converter outputs a current on pin 11, which is converted to a voltage by the resistor connected to this pin. If this voltage exceeds a certain value, that is, the switching frequency is too high, the Main antenna is selected for the period set by the time constant on pin 10. The Main antenna is selected for this time regardless of the state of the other inputs.

Pin Description
Unit (resistance : $\Omega$, capacitance : F)

| Pin No. | Function | External circuits | Notes |
| :---: | :---: | :---: | :---: |
| 1 | Noise input | Isolate noise components present in the IF output signal with a high-pass filter, and input via a decoupling capacitor. | The input impedance of the LA 1061 M varies with input level. The minimum value is $10 \mathrm{k} \Omega$. |

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Unit (resistance : $\Omega$, capacitance : F)

| Pin No. | Function | External circuits | Notes |
| :---: | :---: | :---: | :---: |
| 2 | H.P.F |  | Highpass filter |
| 3 | AGC |  |  |
| 4 | Mono-stable multi-vibrator 1 | This time constant sets the count gate time |  |
| $\begin{aligned} & 5 \\ & 6 \end{aligned}$ | Antenna switching outputs |  | Pin 6 is for MAIN, pin 5 is for SUB. |
| 7 | GND |  |  |
| 8 | $\mathrm{V}_{\mathrm{CC}}$ |  |  |

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Unit (resistance : $\Omega$, capacitance : F)

| Pin No. | Function | External circuits | Notes |
| :---: | :---: | :---: | :---: |
| 9 | AGC <br> Voltage output |  |  |
| 10 | Mono-stable multi-vibrator 3 |  | The LA1061M counts antenna switches for the time interval generated by multi-vibrator 2 . The internal D/A converter outputs a current on pin 11, which is converted to a voltage |
| 11 | D/A converter |  | this pin. If this voltage exceeds a certain value, that is, the switching frequency is too high , multi-vibrator 3 operates to hold the antenna switching outputs at Main for the time set |
| 12 | Mono-stable multi-vibrator 2 |  | by multi-vibrator 3 . |
| 13 | Weak signal comparator |  | The threshold level is set at approximately 2 V . |
| 14 | Strong signal comparator |  | The threshold level is set at approximately 1 V . |

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