



Monolithic Linear IC

LA1177

Electronic Tuning-Use FM Front End for Car Radio, Home Stereos

Overview

- The LA1177 is an FM front end IC for use in car radio, home stereo applications. It requires fewer external parts. The on-chip oscillator and oscillation buffer facilitate designing of electronic tuning sets.

Features

- Wide-band AGC circuit (Improvement in intermodulation, cross modulation characteristics).
- On-chip local oscillation buffer (For electronic tuning).

Functions

- Oscillator, oscillation buffer.
- Mixer.
- Wide-band AGC circuit.
- IF amplifier.

Specifications

Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V _{CC} max	Pins 2, 9	10	V
Allowable power dissipation	P _d max	T _a ≤70°C	440	mW
Operating temperature	T _{op}		-20 to +70	°C
Storage temperature	T _{stg}		-40 to +125	°C

Operating Conditions at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Recommended supply voltage	V _{CC}		8	V
Operating voltage range	V _{CC} op		8 to 9	V

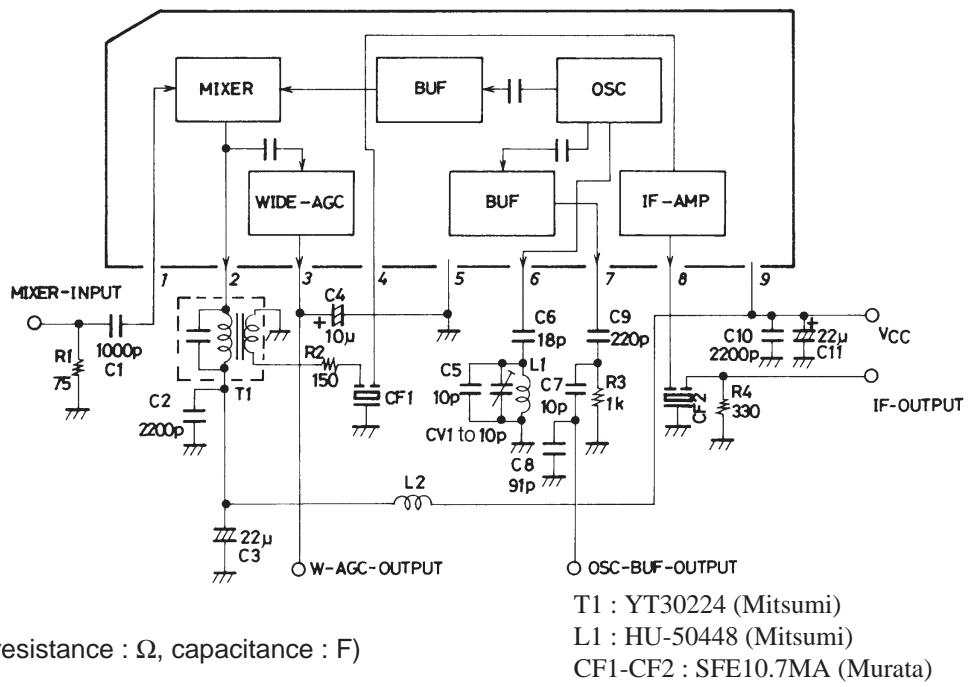
Electrical Characteristics at Ta=25°C, V_{CC}=8V, fin=88MHz

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Quiescent current	I _{CC0}	No input	21	26	31	mA
AGC high-level voltage	V _{AGC-H}	V _{IN} =0dB μ	7.7	8.0		V
AGC low-level voltage	V _{AGC-L}	V _{IN} =100dB μ		0.07	0.3	V
AGC mixer input voltage	V _{i AGC}	V _{AGC} ≤2V, Pin 3	73	80	87	dB μ
IF saturation output voltage	V _{IF-max}	V _{IN} =1.0dB μ	108	112	116	dB μ
Input limiting voltage	V _{i lim}		76	83	90	dB μ
Voltage gain	V _G	V _{IN} =65dB μ	88	92	96	dB μ
Local OSC output voltage	V _{OSC}	No input, 75Ω termination	80	84	88	dB μ



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Evaluation Circuit and Internal Equivalent Circuit Block Diagram



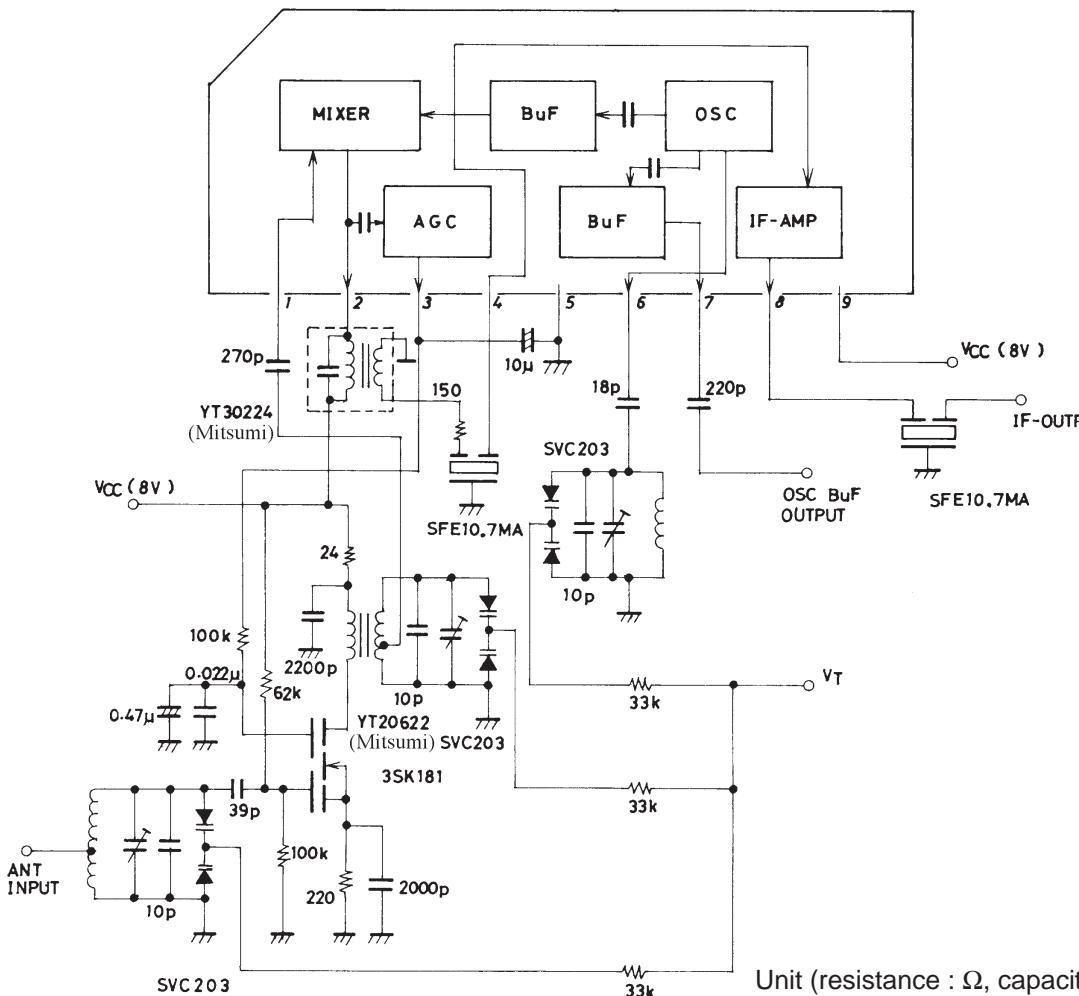
Typical Voltage on Each Pin

Pin No.	Typical voltage	Description	Remarks
1	2.7V	Mixer input	
2	8.0V	Mixer output	
3	8.0V	AGC input	No input
4	2.0V	IF input	
5	0V	GND	
6	4.9V	Oscillator base terminal	
7	1.4V	Oscillation buffer output	
8	4.4V	IF output	
9	8.0V	V _{CC}	

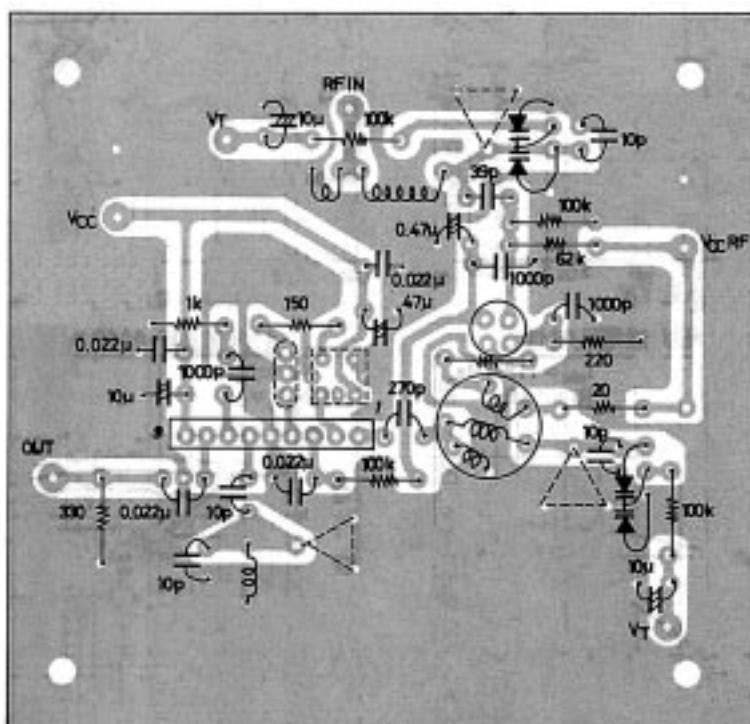
Note : Extreme caution should be exercised when applying voltage across pin 9 (+) and other pins as dielectric breakdown may occur.

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Sample Application Circuit



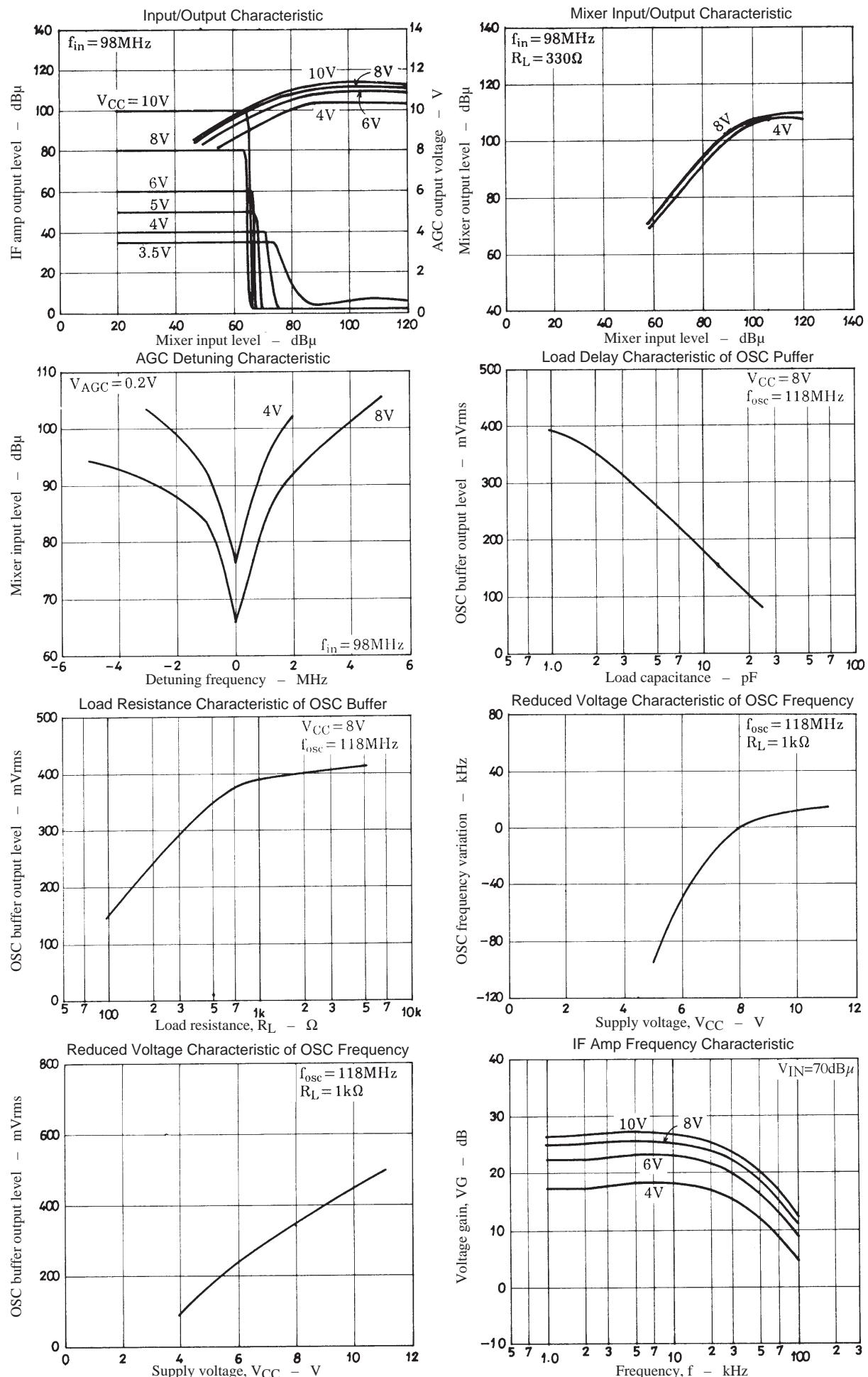
Sample Printed Circuit Pattern



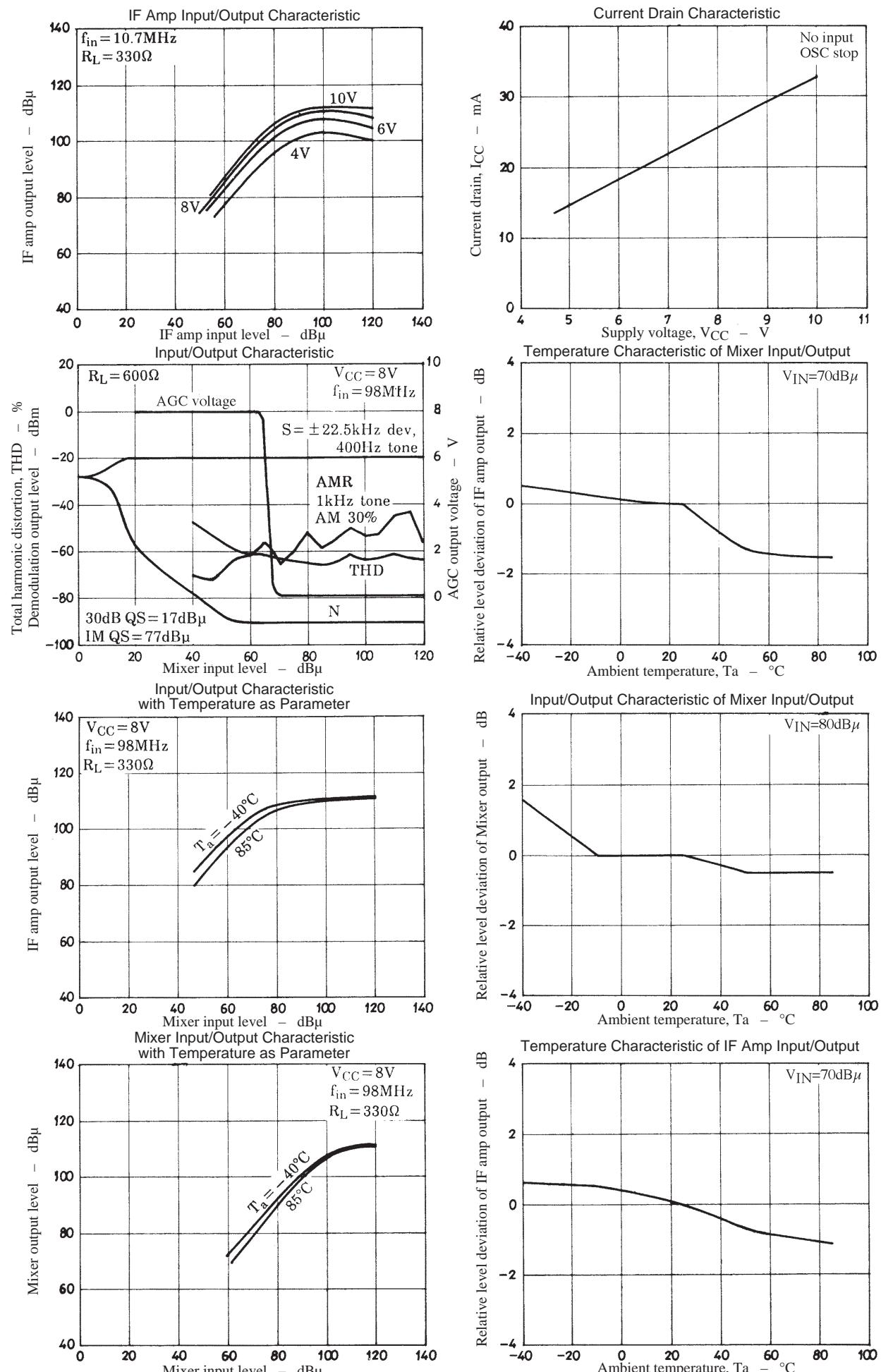
Cu-foiled area $85 \times 82 \text{ mm}^2$

Unit (resistance : Ω , capacitance : F)

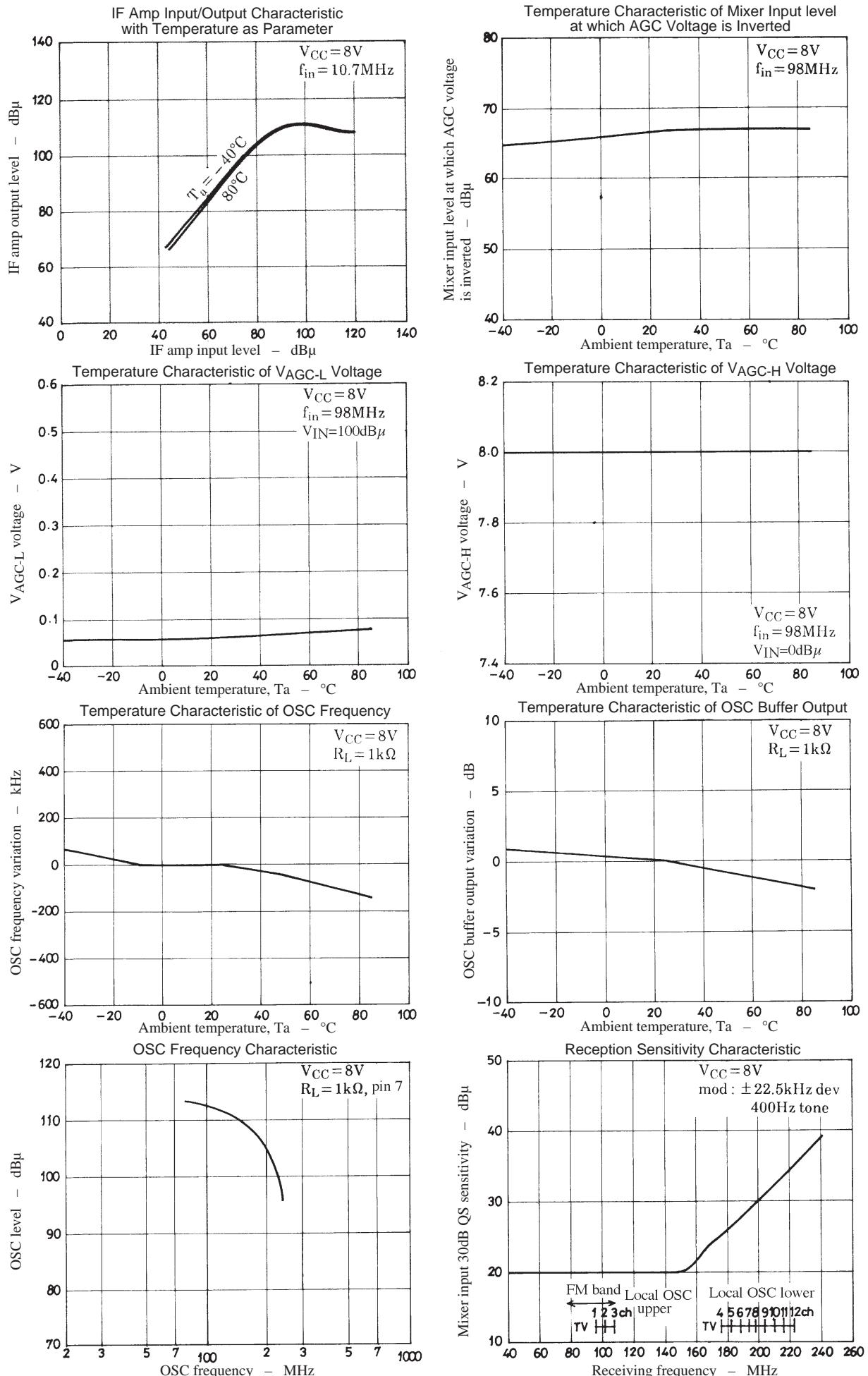
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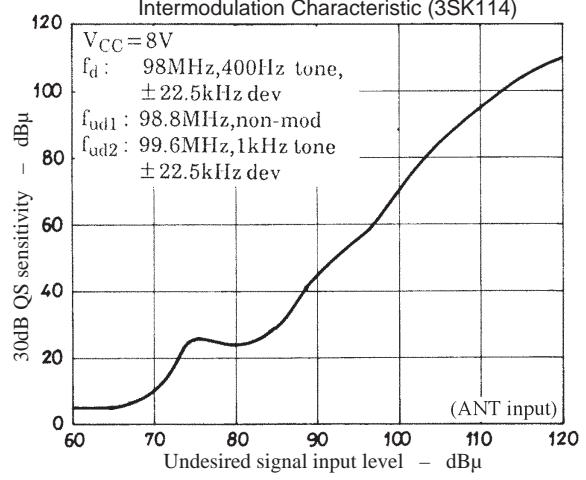
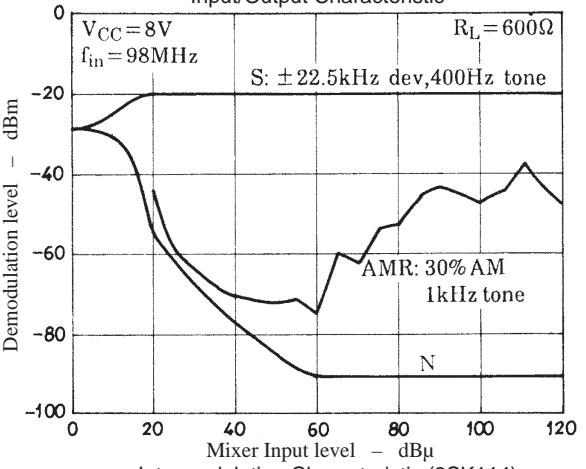
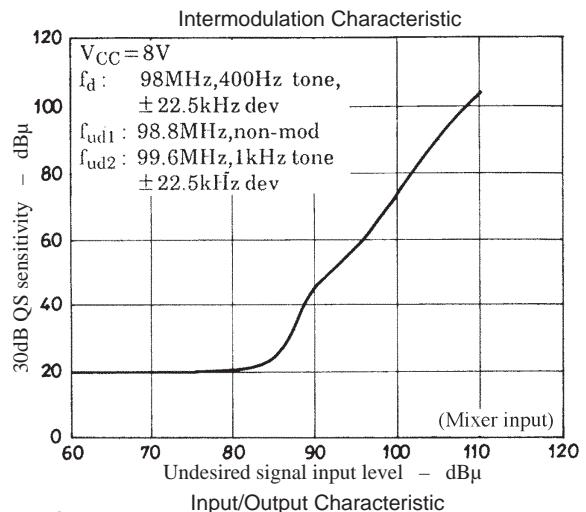
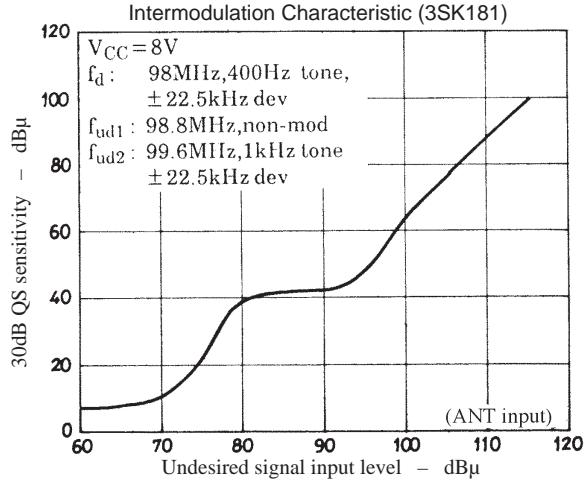
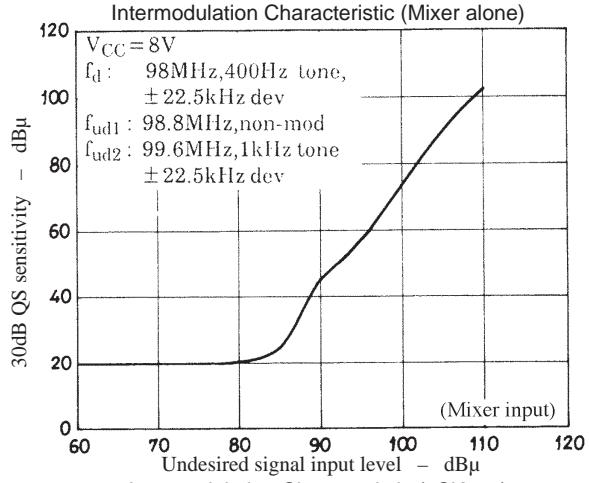
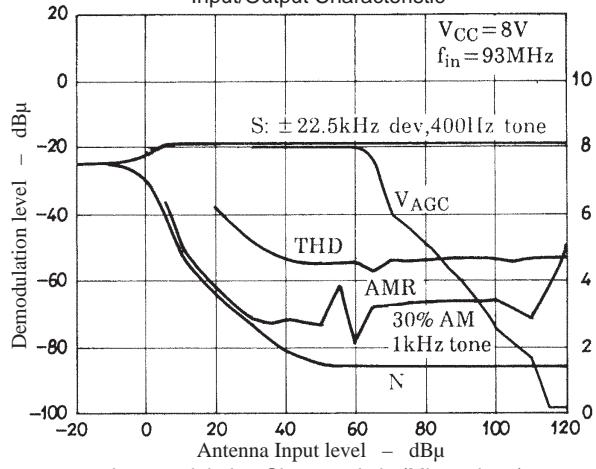
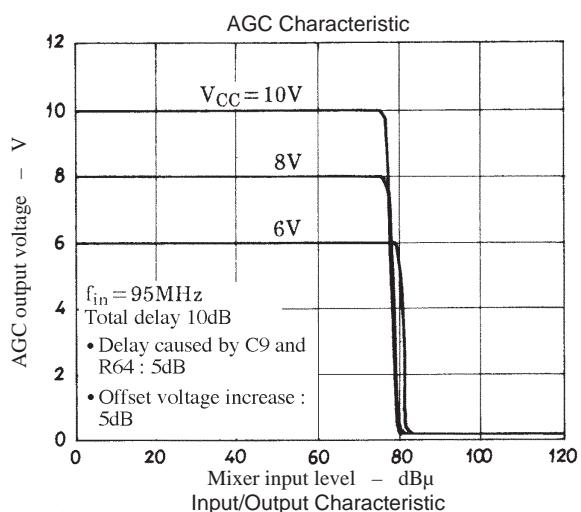
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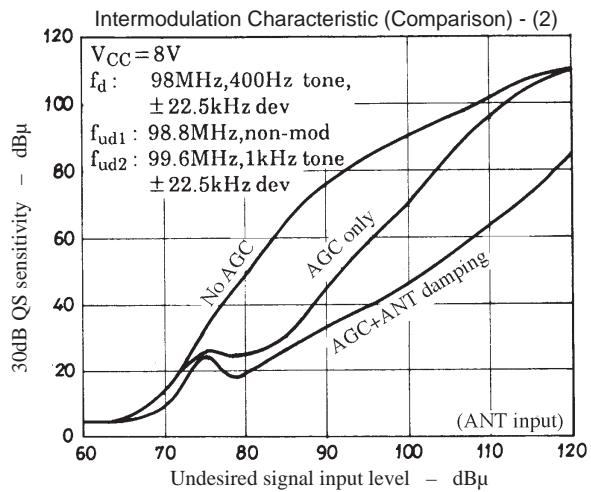
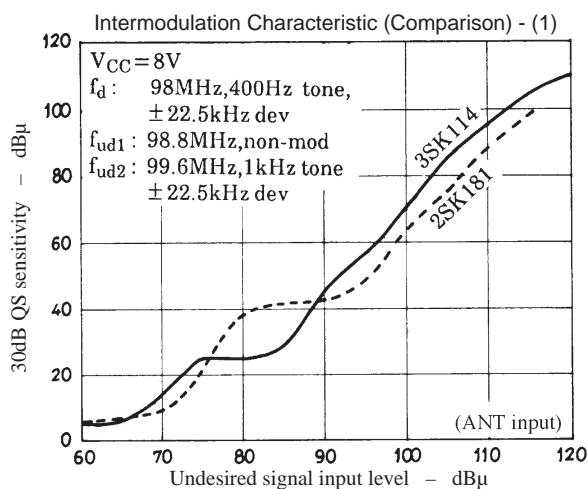


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