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#### Monolithic Linear IC



# SANYO

# Single-Chip Home Stereo IC with Electronic Tuning Support

## Overview

The LA1837 is a single-chip AM/FM IF and MPX IC that supports electronic tuning and was developed for use in home stereo systems. It is optimal for use in automatic station selection systems that use the SD and IF counting techniques.

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#### **Functions**

- AM: RF amplifier, mixer, oscillator, IF amplifier, detector AGC, oscillator buffer, S-meter, narrowband SD, IF buffer
- FM IF: IF amplifier, quadrature detector, S-meter, Scurve detector, IF buffer output
- MPX: PLL stereo decoder, stereo display, forced mono, VCO stop, post amplifier, audio muting, adjacent channel interference rejection function

#### **Features**

- On-chip MPX VCO (no external components required)
- Adjacent channel interference rejection function
- (third and fifth order)
- Supports both the SD and the IF counting techniques.
- The AM and FM SD sensitivities can be set independently.
- The AM and FM output levels can be set independently.
- Improved AM practical sensitivity and high-input distortion characteristics

## **Specifications**

PDF

#### Maximum Ratings at $Ta = 25^{\circ}C$

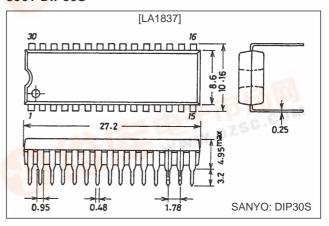
Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V <sub>CC</sub> max		12	V
Allowable power dissipation	Pd max	Ta ≤ 70°C	550	mW
Operating temperature	Topr		-20 to + 70	°C
Storage temperature	Tstg		-40 to +125	°C

#### Operating Conditions at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Recommended supply voltage	V <sub>CC</sub>		9	V
Operating voltage range	V <sub>CC</sub> op		7 to 11	V

# Package Dimensions

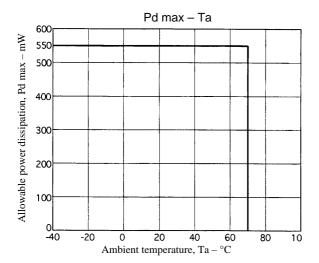
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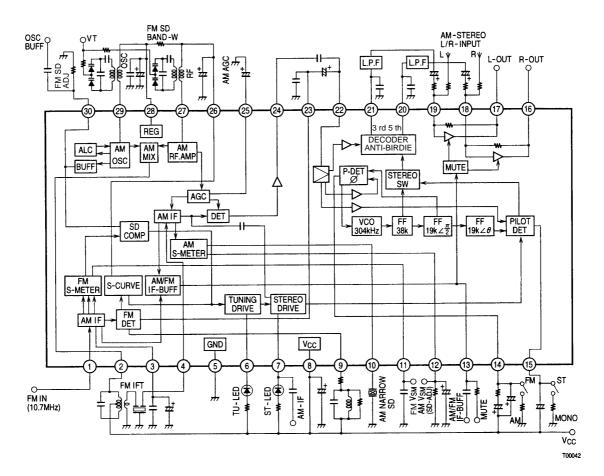
# Electrical Characteristics at Ta = 25 $^{\circ}C,$ V\_{CC} = 9 V, in the specified Test Circuit

Deremeter	Queshal	Conditions	Ratings			Linit
Parameter	Symbol Conditions -		min	typ	max	Unit
[FM Mono Characteristics] fc = 10.	7 MHz, fm = 1 k⊦	Iz, with the coil adjusted so that $V_{AFC} - V_{REG} = 0 V$				•
Current drain	I <sub>CCO-FM</sub>	With no input	18	31	44	mA
Demodulation output	V <sub>OFM</sub>	100 dBµ, 100% mod. The pin 16 output	730	1100	1460	mVrms
Channel balance	C.B-mono	100 dBµ, 100% mod. The pin 16 output/pin 17 output	-1.5	0	+1.5	dB
Total harmonic distortion (mono)	THD <sub>FM(1)</sub>	100 dBµ, 100% mod. The pin 16 output		0.3	1.3	%
Total harmonic distortion (mono)	THD <sub>FM(2)</sub>	100 dBµ, 200% mod. The pin 16 output		1.0	5	%
Signal-to-noise ratio	S/N <sub>FM</sub>	100 dBµ, 100% mod. The pin 16 output	72	80		dB
AM rejection ratio	AMR	100 dBµ, AM 30 % mod. The pin 16 output	45	65		dB
Limiting input voltage	–3 dBL.S.	100 dB $\mu$ , 100% mod. Referenced to the output. The input such that the output is down $-3$ dB.	26	32	38	dBµ
LED sensitivity	SD <sub>On-FM</sub>		51	60	69	dBµ
LED bandwidth	SD <sub>BW</sub>	100 dBµ	85	120	170	kHz
IF count buffer output	V <sub>IFBuff-FM</sub>	100 dBµ. The pin 13 output	80	120	160	mVrms
C motor output	V <sub>SM-FM(1)</sub>	0 dBµ. The pin 11 output	0	0.1	0.5	V
S-meter output	V <sub>SM-FM(2)</sub>	100 dBµ. The pin 11 output	3.6	4.3	5.0	V
Muting attenuation	Mute-Att	100 dBµ, 100% mod. The pin 16 output	75	85		dB
[FM Stereo Characteristics] fc = 10	.7 MHz, 100 dBµ	, fm = 1 kHz, L + R = 90%, Pilot = 10%		II		1
Separation (left)	Sep <sub>L</sub>	L mod. The pin 16 output/pin 17 output	30	45		dB
Separation (right)	Sep <sub>R</sub>	R mod. The pin 17 output/pin 16 output	30	45		dB
Stereo on level	ST <sub>ON</sub>	The pilot modulation such that V7 is under 0.7 V.	1.3	2.7	5	%
Stereo off level	ST <sub>OFF</sub>	The pilot modulation such that V7 is over 4.5 V.		1.5		%
Total harmonic distortion (main)	THD-main	L+R mod. The pin 16 output		0.3	1.3	%
Adjacent channel interference	Brej-3rd	fs = 113 kHz, Vs = 90 %, Pilot = 10 %, The pin 16 output vs. the L-R mod. 1 kHz demodulated output		40		dB
rejection ratio	Brej-5th	fs = 189 kHz, Vs = 90 %, Pilot = 10 %, The pin 16 output vs. the L-R mod. 1 kHz demodulated output		40		dB
[AM Characteristics] fc = 1000 kHz	, fm = 1 kHz					
Current drain	I <sub>CCO-AM</sub>	With no input	15	25	35	mA
Detector output	V <sub>OAM(1)</sub>	23 dBµ, 30% mod. The pin 16 output	100	180	360	mVrms
	V <sub>OAM(2)</sub>	80 dBµ, 30% mod. The pin 16 output	200	320	500	mVrms
Signal to point ratio	S/N <sub>AM(1)</sub>	23 dBµ, 30% mod. The pin 16 output	18	22		dB
Signal-to-noise ratio	S/N <sub>AM(2)</sub>	80 dBµ, 30% mod. The pin 16 output	49	55		dB
Total harmonic distortion (mass)	THD <sub>AM(1)</sub>	80 dBµ, 30% mod. The pin 16 output		0.4	1.2	%
Total harmonic distortion (mono)	THD <sub>AM(2)</sub>	80 dBµ, 80% mod. The pin 16 output		1.0	4.0	%
LED sensitivity	SD <sub>On-AM</sub>		17	27	37	dBµ
Local oscillator buffer output	V <sub>OSC-AM</sub>	With no input. The pin 30 output	110	160	220	mVrms
IF counter buffer output	V <sub>IFBuff-AM</sub>	80 dBµ, no modulation. The pin 13 output	160	220	300	mVrms
ST-IF output	V <sub>STIF-AM</sub>	80 dBµ, no modulation. The pin 7 output	16	34	48	mVrms
S-meter output	V <sub>SM-AM</sub>	0 dBµ, no modulation.	0	0	0.2	V

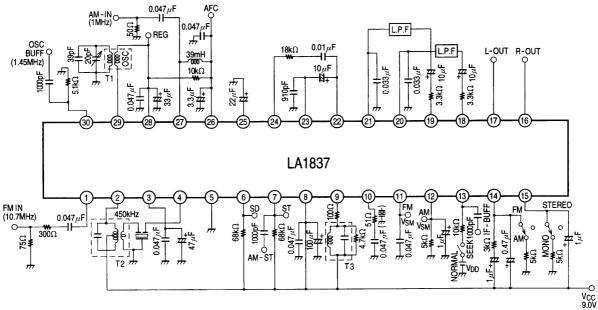


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#### **Block Diagram**



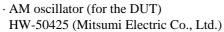
#### **Test Circuit Diagram**

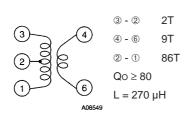


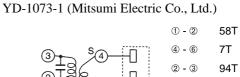
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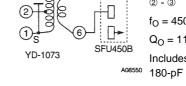
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#### **Coil Specifications**



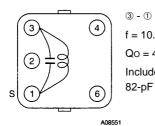






· FM detector

600BEAS-9715Z (The Toko Electric Corporation)



f = 10.7 MHz Qo = 40 Includes an internal 82-pF capacitor.

22T

#### **Pin Functions**

Pin No.	Pin function	Pin voltage (V)	Pin description	Equivalent circuit
1	FM IF input	Vreg	Input impedance ri = 330 $\Omega$	() () () () () () () () () () () () () (
2	AM mixer output	V <sub>CC</sub>	Connect the mixer coil between this pin and V <sub>CC</sub> .	
3	FM IF input bypass	Vreg	Also functions as the MPX regulator filter.	See pin 1.
4	AM IF input	Vreg	Input impedance ri = 2 k $\Omega$	(4)
5	GND	0 V		
6 7	TU - LED ST - LED AM-IF output	V <sub>CC</sub> V <sub>CC</sub>	Active low Open collector output AM stereo IF output (pin 7) The current input must not exceed 150 μA.	
8	V <sub>CC</sub>	V <sub>CC</sub>		
9	FM detector	V <sub>CC</sub>	Recommended detector coil: 600BEAS- 9715Z (The Toko Electric Corporation)	Continued on next page

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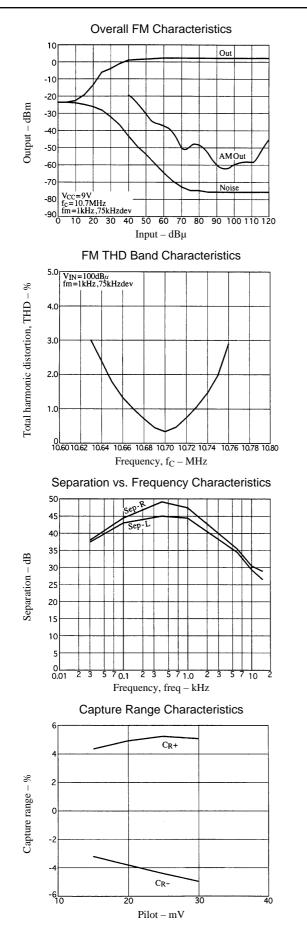
Pin No.	Pin Function	Pin voltage (V)	Pin description	Equivalent circuit
10	AM narrow band CF connection	1.3 V	Recommended narrow band CF: BFU450 C4N (Murata Mfg. Co., Ltd.) If the narrow band SD is not used, bypass this circuit with a 50- $\Omega$ resistor and a 0.047- $\mu$ F capacitor connected in series.	
11	FM S-meter output	0 V	R <sub>L</sub> = 8 kΩ	
12	AM S-meter output AM SD sensitivity adjustment	0 V (AM)	The AM SD sensitivity is adjusted with the value of the external resistor connected between this pin and ground.	28 A08559
13	AM and FM IF buffer output, output control switch (mute switch)	0 V	V13 ≤ 0.5 V: Reception state 1.4 V ≤ V13 ≤ 2.2 V: IF buffer output turned on V13 ≥ 3.5 V: IF buffer output and muting are turned on	
14	Phase comparator low-pass filter (FM/AM switching)	V <sub>CC</sub> – 1.4 (FM) 0 V (AM)	Connecting this pin to ground through a resistor sets the IC to AM mode. Resistor value limits: $2.7 \text{ k}\Omega \text{ (when V}_{\text{CC}} = 7 \text{ V)}$ $3.9 \text{ k}\Omega (8 \text{ V}) 5.1 \text{ k}\Omega (9 \text{ V})$ $6.2 \text{ k}\Omega (10 \text{ V}) 7.5 \text{ k}\Omega (11 \text{ V})$	
15	Pilot detector low-pass filter (forced mono) (VCO stop)	V <sub>CC</sub> – 1.0	If a current of 50 μA or greater flows from this pin, the system is forced to mono. Connecting this pin to ground stops the VCO. The resistor limit values are the same as for pin 14.	
16 17 18 19	Post amplifier input and output	Vreg Vreg	Output impedance $r_0 = 200 \Omega$ Pin 16: right output, pin 17: left output Inverting inputs Pin 18: right input, pin 19: left input $R_{NF} = 33 k\Omega$	(18) (19) (19) (17) (17) (17) (17) (17) (17)
20 21	MPX output	3.5 V 3.5 V	Output impedance $r_0 = 3.3 \text{ k}\Omega$ Pin 20: right de-emphasis Pin 21: left de-emphasis	20) 21) A08564

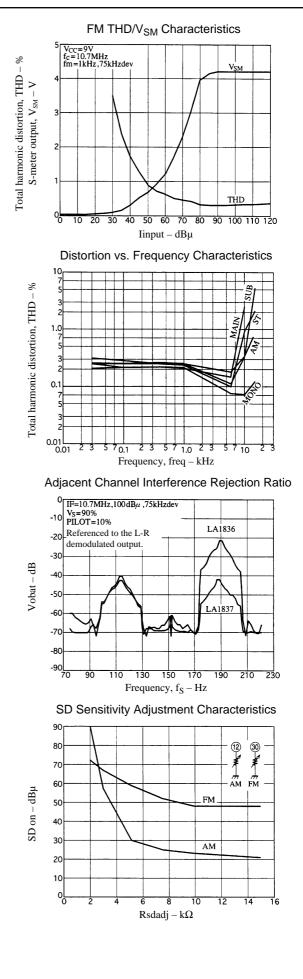
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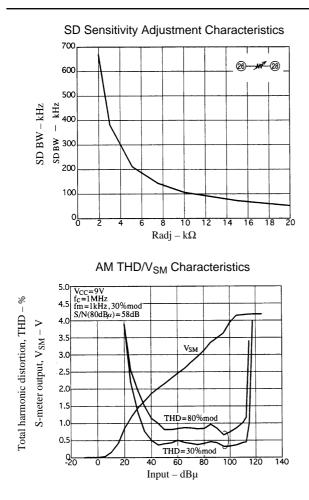
Pin No.	Pin Function	Pin voltage (V)	Pin description	Equivalent circuit
22	MPX input	2.9 V	Input impedance $r_i = 20 \ k\Omega$	(22)
23	FM demodulator output	2.8 V (FM) 2.8 V (AM)	Output impedance $r_0 = 3.0 \text{ k}\Omega$ The separation can be modified by inserting a capacitor between this pin and ground. Set V <sub>O</sub> sub/V <sub>O</sub> main to be about 0 dB.	
24	AM detector output	0 V (FM) 0.5 V (AM)	Output impedance $r_0 = 3.3 \text{ k}\Omega$ The AM frequency characteristics can be modified by adjusting the time constants of the circuits between this pin and pin 22, and between this pin and ground.	
25	AM AGC	0 V (FM) 0.5 V (AM)	The built-in load resistor R = 11 k $\Omega$ .	
26	AFC	Vreg	The FM SD bandwidth can be modified with the external resistor connected between this pin and pin 28.	26) A08569
27	AM RF input	Vreg	Use this pin at the same potential as pin 28.	(27)
28	REG	Vreg	Vreg = 3.6 V	28) A08571
29	OSC	Vreg	Use an oscillator coil between this pin and pin 28.	29 408572
30	Oscillator buffer output, FM SD sensitivity adjustment	1.6 V (FM) 1.3 V (AM)	The FM SD sensitivity can be modified with the external resistor connected between this pin and ground. Output impedance $r_0 = 20 \Omega$	30 

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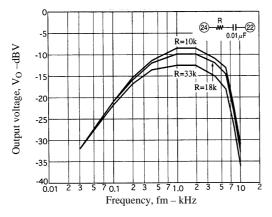




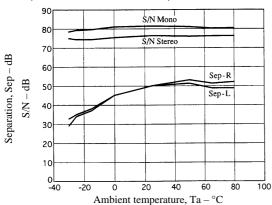
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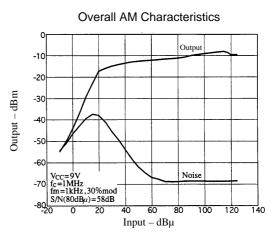


AM Detector Output Frequency Characteristics (2)

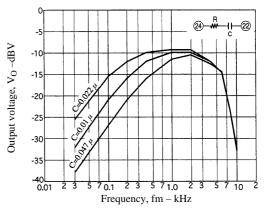


Separation vs. Ambient Temperature Characteristics

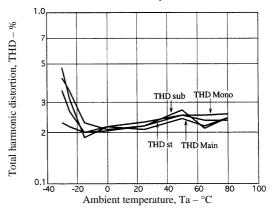




AM Detector Output Frequency Characteristics (1)



Distortion vs. Ambient Temperature Characteristics



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