Monolithic Linear IC



No.573F

LA3161

2-Channel Preamplifier For Car Stereo

Features

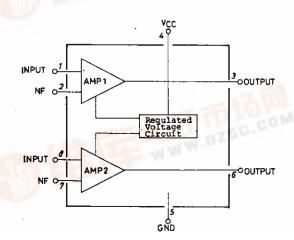
- . On-chip 2 preamplifiers.
- . Good ripple rejection owing to on-chip voltage regulator.
- . Minimum number of external parts required.
- . Low noise.
- . 8-pin SIP package facilitating easy mounting.
- . Pin-compatible with LA3160.

Maximum Ratings at Ta = 25°C			unit
Maximum Supply Voltage	v_{CC}^{max}	18	v
Allowable Power Dissipation	Pdmax	200	mW
Operating Temperature	Topr	-20 to $+75$	°C
Storage Temperature	Tstg	-40 to +125	°C '
Recommended Operating Conditions	at $Ta = 25$ °C		unit
Supply Voltage	v_{CC}	9	V
Load Resistance	R_{T}	10k	Ω

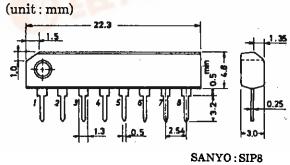
Operating Characteristics at Ta=25°C, $V_{CC}=9V$, $R_L=10k\Omega$, $R_g=600\Omega$, f=1kHz, NAB

			min	тур	max	unit
Current Dissipation I	CC			6.5	8.0	mΆ
	rG	Closed loop		35		dB
		Open loop, vo=0.77V	70	78		dB
Output Voltage v	'n	THD=1%	1.0	1.3		v
Total Harmonic Distortion T	HD	$v_0=0.5V$		0.05	0.30	8
Input Resistance r	i		70k	100k		Ω
Equivalent Input Noise Voltage V	NT	$R_{\alpha}=2.2k\Omega$		1.2	2.0	uF
	T	$R_{\alpha}=2.2k\Omega$	-50	-65		đВ
Ripple Rejection R	r	9 \((0)		-40		đВ

Equivalent Circuit Block Diagram



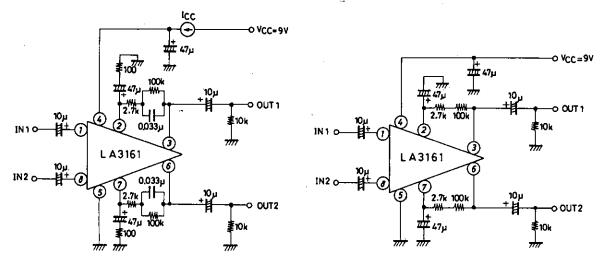
Package Dimensions 3016B



SANYO Electric Co., Ltd. Semiconductor Business Headquarters
TOKYO OFFICE Tokyo Bldg., 1-10, 1 Chome, Ueno, Taito-ku, TOKYO, 110 JAPAN

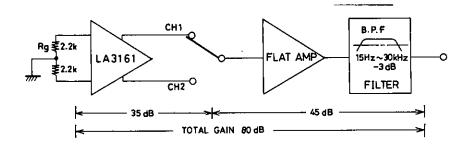
 $\textbf{Test Circuit 1}: V_O, VG, THD, I_{CC}, \mathbf{r_i}$

Test Circuit 2: VGO

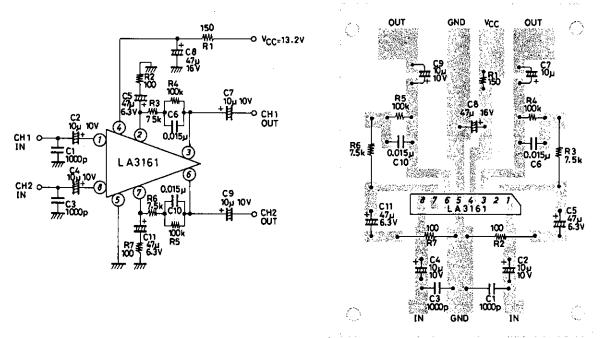


Test Circuit 3: Noise

Unit (resistance: Ω , capacitance: F)



Sample Application Circuit 1: Preamplifier for Car Stereo



Unit (resistance: Ω , capacitance: F)

Function of External Parts

 C_2, C_4 are input coupling capacitors. In NAB equalizer amplifier, the gain at low frequencies is high and 1/f noise inside the IC is emphasized as output noise. Therefore, if the reactance of capacitor at low frequencies is increased, the dependence of 1/f noise on the signal source resistance causes the output noise voltage to deteriorate, and the value of reactance must be made small enough as compared with the signal source resistance. C_2, C_4 also influence the operation start time and the adequate value of these capacitors is 10uF. (Since C_2 , C_4 of less than 4.7uF make the operation start time longer, use C_2 , C_4 of 4.7uF or more.)

 C_5 , C_{11} are NF capacitors. The lower cut-off frequency depends on the value of these capacitors.

If the lower cut-off frequency is taken as $\mathbf{f}_{\mathbf{L}}$:

$$C5 \cdot (C11) = 1/2\pi \cdot f_L \cdot R2 (R7)$$

If the value of this capacitor is made larger, the operation start time of amplifier is more delayed. The adequate value of capacitor is 47uF.

The frequency characteristic of the equalizer amplifier depends on C_6 and R_4 , R_3 (C_{10} and R_5 , R_6).

The time constants to obtain the standard NAB characteristic are as shown below.

Tape speed	9.5 cm/sec	4.75 cm/sec
C6 (R3+R4)	3180 µ sec	1590 µ sec
R3 C6	90 μ sec	120 µ sec

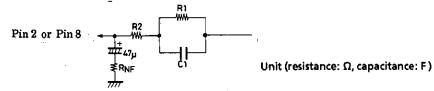
C8 is a bias capacitor for the power line. C8 of 47uF is inserted at a point as close to the power supply pin (pin 4) as possible.

 C_1 , C_3 are for preventing radio interference in the strong electric field, interference attributable to engine noise, and blocking oscillation at the time of large amplitude operation. The adequate value of C_1 , C_3 is approximately 1000pF.

C7, C9 are output coupling capacitors. The adequate value of C7, C9 is 10uF.

NAB element and determination of gain

Since the DC feedback is provided by R_1 , R_2 of NAB element, which brings about DC output potential at pins 3, 6, it is impossible to change the value of R_1 , R_2 of NAB element greatly. Therefore, when determining the gain, change $R_{\rm NF}$ with R_1 , R_2 , C_1 (NAB element) kept constant.



(1) How to obtain R_{NF} Impedance Z of NAB element is

$$z = \frac{1}{1/R! + j \omega C!} + R2$$

= $(R! + R2) \left\{ \frac{1 + j \omega C!}{1 + j \omega C!R!} \left\{ \frac{R!R2}{R! + R2} \right\} \right\}$

For a general negative feedback amplifier circuit, A=Ao/(1+Ao β) applies, and Z=A.RNF is obtained under conditions of Ao \gg A, A \gg 1 (β =RNF/(RNF+Z), Ao=open-loop gain, A=feedback gain).

Therefore, we can use an approximation of RNF=Z/A.

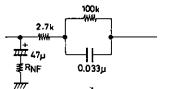
 $A = (VG \text{ for } 1 \text{ kHz}) \text{ times, (Set } R_1, R_2 \text{ at approximately } 100 \text{kohms.})$ Each time constant of NAB characteristic

	Tape speed	9.5 cm/sec	4.75 cm∕sec
Τı	CI, RI	3180 μ sec	1590 ⊭sec
Т2	CI (RI//R2)	90 µ sec	120 µ sec

(2) Examples of NAB constant

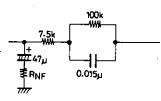
Unit (resistance: Ω, capacitance: F)

(a) Tape speed: 9.5cm/sec. (8 tracks)



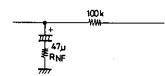
	VG :	R_{NF}	(VG/	f=1kH	z)
VG	30	35	40	dΒ	
RNF	180	100	56	Ω	

(b) Tape speed: 4.75cm/sec. (cassette)



VG : RNF				
VG	30	35	40	đВ
RNF	440	240	130	Ω
	440		,00	

(c) Flat amp



VG : RNF				
VG	30	35	40	₫B
RNF	3-2	1.8	1	kΩ

Proper cares in using IC

1. Maximum Rating

If the IC is used in the vicinity of the maximum rating, even a slight variation in conditions may cause the maximum rating to be exceeded, thereby leading to a breakdown. Allow an ample margin of variation for supply voltage, etc. and use the IC in the range where the maximum rating is not exceeded.

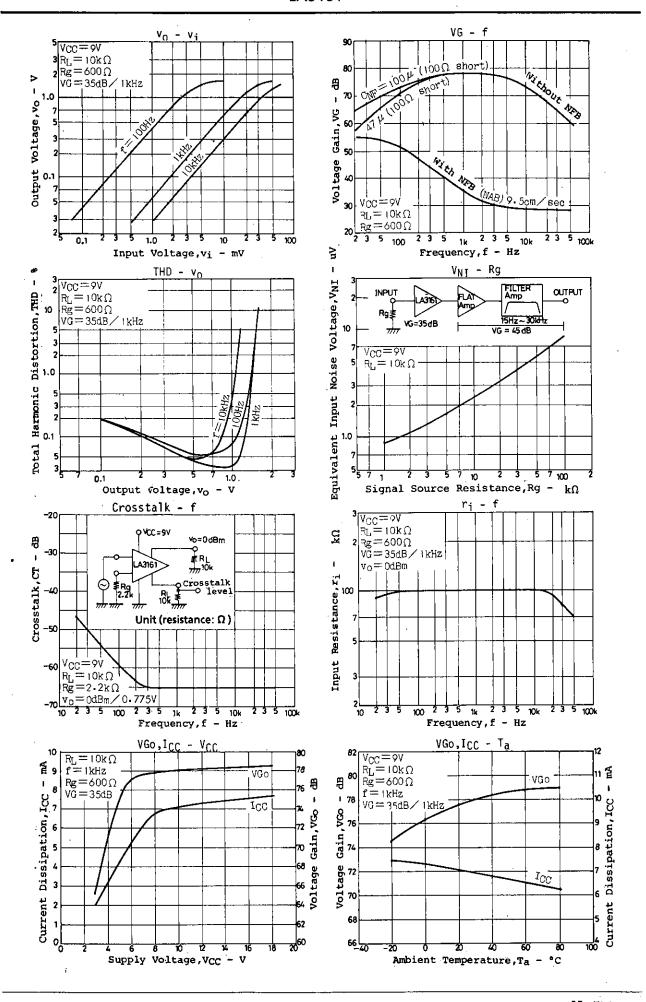
2. Short between pins

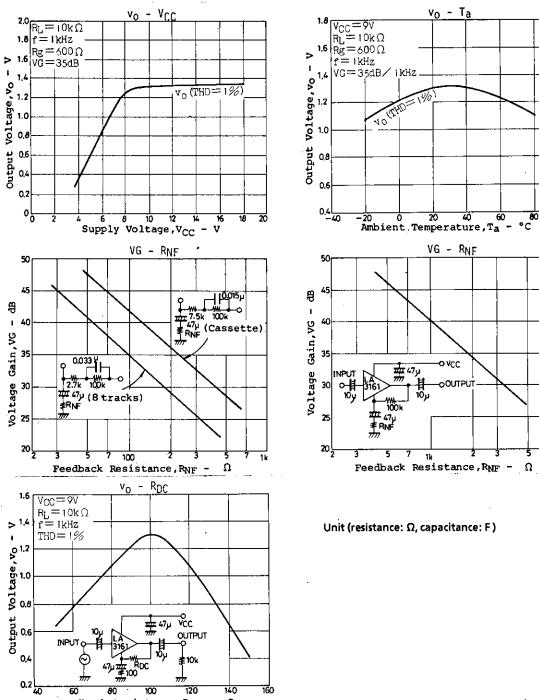
If the supply voltage is applied when the space between pins is shorted, a breakdown or deterioration may occur. When installing the IC on the board or applying the supply voltage, make sure that the space between pins is not shorted with solder, etc.

3. Breakdown of IC attributable to inverted insertion
If the IC is inserted inversely and operated, the IC may suffer from something unusual, thereby leading to a breakdown or deterioration of the IC.
When installing the IC on the board or operating the IC, check the marked surface of IC.

Proper cares to be taken for obtaining optimum operation of IC

- . Set DC resistance of R₁, R₂ of NAB element at approximately 100kohms.
- Determine the gain by changing $R_{
 m NF}$ without changing NAB constant. (Refer to Examples of NAB constant.)





Feedback Resistance, RDC -

100

- No products described or contained herein are intended for use in surgical implants, life-support systems, aerospace equipment, nuclear power control systems, vehicles, disaster/crime-prevention equipment and the like, the failure of which may directly or indirectly cause injury, death or property loss.
- Anyone purchasing any products described or contained herein for an above-mentioned use shall:
 - ① Accept full responsibility and indemnify and defend SANYO ELECTRIC CO., LTD., its affiliates, subsidiaries and distributors and all their officers and employees, jointly and severally, against any and all claims and litigation and all damages, cost and expenses associated with such use:
 - ② Not impose any responsibility for any fault or negligence which may be cited in any such claim or litigation on SANYO ELECTRIC CO., LTD., its affiliates, subsidiaries and distributors or any of their officers and employees jointly or severally.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production, SANYO believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.