

Monolithic Linear IC

SANYO	No. 1340C	LA5310M
	Voltage Divider for LCD Applications	

The LA5310M is a voltage divider IC for use in LCD matrix multidrive applications.

Features

- . Power supply for 1/9 bias LCD applications
- . 5 OP amps producing 5 voltage outputs
- . Low current dissipation (1.0mA max.)
- . Miniflat package

Maximum Ratings at Ta=25°C

			unit
Maximum Supply Voltage	V _{CCmax}	35	V
Output Current	I _{OUT}	5	mA
Allowable Power Dissipation	P _{dmax}	300	mW
Operating Temperature	T _{opr}	-20 to +75	°C
Storage Temperature	T _{stg}	-30 to +125	°C

Operating Conditions at Ta=25°C

			unit
Supply Voltage Range	V _{CC op}	11 to 25	V
Recommended Output Current	I ₁	0 to 3	mA
"	I _{2, I₃}	-3 to +3	mA
"	I _{4, I₅}	-3 to 0	mA

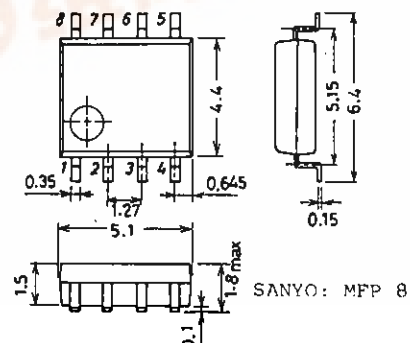
Operating Characteristics at Ta=25°C

			min	typ	max	unit
Current Dissipation	I _{CC}	V _{CC} =25V			1.0	mA
Output Voltage	V ₁		-1.25	-1.20	-1.15	V
Output Ratio 1	R _{a1}	V ₂ /V ₁ , V _{CC} :0	1.96	2.00	2.04	V
"	"	V _{ref} =-12V, GND=-25V				
"	2	R _{a2} V ₅ -V ₃ /V ₅ -V ₄ ,	1.96	2.00	2.04	V
"	3	R _{b1} -V ₅ /-V ₁ ,	8.73	9.00	9.27	V
"	4	R _{b2} -V ₅ /-V ₂ ,	4.37	4.50	4.63	V
"	5	R _{b3} -V ₅ /-V ₅ +V ₃ ,	4.37	4.50	4.63	V
"	6	R _{b4} -V ₅ /-V ₅ +V ₄ ,	8.73	9.00	9.27	V

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Package Dimensions 3032B

(unit: mm)



SANYO: MFP 8

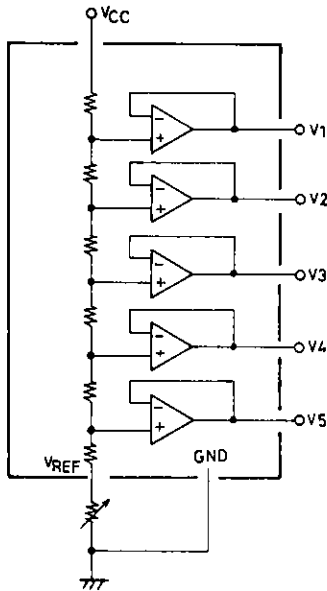


LA5310M

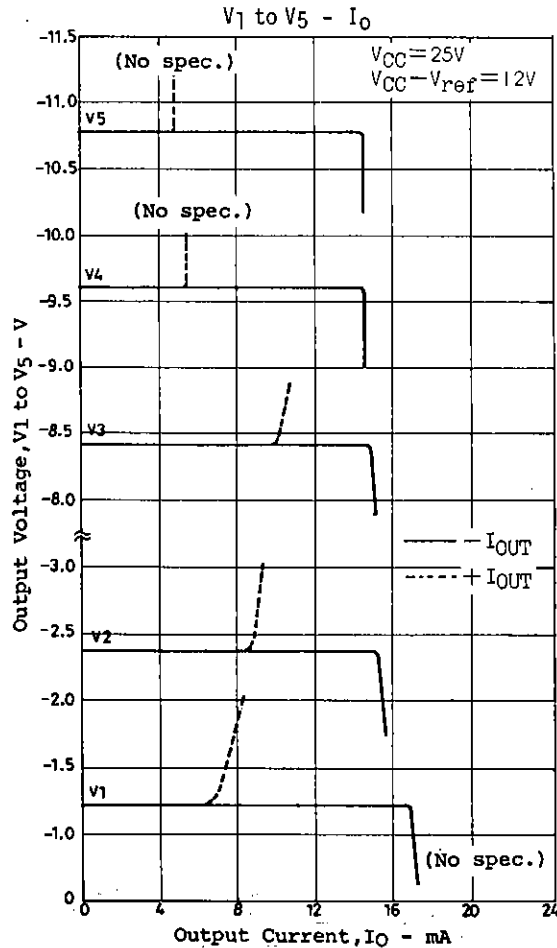
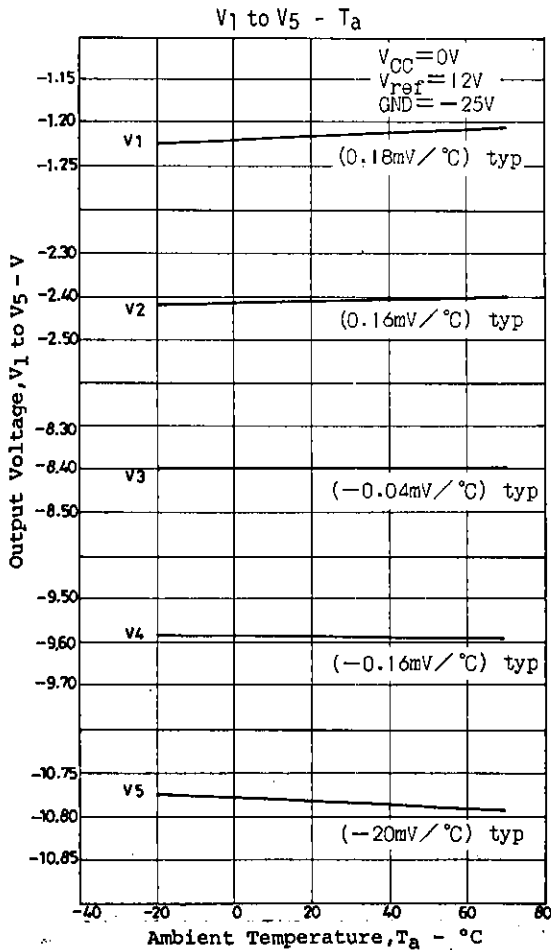
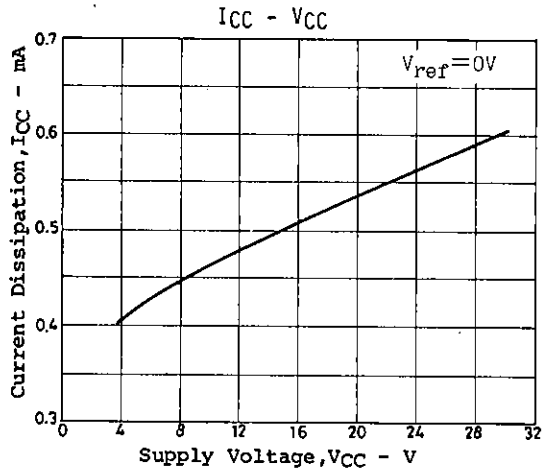
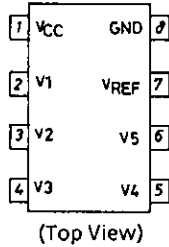
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			min	typ	max	unit
Load Regulation	ΔV_1	$+100\mu A < I_{OUT} < +3mA$			20	mV
"	ΔV_2	"			20	mV
"	ΔV_3	"			20	mV
"	$-\Delta V_2$	$-3mA < I_{OUT} < -100\mu A$			20	mV
"	$-\Delta V_3$	"			20	mV
"	$-\Delta V_4$	"			20	mV
"	$-\Delta V_5$	"			20	mV

Equivalent Circuit



Pin Assingment



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