Ordering number: EN 3 2 4 LA653 中心的

Monolithic Linear IC



No.3264

LA6530,6531

2-Channel BTL-Use Drivers

The LA6530, 6531 are 2-channel BTL-use drivers designed for compact disc pickup actuation.

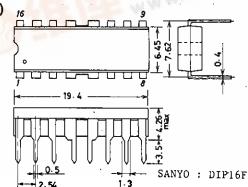
Functions and Features

- · High output current ($I_0 \max = 0.7A$)
- Wide operating voltage range (4 to 15V)
- · Low input bias current
- · LA6530 Output of amps 1 to 4 at muting-ON mode: OFF
- · LA6531 Output of amps 1 to 4 and buffer amp at muting-ON mode : OFF

Maximum Ratings at Ta = 25°C						unit	
Maximum Supply Voltage	V_{CC}	max			16	V	
Allowable Power Dissipation	Pd n	nax	145 150		1.9	W	
Differential Input Voltage	V_{ID}		Amp 2, amp 3		15	V	
Common-Mode Input Voltage	V_{ICN}	И	Amp 2, amp 3		15	V	
Maximum Input Voltage		3 max	Buffer amp		15	V	
Maximum Flow-in Current	I _M m	nax			1.0	mA	
at Muting Pin							
Maximum Output Current	I _O m	ax			0.7	Α	
Operating Temperature	Topi	r		- 20 to +	75	$^{\circ}\mathrm{C}$	
Storage Temperature	Tstg	;		-55 to +1	150	°C	
Operating Conditions at Ta = 25°C						unit	
Maximum Supply Voltage	v_{cc}	max			5	V	
Load Resistance	R_{L}				8	Ω	
Operating Characteristics at Ta=25°C, V _{CC} =5.0V					typ	max	unit
No-Loaded Current Dissipation	n 1	$I_{CC}1$	Mute OFF, pins 8,9,16 GND	5	10	20	mA
No-Loaded Current Dissipation	n 2	$I_{CC}2$	Mute OFF, pins 8,9,16 GND	3	7	15	mA
No-Loaded Current Dissipation	n 3	$I_{CC}3$	Mute OFF, pins 8,9,16 1/2 V _{CC}	10	20	30	mA
No Looded Compant Dissipation	4	Land	Mute OFF, pins 8,9,16 $1/2$ V_{CO}	4	8	16	mA
No-Loaded Current Dissipation	n 4	$I_{CC}4$	Mule Of F, pills 0,3,10 1/2 VCC	; 4	0	10	****
No-Loaded Current Dissipation	n 4	1004	Mute Of 1 ,pins 0,9,10 1/2 VCC	-	_	on nex	

Package Dimensions 3054A-D16FNIC

(unit: mm)



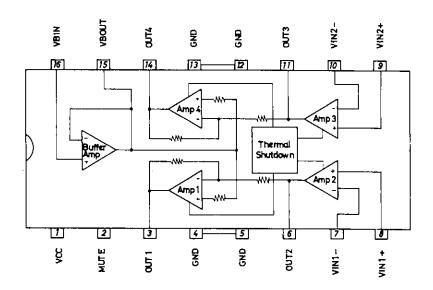
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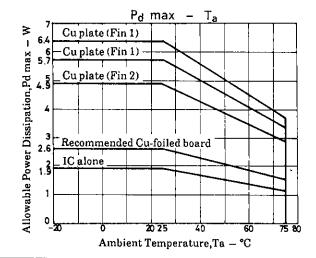
			min	typ max	unit
Output Offset Voltage 1	$V_{OF}1$	Out 1 - Out 2	-50	50	mV
Output Offset Voltage 2	$V_{OF}2$	Out 4 - Out 3	-50	50	mV
Buffer Input-Output	$V_{\rm BIO}$	Buffer amp	-30	30	mV
Voltage Difference		-			
Buffer Input Voltage Range	V_{BICM}	Buffer amp	1.5	$V_{\rm CC} - 1.5$	v
Common-Mode Input Voltage Range	V_{ICM}	Amp 2, amp 3	1.0	$V_{CC} - 1.5$	V
Input Bias Current	IB			50 300	nΑ
Output Voltage	v_0	$R_L = 8.0\Omega$	2.8	3.3	v
Bridge Output Voltage Difference	V_{OD}	Pins 3-6,11-14 8Ω load	1.8	2.2	V
Closed-Circuit Voltage Gain	V_{G}		30	38	dB
Muting Pin ON-State Voltage	V_{M}			0.7	V
Muting Pin Flow-in Current	$I_{\mathbf{M}}$			3	μÁ
Note) With thermal shutdown function	n			-	1

* : The LA6531 is so designed that the outputs at OUT1 to OUT4 and the output at VBOUT are turned OFF.

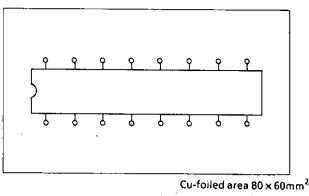
lepha: Be carefull in handling the LA6530, 6531, because dielectric breakdown is liable to occur.

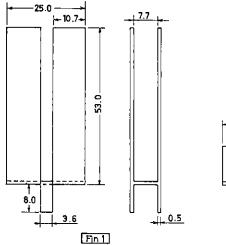
Equivalent Circuit Block Diagram

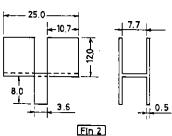




Sample Printed Circuit Pattern







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