Ordering number: E京3045/533供应商

Monolithic Linear IC

LA7533

SANYO

No.3045

IF Signal Processing (VIF+SIF)
Circuit for TV / VCR Use

The LA7533 is an IC containing the VIF section and SIF section on a single chip in the DIP20S package. The use of the small-sized package serves to make VTR tuner units smaller.

As compared with the LA7530N, the LA7533 is improved in characteristics when it is operated at supply voltage 9V (DG, DP, RF AGC temperature characteristics).

The LA7533 is applicable to the circuit designed for the LA7530N.

#### **Functions**

- · VIF section: VIF AMP, VIDEO DET, PEAK IF AGC, B/W NOISE CANCELLER, RF AGC, AFT, VIDEO MUTE.
- · SIF section: SIF LIMITER AMP, FM DET, SND MUTE.

#### Features

- · High-gain VIF amp requiring no preamp.
- · Higher AGC speed.
- · Adjustment-free FM detector because of ceramic discriminator-used quadrature detection.
- · Possible to mute video, sound for VTR.
- · Small-sized package.
- · Minimum number of external parts required.
- · Operated at supply voltage 9V

Maximum	Ratinge	at Ta -	- 95°€
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Maximum Supply Voltage	$ m V_{CC}$ max		14	V
External Flow-out Current	I <sub>16</sub> max		5	mA
Pin 20 Maximum	$V_{20}$ max		$V_{CC}$	V
Supply Voltage			M. W. CO.	
Allowable Power Dissipation	Pd max	Ta≤40°C	1,1	W
Operating Temperature	Topr		-20  to  +70	°C
Storage Temperature	Tstg		-55  to  + 125	°C
\ 11 . C1 ' 11.11 ( CD ) A	×00			

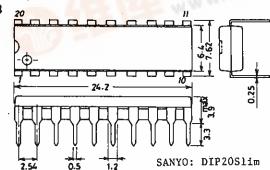
### Operating Conditions at Ta = 25°C

peraning conditions at 1a-20	C		unit
Recommended Supply Voltage	$V_{CC}$	9	V
Operating Voltage Range	$ m V_{CC}$ op	8.1 to 13.2	V

## **Package Dimensions**

(unit :mm)

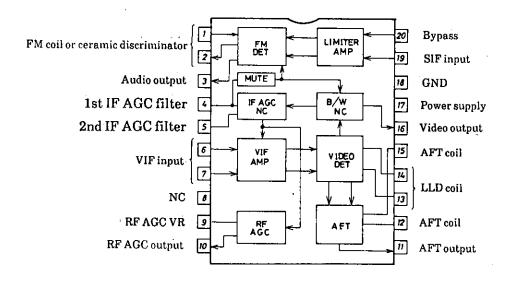
3021B



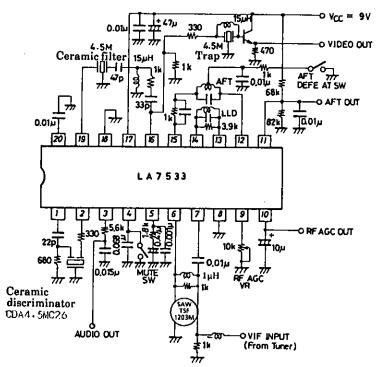
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Operating Characteristics at	$Ca = 25^{\circ}C, V_{CO}$	$g = 9V, f_p = 58.75MHz, f_s = 54.25I$				
	$c_0 = 4.5 MHz(S)$		min	typ	max	unit
Total Circuit Current	<u>I</u> 17	DC	40	49	63	mA
Maximum RF AGC Voltage	$V_{10H}$	DC	6.2	6.5	6.8	V
Minimum RF AGC Voltage	$V_{10L}$	DC		0.1	0.5	V
Quiescent Video	$V_{16}$	DC	4.2	4.6	5.0	V
OutputVoltage						
Quiescent AFT Ouptut Voltage	· V <sub>11</sub>	DC	2.9	4,9	5.9	V
Input Sensitivity	$\mathbf{V_{i}}^{-}$	$f_{\rm m} = 400 \text{Hz}  40\% \text{AM},$	31	37	42	dΒμ
•	•	$\vec{V}_0 = 0.8 \text{Vp-p}$				
AGC Range	GR	$f_{\rm m} = 400  \rm Hz^{-} 40\%  AM$	57	63		dB
<b>3</b>		$V_0 = 0.8 \text{Vp-p}$	٠.	- 00		uВ
Maximum Allowable Input	V <sub>i</sub> max	$f_{\rm m} = 15 {\rm kHz}^{2} 78\% {\rm AM},$	90	130		mVrms
	. 1	$V_0 = \pm 1 dB$	00	100		111 4 1 1112
Video Output Amplitude	$V_{o(VIDEO)}$	$V_i = 10 \text{mVrms},$	1.4	1.65	1.9	Vn n
· raco Output i impireado	* O(VIDEO)	$f_{\rm m} = 15 \text{kHz}  78\% \text{AM}$	1,4	1.00	1.9	Vp-p
Output S/N	S/N	$V_i = 10 \text{mVrms CW}$	48	53		JD.
Carrier Leak	CL CL					dB
Carrier Deak	CL	$V_i = 100 \text{mVrms},$	50	<b>5</b> 5		dB
Maximum AFT Voltage	<b>V</b>	$f_{m} = 15kHz 78\%AM$		~ -		
Minimum AFT Voltage	$\mathbf{v}_{11H}$	V <sub>i</sub> =10mVrms SWEEP	8.1	8.5	8.9	V
	$\mathbf{v}_{11L}$	V <sub>i</sub> =10mVrms SWEEP	0.1	0.4	0.9	V
AFT Detection Sensitivity	$\mathbf{S}_{\mathbf{f}}$	V <sub>i</sub> =10mVrms SWEEP	45	70		mV/kHz
White Noise Threshold Level	$\mathbf{V}_{\mathbf{WTH}}$	$V_i = 10 \text{mVrms SWEEP}$	4.7	5.1	5.5	V
White Noise Clamp Level	$\mathbf{v}_{\mathbf{wcl}}$	$V_i = 10 \text{mVrms}$ SWEEP	2.9	3.3	3.7	V
Black Noise Threshold Level	$\mathbf{v}_{\mathtt{BTH}}$	$V_i = 10 \text{mVrms}$ SWEEP	1.6	1.85	2.1	v
Black Noise Clamp Level	$V_{BCL}$	$V_i = 10 \text{mVrms}$ SWEEP	2.6	2.9	3.2	V
SIF Output Signal Voltage	$V_{o(SIF)}$	P/S = 20dB	70	100		mVrms
Frequency Characteristic	fc	-3dB	5	7		MHz
Differential Gain	ĎG	$V_i = -27 dBm \text{ (peak) } 87.5\%$	_	3		%
		VIDEOMOD		·		70
Differential Phase	DP	$V_i = -27 dBm \text{ (peak) } 87.5\%$		3		doa
		VIDEOMOD		U		deg
VIF Input Resistance	$\mathbf{r_{i}}$	112301402		1.5		1.0
VIF Input Capacitance	c <sub>i</sub>			3.0		kΩ
SIF Limiting Voltage		−3dB			coo	pF
Detection Output Voltage	Vi (lim)		440	300	600	μVrms
Detection Suspen voltage	$V_{o({ m DET})}$	$V_i = 100 \text{mVrms,} f_m = 400 \text{Hz,}$ $\Delta f = \pm 25 \text{kHz}$	440	670	800	mVrms
Total Harmonic Distortion	THD					
10tal Halmonic Distol fion	$THD_{(DET)}$	$V_i = 100 \text{mVrms}, f_m = 400 \text{Hz},$		0.6	1.5	%
AM Rejection	AMR	$\Delta f = \pm 25 \text{kHz}$				
Am Rejection	AWIK	$V_i = 100 \text{mVrms}, f_m = 400 \text{Hz},$	50	60		dB
Noise Outnut Valters	<b>T</b> 7	$\Delta \hat{f} = \pm 25 \text{kHz} 30\% \text{AM}$				
Noise Output Voltage	$\mathbf{v}_{\mathbf{N}}$				3.5	mVrms
Pin 4 Muting Start Voltage	$V_{M(4)}$		0.3	0.5		V
Pin 20 Muting Attenuation	$\mathrm{ATT}_{\mathrm{M}(20)}'$		60			dB
•						

# Equivalent Circuit Block Diagram



## Sample Application Circuit (USA)



Unit (resistance:  $\Omega$ , capacitance: F)

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