Monolithic Linear IC

SANYO

No.2868A

LA7951

Video Switch for TV / VCR Use

Overview

The LA7951 Video Switch is a solid-state 4-input 4-output video switch ideally suited for use as a video selector switch in multiple-source video systems and multiple VCR video editing systems.

The switching logic, coupled with built-in video amplifier, ripple filter and 75Ω output driver facilitate a minimum parts count video switching subsystem. The solid-state switches feature low crosstalk and wide bandwidth.

The LA7951 operates from a single 12V power supply, and is available in 14-pin plastic DIPs.

Features

- · 4-input 4-output multi-functional video switch
- · Low crosstalk, wide bandwidth
- · Internal 6dB video amplifier
- · Ripple filter
- 75Ω output driver for video monitor (COMMON OUT)

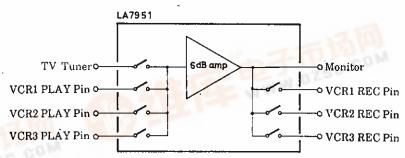


Figure 1. Editing System Switch Connections

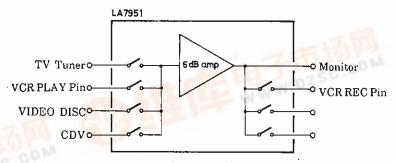
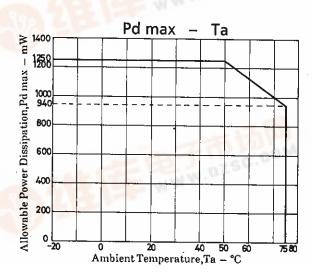
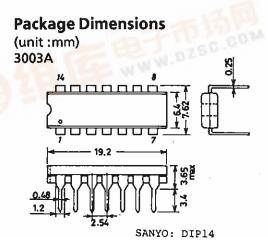


Figure 2. Video Selector Switch Connections

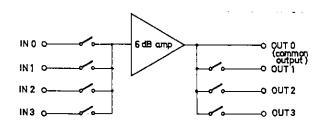




		•				
Maximum Ratings at Ta = 25°C	,				uni	t
Maximum Supply Voltage	V_4 max			14		I
Maximum Control	V ₆ max, V ₉	max		V_{CC}	, 1	I
Input Voltage	V ₁₃ max					
Maximum Signal	I ₁ max			-5	mA	
Output Current (1)						
Maximum Signal	I ₇ max, I ₈ n	nax		~20	m/	\
Output Current (2)	I ₁₄ max					
Allowable Power Dissipation	Pd max	Ta≦50°C		1.25	V	I
Operating Temperature	Topr		- 20	to + 75		
Storage Temperature	Tstg			0 + 125		
On another of Constitutions of Manager	00				_	
Operating Conditions at Ta = 25					uni	
Recommended Supply Voltage	v_{cc}			12		
OperatingVoltage Range	$ m V_{CC}$ op	•	9	to 13.2	· \	7
_						
Operating Characteristics at Ta	$1 = 25$ °C, V_{CC}	=12V	min	typ	max	unit
Quiescent Current	I_{CC}	Pins 7,8,14 open	20	27.5	37	mA
		Pins 6,9,13 to GND				
Input Pin Bias Voltage	$V_3, V_5, V_{10}, V_$	V ₁₂	2.6	3.1	3.6	V
Output Pin Bias Voltage (1)	V_1		4.6	5.3	6.0	V
Output Pin Bias Voltage (2)	V_{7}, V_{8}, V_{14}		3.3	4.0	4.7	v
Control Threshold Voltage H1	V _{6H}		2.3		v_{cc}	v
Control Threshold Voltage H2	V_{9H}, V_{13H}		3.0		v_{cc}	v
Control Threshold Voltage L1	V_{6L}		0		0.8	v
Control Threshold Voltage L2	V_{9L} , V_{13L}		ő		1.5	v
Control Input Current 1	I ₆	$V_6 = 5V$	U	0.32	0.5	mA
Control Input Current 2	$I_{9}I_{13}$	$V_9 = V_{13} = 5V$		0.52	-50	
Voltage Gain 1	GV1	$f = 1MHz, V_0 = 1Vpp$	0.5	-		μA
, orango ourii i	UVI	See Note.	- 0.0	+0.5	# 1.0	dB
Voltage Gain 2	GV2	$f = 1MHz, V_0 = 1Vpp$	0.7			10
· orage dam z	GVZ	See Note.	-0.7	+0.3	+1.3	dB
Frequency Characteristics	GV_f		2			100
1 requestey Offar acteristics	GVI	$f = 100 \text{kHz}, V_0 = 1 \text{Vpp} = 0 \text{dB},$	– 3	- 1		dB
Output Noise Voltage	17	f=10MHz See Note.				
Output Noise voltage	v_N	BPF = 10kHz to 4.2MHz		0.5	1.0	mVrms
Output Dans wis Dans 1	DD4	See Note.				
Output Dynamic Range 1	DR1	$f = 10kHz, V_0 = 1.5Vpp$		0.5	1.2	%
0 + + D + + D + 0	222	See Note.				
Output Dynamic Range 2	DR2	$f=10kHz, V_o=1.5Vpp$		1.0	2.0	%
•		See Note.				
Crosstalk 1	CT1	$f = 4MHz, V_o = 1Vpp$	50	-55		dB
<u>.</u> .		See Note.				
Crosstalk 2	CT2	$f = 4MHz$, $V_o = 1Vpp$	-20	-40		dB
		See Note.				

Note) Measured output terminated with 75Ω . Current flowing into IC is taken as plus (+). Parameter 1 refers to the COMMON OUT signal output and parameter 2 to the OUT1 to OUT3 signal outputs.

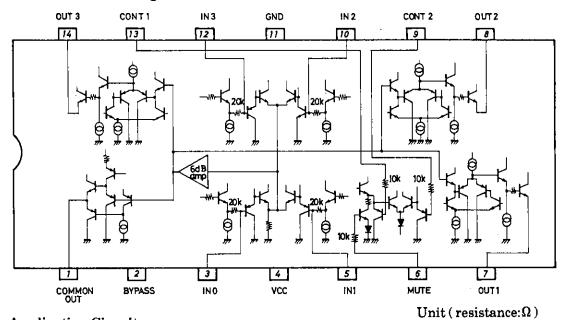
Video Switch Block Diagram



Switching Functions

Control			Input			Output				
MUTE (Pin 6)	1 (Pin 13)	2 (Pin 9)	0 (Pin 3)	1 (Pin 5)	2 (Pin 10)	3 (Pin 12)	COMMON (Pin 1)	1 (Pin 7)	2 (Pin 8)	3 (Pin 14)
L			OFF	OFF	OFF	OFF	ON	OFF	off	OFF
Н	L	L	ON	OFF	OFF	OFF	ON	ON	ON	ON
Н	L	Н	OFF	ON	OFF	OFF	ON	OFF	ON	ON
Н	Н	L	OFF	OFF	ON	OFF	ON	ON	OFF	ON
H	Н	Н	OFF	OFF	OFF	ON	ON	ON	ON	OFF

Equivalent Circuit Block Diagram



Sample Application Circuit

1000µ3 OUT 0 0-季75 IN 0 Ь IN1 Unit (resistance: Ω , capacitance: F)

The power supply decoupling capacitor should be mounted as close to the LA7951 as physically possible.

MUTE

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