Monolithic Linear IC

SANYO

No.3352B

LA7954

Video Switch for TV / VCR Use

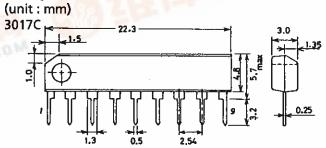
Features

- · 4 inputs, 1 output
- · Excellent crosstalk characteristic
- · Wide band

Maximum Ratings at Ta = 25°C	U DZSC.			unit	
Maximum Supply Voltage	V7 max	-	14	V	
Maximum Input Apply Voltag	e(1) V4 max, V6 max		8	V	
60	V8 max, V9 max,				
Maximum Input Apply Voltag	e (2) V2 max, V3 max	$V_{\rm CC} = 14V$	14	V	
Maximum Output Current	I1 max		7	mA	
Allowable Power Dissipation	Pd max	Ta≦65°C	540	mW	
Operating Temperature	Topr		-20 to +65	°C	
Storage Temperature	Tstg		-55 to + 150	°C	
Operating Conditions at $Ta = 2$	5°C			unit	
Operating Voltage Range	$ m V_{CC}$ op		8 to 13.5	V	
Recommended Supply Voltage	v_{CC}		12	V	
Operating Characteristics of T					M.
Operating Characteristics at T			min typ		unit
	Icc		9 12		mA
Input Bias Voltage	V4, V6,		4.5 4.8	5.1	V
•	V8, V9				
Output Bias Voltage	V1		3.7 4.1	4.3	V
Output DC Offset Voltage	Vos	NIL S	-50 0	+50	mV
	V2H, V3H,		2.3		v
12 See 12	V2L, V3L			0.7	V

Continued on next page.

Package Dimensions



SANYO: SIP9

Continued from preceding pag	e.					
•			min	typ	max	unit
Control Input Current	12, 13		-20	-6		μΑ
Voltage Gain	GV	$f=1MHz, V_{IN}=2Vpp (Note 1)$	-0.5	-0.2		dB
Frequency Characteristic	GV-f	$\int 0 dB at f = 100 kHz (Note 1)$	-3	0		dB

 $f = 10 \text{MHz}, V_{\text{IN}} = 2 \text{Vpp}$ $f = 15 \text{LHz}, V_{\text{IN}} = 6 \text{mg} \text{ (Note 1)}$

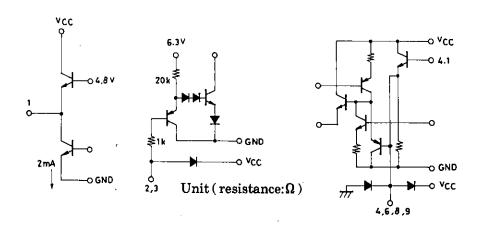
 $f = 15kHz, V_{IN} = 6p-p \text{ (Note 1)}$ Output Dynamic Range 0.3 6 % V_{DR} $V_{IN} = 2V_{p-p,f} = 3MHz$ (Note 1) Crosstalk (Note 2) CT48 58 dΒ $V_{IN} = 2V_{p-p,f} = 5MHz$ (Note 1) 45 55 dB

Video Switch Truth Table

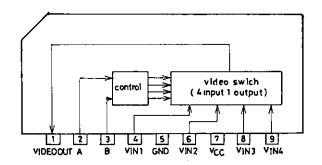
S2 (Pin 2)	S3 (Pin 3)	V _{IN} 1 (Pin 4)	V _{IN} 2 (Pin 6)	V _{IN} 3 (Pin 8)	V _{IN} 4 (Pin 9)
Н	Н	ON	OFF	OFF	OFF
L	H	OFF	ON	OFF	OFF
H	L	OFF	OFF	ON	OFF
L	L	OFF	OFF	OFF	ON

Note 1: Refer to this Truth Table and make measurements by switching S2, S3.

Input/Output Equivalent Circuit

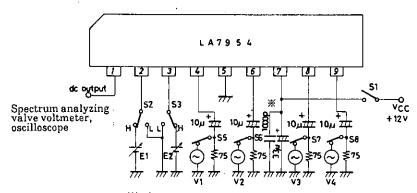


Equivalent Circuit Block Diagram

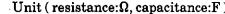


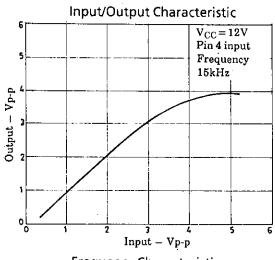
^{*} Current flowing into the IC is defined as positive; current flowing out is defined as negative.

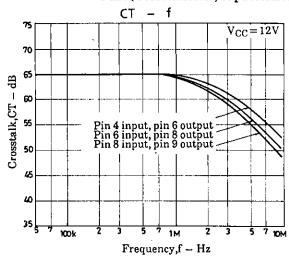
Test Circuit

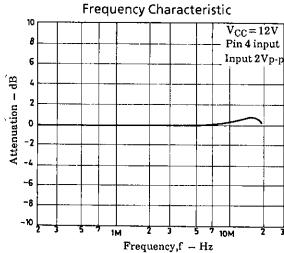


 \times : Connect the bypass capacitor for v_{CC} as close to pin 7 as possible. Unit (resistance: $\Omega,$ capacitance: F)









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