Ordering number: E1231537956供应商

Monolithic Linear IC

SANYO

No.3353A

**LA7956** 

# Video Switch for TV / VCR Use

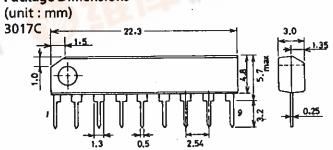
#### **Features**

- · 4 inputs, 1 output,  $75\Omega$  termination, driver on-chip
- · 6dB amp on-chip
- · Excellent crosstalk characteristic
- · Wide band

Maximum Ratings at Ta = 25	°C				unit	
Maximum Supply Voltage	V7 n	nax		14	V	
		nax, V6 max		8	V	
		nax, V9 max,			440	
Maximum Input Supply Vol		nax, V3 max $V_{CC} = 14V$		14	V	C-0.34
Maximum Output Current	I1 m			10	mA	
Allowable Power Dissipation				540	mW	
Operating Temperature Topi			-20 to		°C	
Storage Temperature	Tstg		-55 to +		°C	
and sta	7		-0000	100	C	
Operating Conditions at Ta=	= 25°C				unit	
Operating Voltage Range V <sub>CC</sub>		αο	10.5 to 3	125	V	
Recommended Supply Volta	ge V <sub>CC</sub>		10.0 00	12	v	
Operating Characteristics at	$t Ta = 25^{\circ}C.V_{e}$	$c_{c} = 12V$	min	trum	***	- 0.14
Quiescent Current	$I_{CC}$	00 12 7	15	typ 21	max 30	unit mA
Input Bias Voltage	V4, V6,		3.5	3.8	4.1	V
	V8, V9		3.0	5.0	4,1	v
Output Bias Voltage	V1		4.6	6.1	7.6	V
Output DC Offset Voltage	Vos	(Note 1)	<b>-50</b>	0.1	+50	mV
Control Threshold Voltage	V2H, V3H,	(1000 2)	2.3	U	T 50	V
I SHE LITTLE WAY	V2L, V3L		2.0		0.7	V
Control Input Current	12, 13		-20	-6	0.1	
Voltage Gain	GV	$f = 1MHz, V_{IN} = 2Vpp (Note 1)$	5.6	6.1	6.4	μA dB

Continued on next page.

#### Package Dimensions



SANYO: SIP9



Continued from preceding pag	e.					
		•	min	typ	max	unit
Frequency Characteristic	GV-f	$ \begin{cases} 0dB & \text{at } f = 100kHz \text{ (Note 1)} \\ f = 10MHz, V_{IN} = 1Vpp \end{cases} $	-3	0		dB
Output Dynamic Range	$ m V_{DR}$	$f = 15kHz, V_{IN} = 1.5p-p (Note 1)$	1.4	1.5		Vpp
Crosstalk (Note 2)	CT	$V_{IN} = 1 V_{p-p,f} = 3 MHz $ (Note 1)	50	58		dB
			(48)	(55)		
		$V_{IN} = 1 V_{p-p,f} = 5 MHz$ (Note 1)	45	<b>5</b> 5		dB
			(45)	(52)		

<sup>\*</sup> The current flowing into the IC is defined as positive and current from the IC is defined as negative.

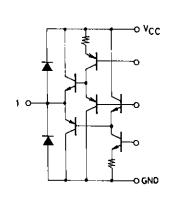
## Video Switch Truth Table

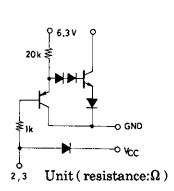
S2 (Pin 2)	S3 (Pin 3)	V <sub>IN</sub> 1 (Pin 4)	V <sub>IN</sub> 2 (Pin 6)	V <sub>IN</sub> 3 (Pin 8)	V <sub>IN</sub> 4 (Pin 9)
Н	Н	ON	OFF	OFF	OFF
L	Н	OFF	ON	OFF	OFF
H	L	OFF	OFF	ON	OFF
L	L	OFF	OFF	OFF	ON

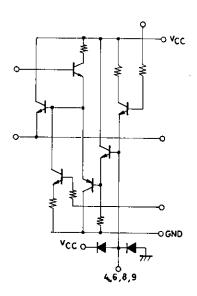
Note 1: Refer to this Truth Table and make measurements by switching S2, S3.

Note 2:( ): Crosstalk between pins 8 and 9

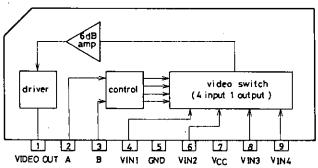
## Input/Output Equivalent Circuit



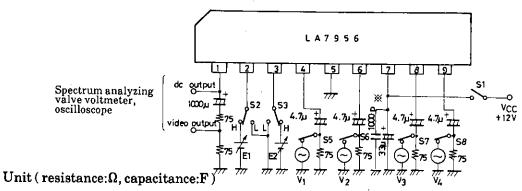




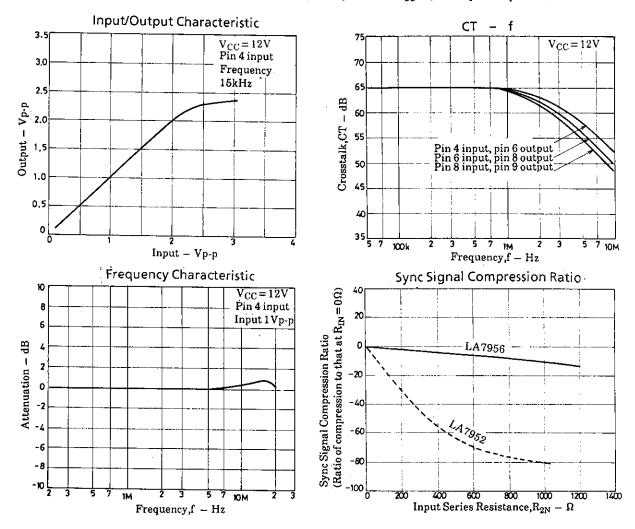
## Equivalent Circuit Block Diagram







 $\times$ : Connect the bypass capacitor for  $V_{CC}$  as close to pin 7 as possible.



#### Design Notes

An improvement in the DC clamp circuit has improved the sync signal compression attributable to the signal source impedance, but the response time of the DC clamp is made longer accordingly than that of the LA7952. Make adjustments by connecting a high resistance (several hundred  $k\Omega$ ) across input pin and GND (decreasing the resistance makes the sync signal compression larger).

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