Monolithic Digital IC

SANYO

No. 4528A

LB1643

Forward/Reverse Motor Driver with Brake

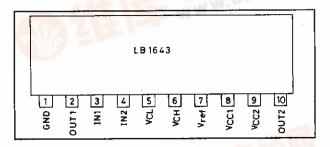
#### **OVERVIEW**

The LB1643 is a forward/reverse motor driver IC. The direction and brake functions are controlled from a single input, and the output voltages can be set by resistors. Further, the output voltage has 3 modes, full, HIGH-level and LOW-level voltage modes. The output voltage can be controlled from a single input and a microcontroller interface.

#### **FEATURES**

- Single-input forward, reverse and brake functions
- Output voltage can be set using resistors
- Single-input full-drive, HIGH-level drive and LOW-level drive select function
- · Microcontroller interface
- Built-in surge-current absorption components
- Built-in reference voltage circuit
- Built-in thermal protection circuit

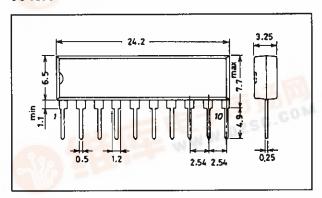
### PIN ASSIGNMENT

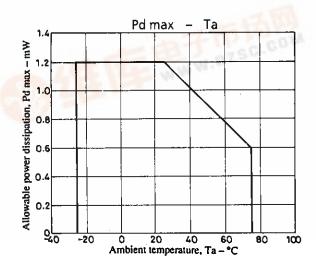


### PACKAGE DIMENSIONS

Unit: mm

#### 3043A

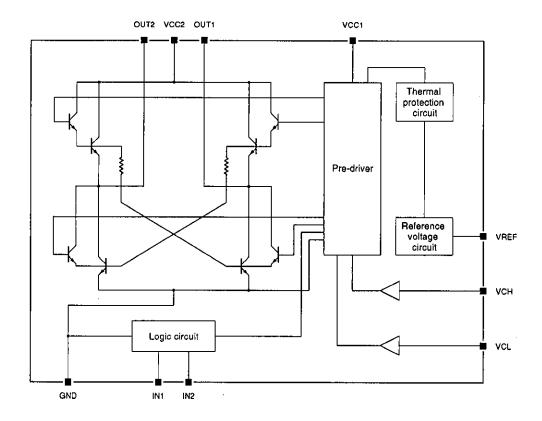






SANYO Electric Co., Ltd. Semiconductor Business Headquarters
TOKYO OFFICE Tokyo Bldg., 1-10, 1 Chome, Ueno, Taito-ku, TOKYO, 110 JAPAN

### **BLOCK DIAGRAM**



# PIN DESCRIPTION

| Number | Name | Equivalent circuit                  | Description   |
|--------|------|-------------------------------------|---|
| 1      | GND  |                                     | Power and signal ground   |
| 3      | IN1  | VCC1                                | Output voltage control input terminal $V_M\cong 2.5\ V$ when input is open.             |
| 4      | IN2  | VCC1 25,uA 519k 75 k 25,uA 513k GND | Forward, reverse and brake control input terminal $V_M\cong 2.5\ V$ when input is open. |
| 5      | VcL  | VCC1 GND                            | Output voltage set terminal   |

Unit (resistance:  $\Omega$ )

| Number | Name | Equivalent circuit | Description   |
|--------|------|--------------------|---|
| 6      | Vсн  | VCC1 GND           | Output voltage set terminal                         |
| 7      | Vref | VCC1 TO GND        | Reference voltage output. V <sub>ref</sub> = 6.35 V |
| 8      | VCC1 |                    | Signal voltage supply                               |
| 9      | VCC2 |                    | Power voltage supply                                |
| 2      | OUT1 | VCC2               | Motor coil connection terminals                     |
| 10     | OUT2 | Out2 Out1          | MODI CONTECTION TERMINALS                           |

Unit (resistance:  $\Omega$  )

## **SPECIFICATIONS**

## **Absolute Maximum Ratings**

 $T_a = 25 \, ^{\circ}C$ 

| Parameter                   | Symbol              | Condition | Rating      | Unit |
|-----------------------------|---------------------|-----------|-------------|------|
| Supply voltage              | V <sub>CC</sub> max |           | 18          | ٧    |
| Input voltage range         | V <sub>IN</sub>     | Vcc > VIN | -0.3 to +6  | - V  |
| Output current              | lout                |           | ±1.6        | A    |
| Power dissipation           | P <sub>d</sub> max  |           | 1.2         | w    |
| Operating temperature range | Topr                |           | -25 to +75  | °C   |
| Storage temperature range   | T <sub>stg</sub>    |           | -55 to +125 |      |

# **Recommended Operating Conditions**

 $T_a = 25 \, ^{\circ}C$ 

| Parameter                               | Symbol           | Condition                           | Rating    | Unit |
|---|------------------|-------------------------------------|-----------|------|
| Comply veltage venges                   | Vccı             |                                     | 8.0 to 18 | ٧    |
| Supply voltage ranges                   | V <sub>CC2</sub> | V <sub>CC1</sub> ≥ V <sub>CC2</sub> | 5 to 18   | ٧    |
| Forward-reverse direction prohibit time | toff             |                                     | ≥ 20      | μ\$  |

## **Electrical Characteristics**

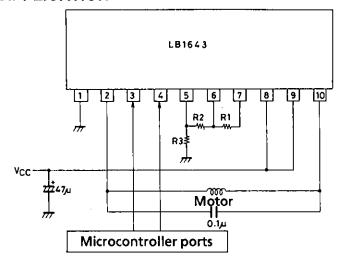
 $T_a = 25$  °C,  $V_{CC} = 12$  V

|  | Symbol                                  | A - 414.   | Rating |      |      | 11-2 |
|--|---|--|--------|------|------|------|
| Parameter                              |   | Condition  | min    | typ  | max  | Unit |
| LOW-level input voltage                | V <sub>INL</sub>                        |  | 0      | -    | 1.0  | ٧    |
| HIGH-level input voltage               | Vinh                                    |  | 4.2    | -    | 6.0  | ٧    |
| MID-level input voltage                | VINM                                    |  | 2.0    | _    | 3.0  | ٧    |
| Input impedance                        | Zin                                     |  | _      | 75   |      | kΩ   |
| Current consumption                    | lac                                     |  | -      | 5.5  | 10   | mA   |
|  | Vout1                                   | $R_L = 60 \Omega$ , $V_{CH} = 2.5 V$ , $V_{IN1} = 2.5 V$ , $V_{IN2} = 0 V$   | 4.4    | 4.95 | 5.4  | V    |
| Output voltages .                      | V <sub>OUT2</sub>                       | $R_L = 60 \Omega$ , $V_{CH} = 2.5 V$ , $V_{IN1} = 2.5 V$ , $V_{IN2} = 5.0 V$ | 4.4    | 4.95 | 5.4  | ٧    |
| Output leakage current                 | loL                                     | R <sub>L</sub> = ∞   | _      | 0.01 | 1.0  | mA   |
|  | V <sub>sat11</sub>                      | Vcc = 12 V,<br>lout = 300 mA   | -      | 1.9  | 2.2  | ٧    |
| Saturation voltages (upper)            | V <sub>sat12</sub>                      | V <sub>CC</sub> = 12 V,<br>I <sub>OUT</sub> = 500 mA                         | -      | 1.9  | 2.3  | ٧    |
|  | Vsat21                                  | Vcc = 12 V,<br>lout = 300 mA   | _      | 0.25 | 0.5  | ٧    |
| Saturation voltages (lower)            | V <sub>sat22</sub>                      | Vcc = 12 V,<br>lout = 500 mA   | _      | 0.4  | 0.65 | ٧    |
| Reference voltage                      | V <sub>ref</sub>                        |  | 6.0    | 6.35 | 6.8  | ٧    |
| Reference voltage load characteristics | $\frac{\Delta V_{ref}}{\Delta l_{ref}}$ | I <sub>ref</sub> = -2.0 to 0 mA  | _      | 0.05 | 0.1  | V/mA |
| Control to output gain                 | Vout/VcH<br>Vout/VcL                    | $V_{CH} = 2.5 \text{ V, } V_{CL} = 2.5 \text{ V,}$<br>$R_L = 60 \Omega$      | 1.5    | 1.90 | 2.4  | -    |
| Thermal shuldown temperature           | T <sub>TSD</sub>                        | See note.  | 150    | 180  | _    | °C   |

### Note

Design value only (not measured).

### TYPICAL APPLICATION



Unit (Capacitance: F)

#### Notes

- 1. Use a microcontroller with CMOS output ports for HIGH-level, LOW-level and open-circuit conditions.
- 2. It is recommended that R1, R2 and R3 total approximately 60 k $\Omega$ .
- 3. The input voltage for IN1 and IN2 should be 0 to 6 V to ensure the output voltage does not cause incorrect operation. Furthermore, IN1 and IN2 voltages should not be applied if the  $V_{CC}$  supply is not applied.
- 4. To avoid occurrences where both the upper and lower transistors are ON simultaneously, make sure IN1 or IN2 is open for a period of tens of ms before switching control to the opposite device.
- 5. Connect a 20  $\mu F$  or larger capacitor between VCC and GND.
- 6. A large current of several hundred mA flows in the motor circuits when the motor is being driven. Accordingly, the output current line and the input circuit should be wired so that they do not have a common impedance.

### LOGIC TABLE

| Input |      | Output voltage                |                  | Operation         |  |
|-------|------|-------------------------------|------------------|-------------------|--|
| IN1   | IN2  | OUT1                          | OUT2             | Operation         |  |
| HIGH  | HIGH | LOW                           | FULL             |                   |  |
| MID   | HIGH | LOW                           | 2V <sub>CH</sub> | Forward (reverse) |  |
| LOW   | HIGH | LOW                           | 2V <sub>CL</sub> | e e               |  |
| HIGH  | MID  | OFF                           | OFF              |                   |  |
| MID   | MID  | OFF                           | OFF              | Brake             |  |
| LOW   | MiD  | OFF                           | OFF              |                   |  |
| HIGH  | LOW  | FULL                          | LOW              |                   |  |
| MID   | LOW  | <sup>'</sup> 2V <sub>CH</sub> | LOW              | Reverse (forward) |  |
| LOW   | LOW  | 2V <sub>CL</sub>              | LOW              |                   |  |

Input levels are

- $V_H \ge 4.2 \text{ V}$
- $V_M = 2.0$  to 3.0 V
- $V_L \le 1.0 \text{ V}$

When IN1 and IN2 are open, they take on a voltage of 2.5 V.

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