Monolithic Digital IC

LB1668, 1668M, LB1667, 1667M WWW.DZSC.CO

Two-Phase Unipolar Drive Brushless Motor Drivers

Preliminary

Overview

The LB1668 Series are 2-phase unipolar drive brushless motor drivers that feature a wide usable voltage range and a minimal number of required external components. They also support the formation of motor lock protection and automatic recovery circuits.

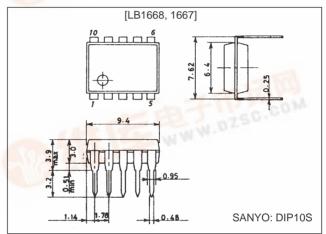
Functions and Features

- · Output protection Zener diodes with variable breakdown voltages
 - When the Z1 and Z2 pins are open: $V_{OLM} = 57 V$
 - When the Z1 and Z2 pins are shorted: $V_{OLM} = 32 \text{ V}$
 - An external Zener diode can be connected between Z1 and V_{CC}.
- Can support both 12-V and 24-V power supplies by changing an external resistor.
- Hall elements can be connected directly.
- 1.5-A output current output transistors built in
- Built-in rotation detection function that outputs low when driven and high when stopped (LB1668/M)
- FG signal function that sets FG high when IN- is low, and sets FG low when IN- is high (LB1667/M)
- · Motor lock protection and automatic recovery functions built in WWW.DZSC.COM
- Thermal shutdown function

Package Dimensions

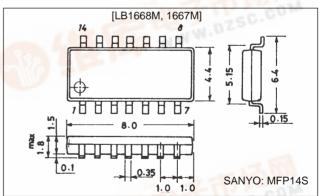
unit: mm

3098B-DIP10S



unit: mm

3111-MFP14S



*: These specifications are subject to change for improvement without notice.

Specifications Absolute Maximum Ratings at $Ta = 25^{\circ}C$

| Parameter | Symbol | Conditions | Ratings | Unit |
|-----------------------------|----------------------------------|---------------------------------------------------------------------------------------------------------------|-------------|------|
| Maximum input current | I _{CC} max | t ≤ 20 ms | 200 | mA |
| Output voltage | Vout | COM | Internal | V |
| Output current | IOUT | | 1.5 | A |
| Output minus current | I _{OM} | | -50 | mA |
| RD/FG influx current | I _{RD} /I _{FG} | | 10 | mA |
| RD/FG voltage | V _{RD} /V _{FG} | | 30 | V |
| Allowable power dissipation | Pd max | LB1667M, 1668M: Mounted on a printed circuit board ($20 \times 15 \times 1.5$ mm ³ , glass epoxy) | 0.8 | w |
| | | LB1667, 1668 | 1.1 | W |
| Operating temperature | Topr | | -30 to +80 | °C |
| Storage temperature | Tstg | | -55 to +150 | °C |



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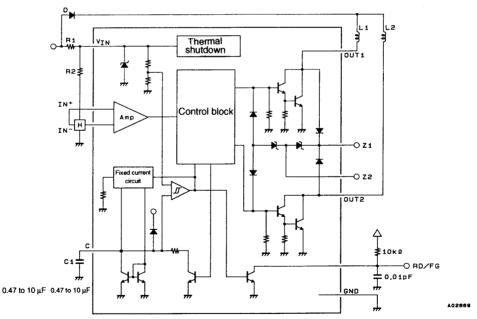
Allowable Operating Ranges at $Ta = 25^{\circ}C$

| Parameter | Symbol | Conditions | Ratings | Unit |
|---------------------------------|-----------------|------------|---------------------------|------|
| Input current range | I _{CC} | | 6.0 to 50 | mA |
| Common-mode input voltage range | VICM | | 0 to V _{IN} –1.5 | V |

Electrical Characteristics at Ta = 25°C, I_{CC} = 10 mA

| Parameter | Symbol | Conditions | Ratings | | | Linit |
|------------------------------------------------|-----------------------------------------|-----------------------------------------|---------|---------------------|------|-------|
| Falanelei | | | min | typ | max | Unit |
| | V _{OLM} 1 | Z1, Z2 open | 54 | 57 | 60 | V |
| Output limit voltage | V _{OLM} 2 | Z1, Z2 short | 31 | 33 | 35 | V |
| | Vosat1 | I _O = 0.5 A | | 0.95 | 1.2 | V |
| Output saturation voltage | Vosat2 | I _O = 1.0 A | | 1.15 | 1.5 | V |
| | Vosat3 | I _O = 1.5 A | | 1.4 | 2.0 | V |
| V _{IN} voltage | V _{IN} | I _{CC} = 7.0 mA | 6.4 | 6.7 | 7.0 | V |
| Amplifier input offset voltage | V _{OFF} | | -7.0 | 0 | +7.0 | mV |
| Amplifier input bias current | I _{BA} | | -250 | | | nA |
| RD/FG output saturation voltage | V _{RD} sat/V _{FG} sat | I _{RD} /I _{FG} = 5 mA | | 0.1 | 0.3 | V |
| C outflow current | I _C 1 | | 2.1 | 3.0 | 3.9 | μA |
| C discharge current | I _C 2 | | 0.31 | 0.44 | 0.59 | μA |
| | V _{TH} 1 | | 0.77 | 0.8V _{IN} | 0.83 | V |
| Comparator input threshold voltage | V _{TH} 2 | | 0.42 | 0.45V _{IN} | 0.48 | V |
| Thermal shutdown current operating temperature | TSD | Design target value | | 180 | | °C |
| Thermal shutdown circuit hysteresis | ΔTSD | Design target value | | 40 | | °C |

Block Diagram and Application Circuit



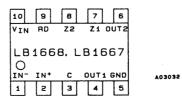
Truth Table (LB1667, LB1667M)

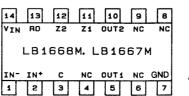
| | IN+ | IN– | С | OUT1 | OUT2 | FG |
|---|-----|-----|---|------|------|----|
| | Н | L | L | Н | L | Н |
| | L | Н | L | L | Н | L |
| Ī | Н | L | н | н | н | н |
| | L | Н | Н | Н | Н | Н |

Truth Table (LB1668, LB1668M)

| IN+ | IN– | С | OUT1 | OUT2 | RD |
|-----|-----|---|------|------|----|
| Н | L | L | Н | L | L |
| L | н | L | L | Н | L |
| Н | L | Н | Н | Н | н |
| L | Н | н | н | н | н |

Pin Assignments

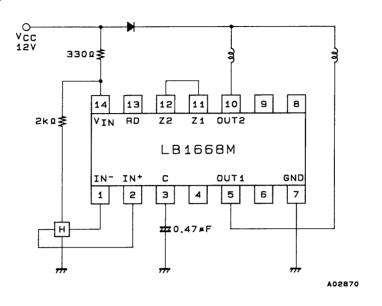




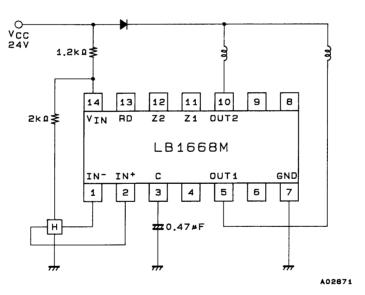
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LB1668M Application Circuits

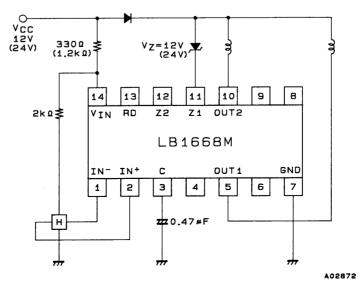
• 12-V power supply type



• 24-V power supply type

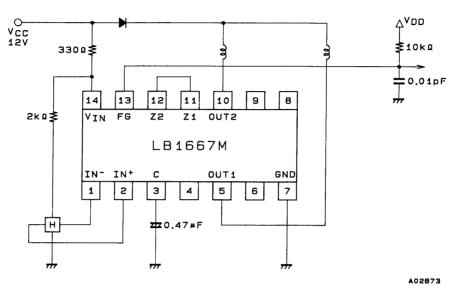


• Circuit for use when large output currents are required and heat dissipation is high.

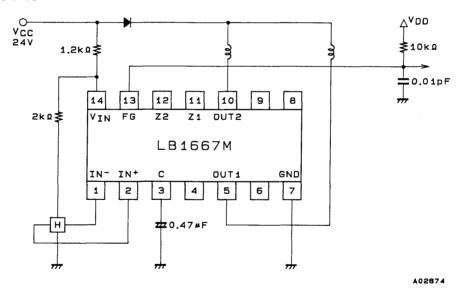


LB1667M Application Circuits

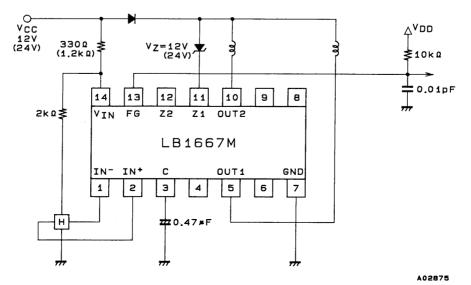
• 12-V power supply type



• 24-V power supply type



• Circuit for use when large output currents are required and heat dissipation is high.



LB1668, LB1668M, LB1667, LB1667M

Design Documentation (See the application circuit diagrams)

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1. Power-supply voltage (V<sub>IN</sub> pin)
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The resistor R1 (when $V_{CC} = 12 \text{ V R1} = 330 \Omega$, and when $V_{CC} = 24 \text{ R1} = \text{about } 1.2 \text{ k}\Omega$) is inserted between V_{IN} and the power supply V_{CC} pin. When the I_{CC} current is set in the range 6 to 50 mA, the V_{IN} pin will be regulated to be 6.7 V. Not only does this provide stability with respect to power-supply voltage variations and motor kickback, but it also provides adequate strength to withstand surges.

2. Output transistors (OUT1 and OUT2 pins)

A Zener diodes with the following characteristics is inserted between the collector and base of each output transistor to absorb kickback voltages at 57 V (typical) and provide output protection.

 $\begin{array}{ll} \text{Sustained output voltage} & : \text{V}_{\text{O}} = 65 \text{ V minimum (design guarantee)} \\ \text{Output current} & : \text{I}_{\text{O}} = 1.5 \text{ A maximum} \\ \text{Output saturation voltage} & : \text{V}_{\text{O}} \text{ sat} = 1.25 \text{ V}/1.0 \text{ A (typical)} \\ \text{Safe operating range} & : \text{I}_{\text{O}} = 1.0 \text{ A}, \text{V}_{\text{OLM}} = 57 \text{ V}, \text{t} = 200 \, \mu\text{s} \\ \end{array}$

3. Output circuit kickback voltage protection (Z1 and Z2 pins)

These ICs support output protection that minimizes kickback noise by changing the kickback absorption voltage and absorption method according to the output current and power-supply voltage used.

—When the Z1 and Z2 pins are shorted

The output protection voltage will be 32 V (typical) using a $V_{CC} = 12$ V power supply.

—When the Z1 and Z2 pins are open

The output protection voltage will be 57 V (typical) using a $V_{CC} = 24$ V power supply.

-With a Zener diode inserted between Z1 and V_{CC} or between Z1 and ground

This technique handles 120 mm H speed applications which require large output currents and involve large amounts of heat generated in the IC by dissipating the motor coil switching loss in external Zener diodes.

4. Output protection when the motor is lock (C, RD, and FG pins)

This circuit detects motor stopping due to, for example, overloading, and cuts the coil current. It also automatically recovers drive and motor turning from the output stopped state when the load returns to an appropriate level. The lock detection time is set with the value of an external capacitor.

When $C = 0.47 \ \mu F$

Lock detection time: about 1 second

Lock protection time (output on): about 0.5 second

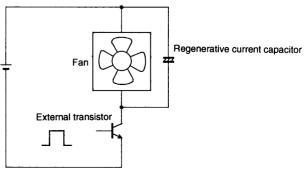
(output off): about 3 seconds

The RD pin (LB1668) is an open collector output and outputs a low level during drive and a high level when stopped. The FG pin (LB1667) is an open collector output and is switched between high and low by the drive phase. It goes to the high level when the motor is stopped.

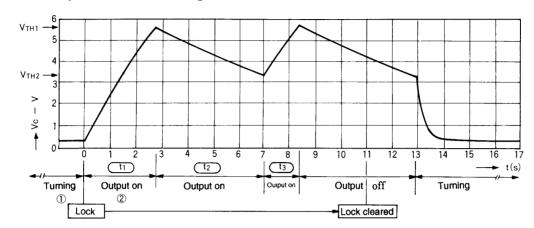
5. Thermal shutdown

This circuit turns the output off in response to coil shorting or IC overheating.

6. In applications that use an external transistor to turn the cooling fan power on and off, connect a capacitor of about 0.47 to 10 μF between the fan power supply V_{CC} and ground to provide a regenerative route for the fan motor coil current.

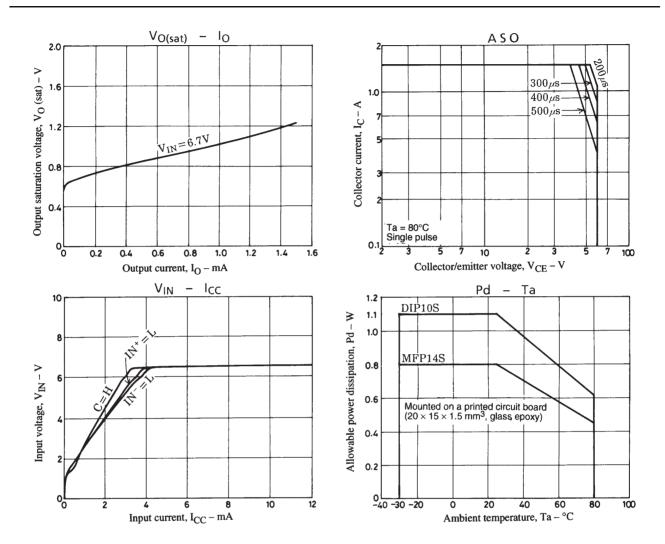


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Automatic Recovery Circuit C-Pin Voltage

- 1. While the blades are turning, the capacitor is charged with a current of about 3 μ A (typical), and C is discharged by pulses that correspond to the motor speed.
- 2. When the blades lock, the capacitor is no longer discharged, and the voltage across the capacitor increases. The output is turned off when that voltage reaches $0.8 \times V_{IN}$.
- 3. When the output is turned off, the capacitor is discharged at about 0.44 μ A (typical). When the capacitor voltage falls under V_{TH}2, if the lock state is not yet cleared the capacitor continues discharging until V_{TH}1. (Note that the output is turned on at this time.) These operations, i.e. items 2. and 3., are repeated with a ton:toff ratio of about 1:6 to protect the motor.
- 4. If the lock is cleared at the point the capacitor voltage reaches V_{TH}^2 , motor rotation is started by turning the output on.



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