



查询LBA120LSTR供应商

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LBA120L DUAL POLE OptoMOS® Relay



	LBA120L	Units
Load Voltage	250	V
Load Current	170	mA
Max R _{ON}	20	Ω

Features

- Small 8 Pin DIP Package
- Low Drive Power Requirements (TTL/CMOS Compatible)
- No Moving Parts
- High Reliability
- Arc-Free With No Snubbing Circuits
- 3750V_{RMS} Input/Output Isolation
- FCC Compatible
- VDE Compatible
- No EMI/RFI Generation
- Machine Insertable, Wave Solderable
- Current Limiting Surface Mount and Tape & Reel Versions Available

Applications

- Telecommunications
 - Telecom Switching
 - Tip/Ring Circuits
 - Modem Switching (Laptop, Notebook, Pocket Size)
 - Hookswitch
 - Dial Pulsing
 - Ground Start
 - Ringer Injection
- Instrumentation
 - Multiplexers
 - Data Acquisition
 - Electronic Switching
 - I/O Subsystems
 - Meters (Watt-Hour, Water, Gas)
- Medical Equipment-Patient/Equipment Isolation
- Security
- Aerospace
- Industrial Controls

Description

LBA120L is 250V, 170mA, 20Ω independent 1-Form-A and 1-Form-B relays. It features low on-resistance combined with enhanced peak load current handling capabilities. Current limiting version is available ("L" suffix, see specification for variations in performance).

Approvals

- UL Recognized: File Number E76270
- CSA Certified: File Number LR 43639-10
- BSI Certified to:
 - BS EN 60950:1992 (BS7002:1992)
Certificate #: 7344
 - BS EN 41003:1993
Certificate #: 7344

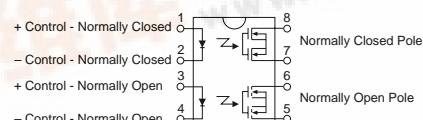
Ordering Information

Part #	Description
LBA120L	8 Pin DIP
LBA120LS	8 Pin Surface Mount (50/Tube)
LBA120LSTR	8 Pin Surface Mount (1000/Reel)

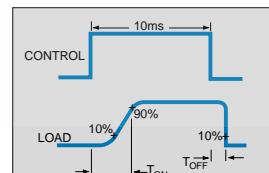
Pin Configuration

LBA120L Pinout

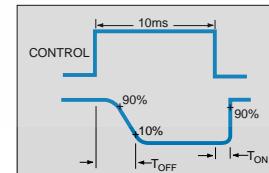
AC/DC Configuration



Switching Characteristics of
Normally Open (Form A) Devices



Switching Characteristics of
Normally Closed (Form B) Devices



Absolute Maximum Ratings (@ 25° C)

Parameter	Min	Typ	Max	Units
Input Power Dissipation	-	-	150 ¹	mW
Input Control Current Peak (10ms)	-	-	50 1	mA A
Reverse Input Voltage	-	-	5	V
Total Power Dissipation	-	-	800 ²	mW
Isolation Voltage Input to Output	3750	-	-	V _{RMS}
Operational Temperature	-40	-	+85	°C
Storage Temperature	-40	-	+125	°C
Soldering Temperature DIP Package	-	-	+260	°C
Surface Mount Package (10 Seconds Max.)	-	-	+220	°C

¹ Derate Linearly 1.33 mW/°C² Derate Linearly 6.67 mW/°C

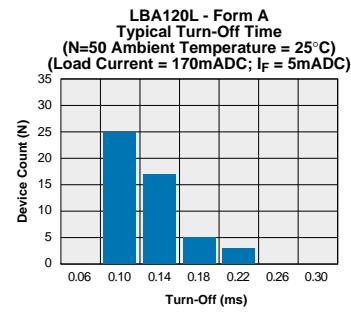
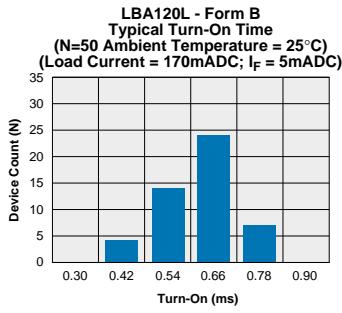
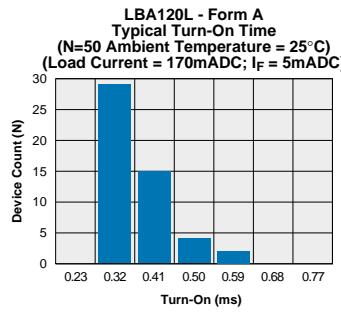
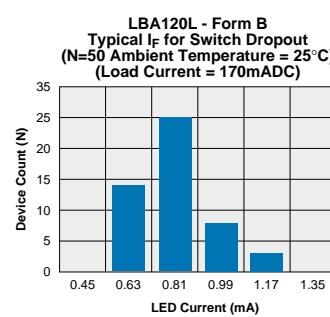
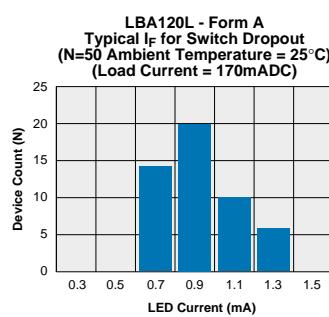
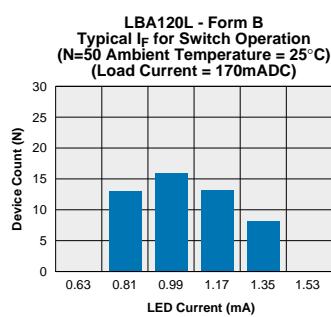
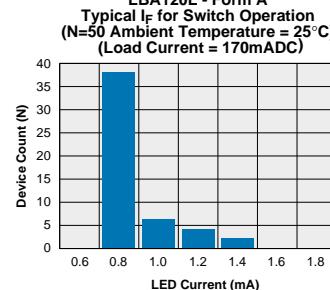
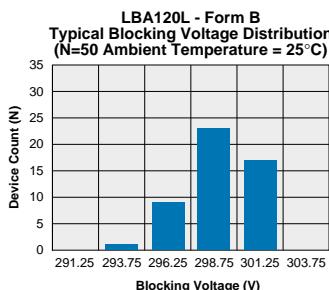
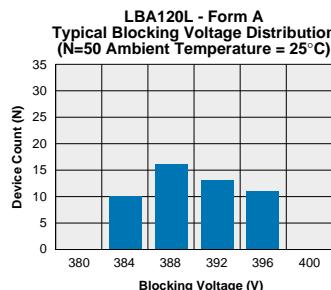
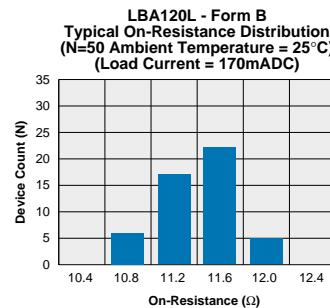
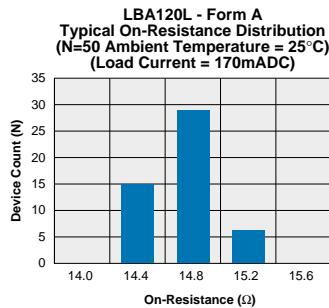
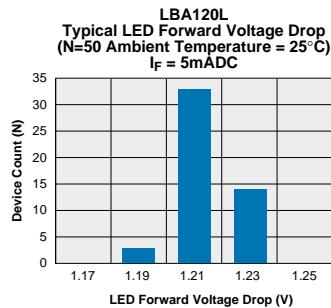
Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at these or any other conditions beyond those indicated in the operational sections of this data sheet is not implied. Exposure of the device to the absolute maximum ratings for an extended period may degrade the device and effect its reliability.

Electrical Characteristics

Parameter	Conditions	Symbol	Min	Typ	Max	Units
Output Characteristics @ 25°C						
Load Voltage (Peak)	-	V _L	-	-	250	V
Load Current*(Continuous) AC/DC Configuration	-	I _L	-	-	150	mA
Peak Load Current	10ms	I _{LPK}	-	-	-	mA
On-Resistance AC/DC Configuration	I _L =170mA	R _{ON}	-	21	25	Ω
Off-State Leakage Current	V _L =250V	I _{LEAK}	-	-	1	μA
Switching Speeds						
Turn-On	I _F =5mA, V _L =10V	T _{ON}	-	-	5	ms
Turn-Off	I _F =5mA, V _L =10V	T _{OFF}	-	-	5	ms
Output Capacitance	50V; f=1MHz	C _{OUT}	-	50	-	pF
Load Current Limiting	Form A only	I _{CL}	190	235	280	mA
Input Characteristics @ 25°C						
Input Control Current	I _L =120mA	I _F	5	-	50	mA
Input Dropout Current	-	-	0.4	0.7	-	mA
Input Voltage Drop	I _F =5mA	V _F	0.9	1.2	1.4	V
Reverse Input Voltage	-	V _R	-	-	5	V
Reverse Input Current	V _R =5V	I _R	-	-	10	μA
Input to Output Capacitance	-	C _{I/O}	-	3	-	pF
Input to Output Isolation	-	V _{I/O}	3750	-	-	V _{RMS}

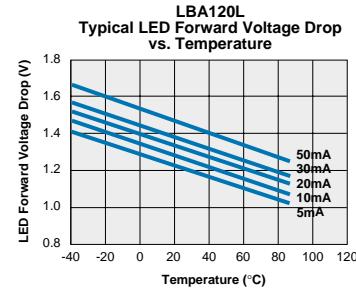
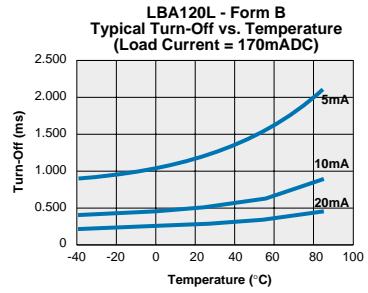
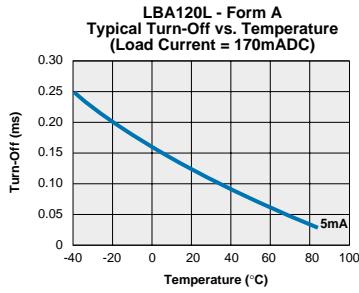
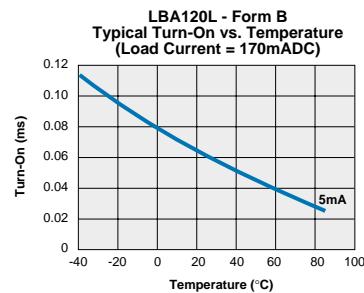
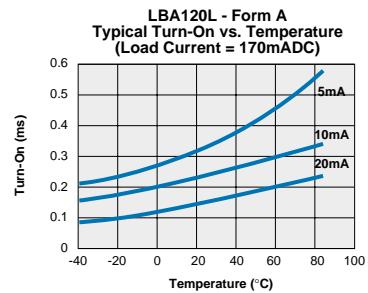
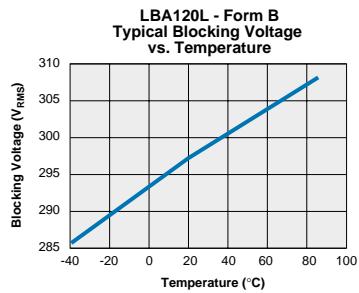
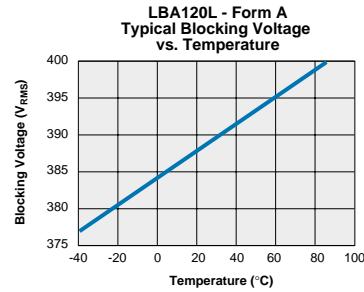
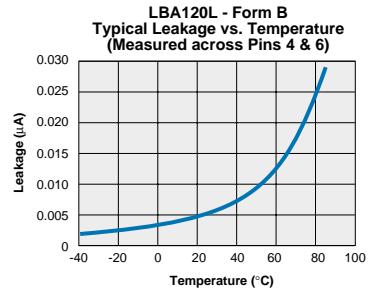
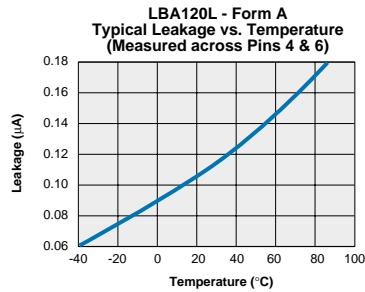
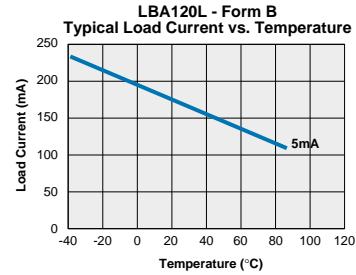
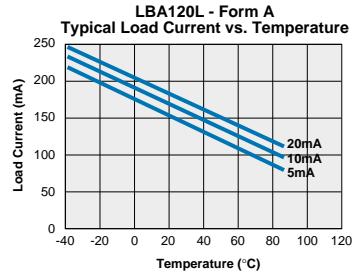
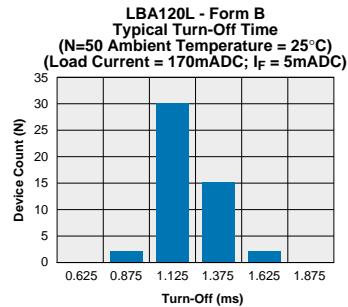
*NOTE: If both poles operate simultaneously load current must be derated so as not to exceed the package power dissipation value.

PERFORMANCE DATA*

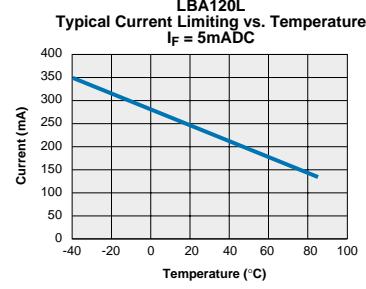
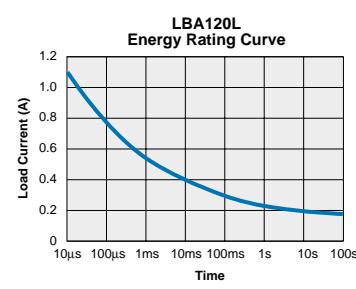
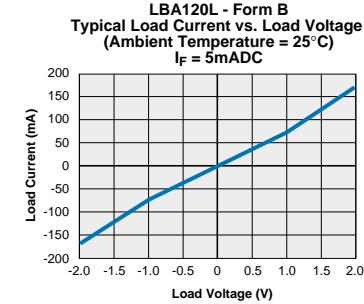
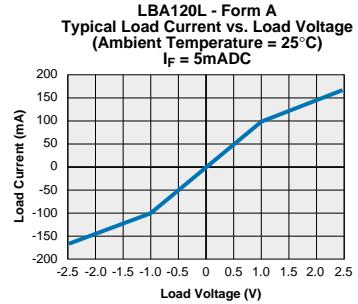
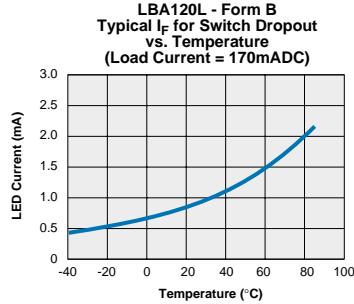
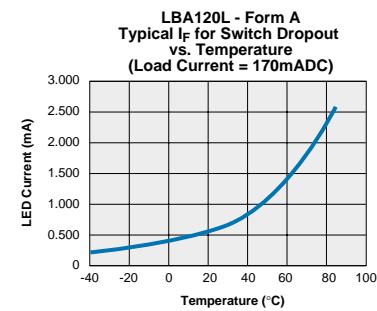
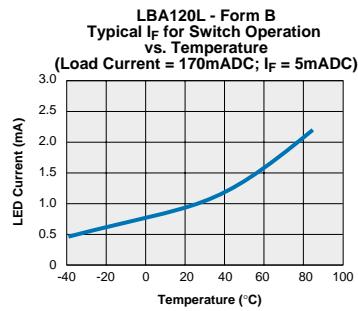
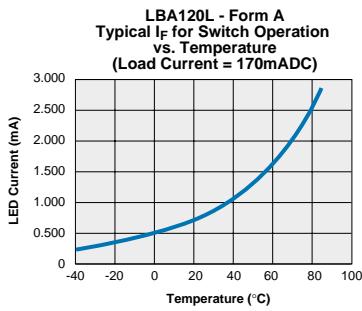
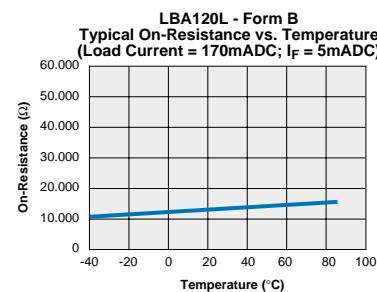
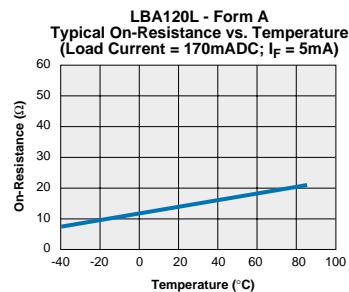
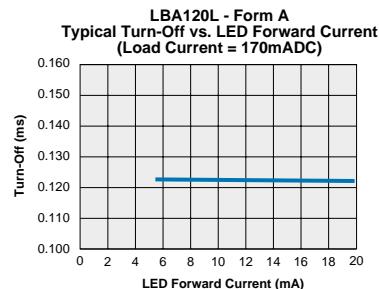
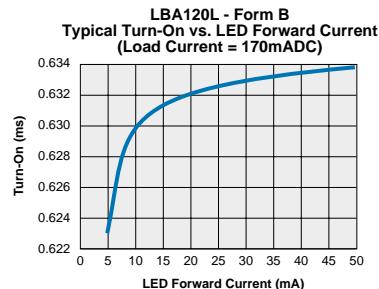
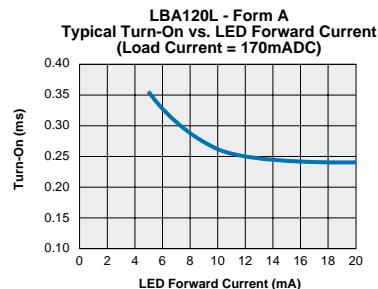


The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

PERFORMANCE DATA*



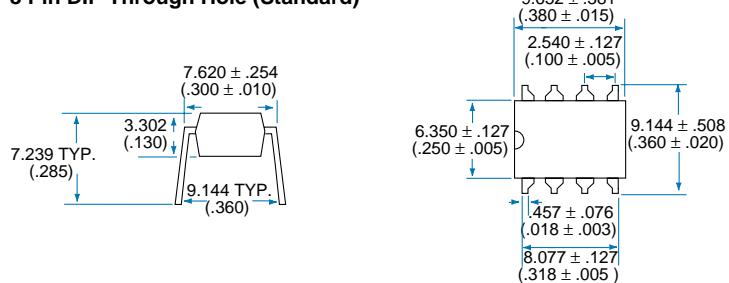
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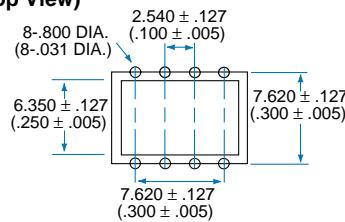
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Mechanical Dimensions

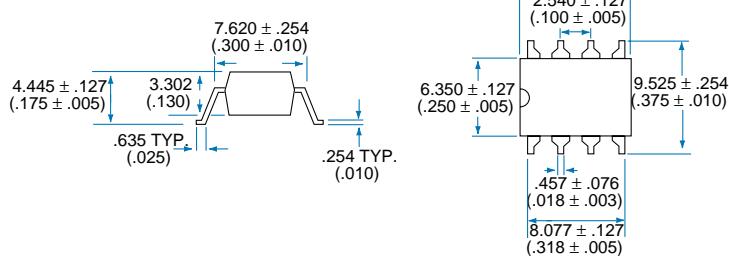
8 Pin DIP Through Hole (Standard)



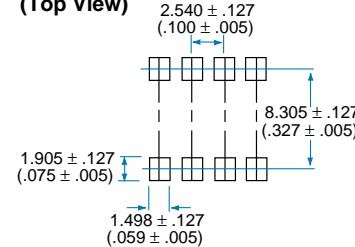
**PC Board Pattern
(Top View)**



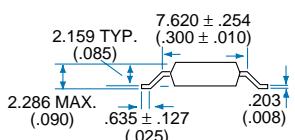
8 Pin DIP Surface Mount ("S" Suffix)



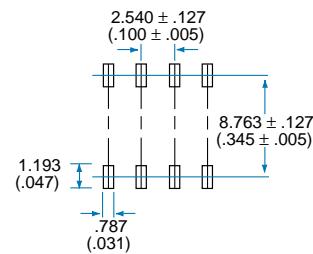
**PC Board Pattern
(Top View)**



8 Pin Flatpack ("P" Suffix)

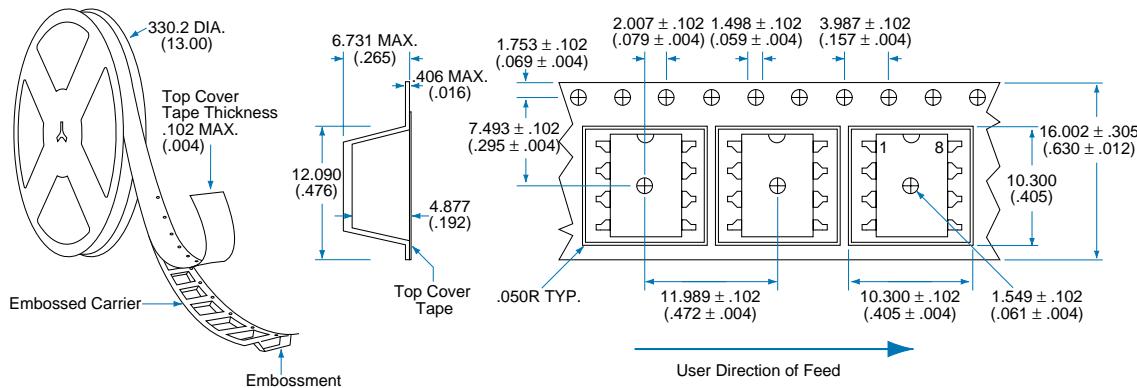


**PC Board Pattern
(Top View)**

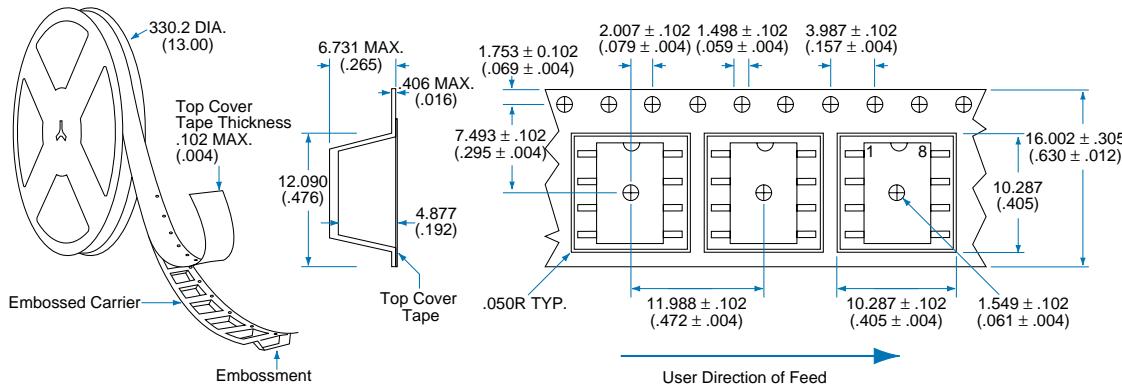


Mechanical Dimensions

Tape and Reel Packaging for 8 Pin Surface Mount Package



Tape and Reel Packaging for 8 Pin Flatpack Package





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