Ordering number : EN*5414A

CMOS LSI

LC82210, 82210L

Motion JPEG Codec

Preliminary

Overview

The LC82210 is a JPEG encoder/decoder that handles motion JPEG. When encoding, the LC82210 accepts an A/D converter generated digital video input signal and encodes that signal in real time. When decoding, the LC82210 decodes a compressed decoded signal and produces a digital video signal that can be directly input to a D/A converter.
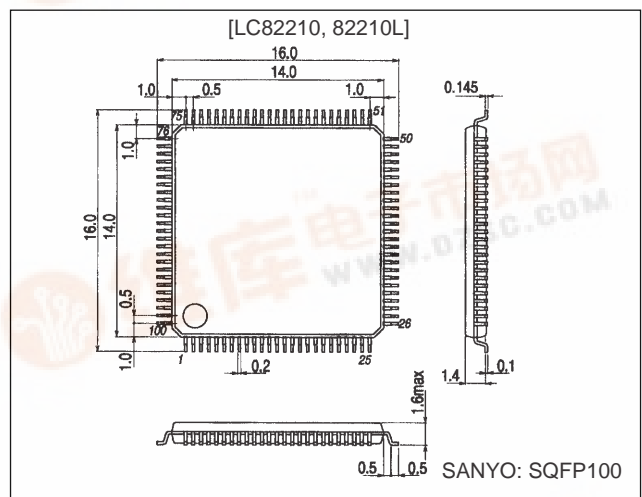
Features

- Encoding/decoding based on the JPEG standard
- The image size can be set arbitrarily.
- I/O image format $Y : U : V = 4 : 2 : 2$
- JPEG format $Y : U : V = 4 : 2 : 2$ or $Y : U : V = 4 : 1 : 1$
- Process 640×480 images 30 frames per second
- Memory control function for the image memory used for display, capture, and other image functions
- Supports the use of either 1M, 2M, 4M, 4M × 2 or 16M bit of DRAM for image memory.
- Supports fixed compression ratio encoding.
- Image memory can be accessed by external systems.
- Image memory compression function
- Supports a function for compressing/expanding part of an image.
- Supports both x86 and 68xx family CPUs. Also supports DMA transfers of compressed data.
- Low power mode
- Power-supply voltage: 3.3 V (internal) 5 V (pin) : LC82210
  3.3 V (single)   : LC82210L

Package Dimensions

unit: mm

3181B-SQFP100

[LC82210, 82210L]

SANYO: SQFP100

This catalog provides information as of January 1998. Specifications and information herein are subject to change without notice.

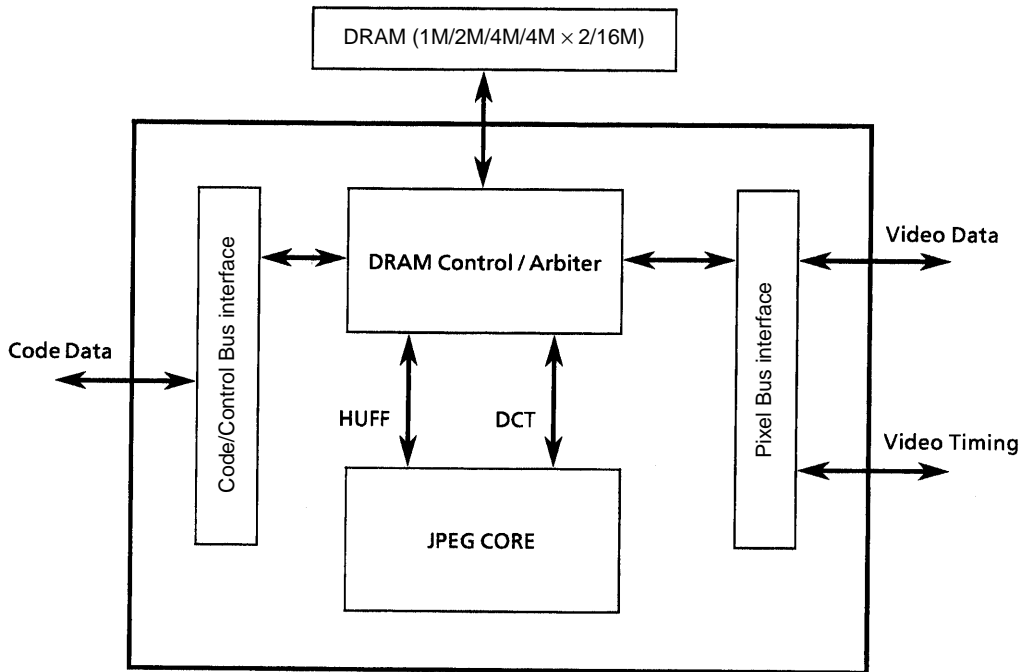## SANYO Electric Co.,Ltd. Semiconductor Bussiness Headquarters

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Block Diagram

Application System Example

Digital Camera / Video System