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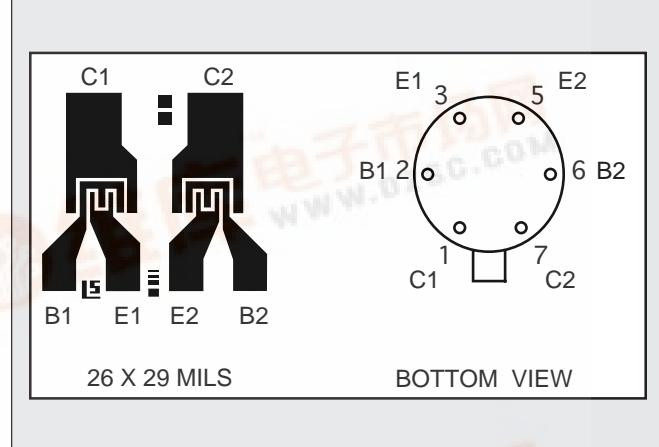
LINEAR SYSTEMS

Linear Integrated Systems

LS358

LOG CONFORMANCE MONOLITHIC DUAL PNP TRANSISTORS

FEATURES		
LOG CONFORMANCE		$\Delta r_e \leq 1\Omega$ from ideal TYP.
ABSOLUTE MAXIMUM RATINGS <u>NOTE 1</u> @ 25°C (unless otherwise noted)		
I_c	Collector Current	10mA
Maximum Temperatures		
Storage Temperature Range	-65°C to +200°C	
Operating Junction Temperature	+150°C	
Maximum Power Dissipation	ONE SIDE	BOTH SIDES
Device Dissipation @ Free Air	250mW	500mW
Linear Derating Factor	2.3mW/°C	4.3mW/°C

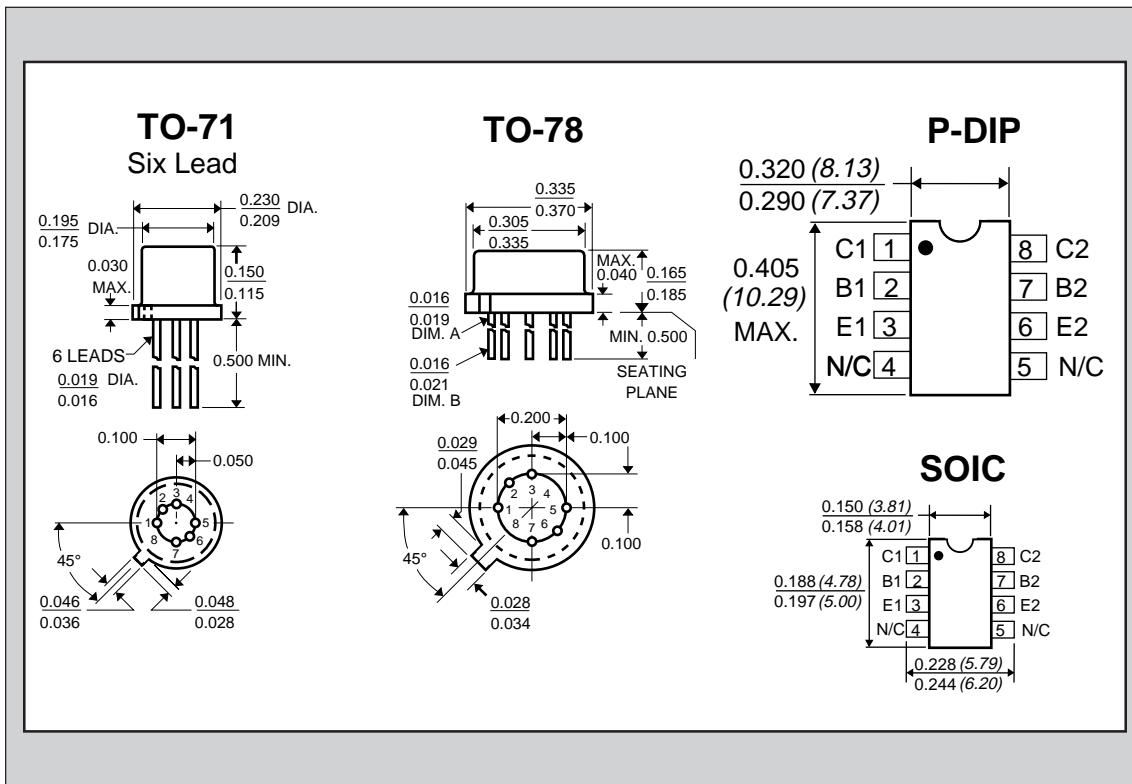


ELECTRICAL CHARACTERISTICS @ 25°C (unless otherwise noted)

SYMBOL	CHARACTERISTICS	LS358	UNITS	CONDITIONS
Δr_e	Log Conformance	1.5	Ω	$I_c = 10-100-1000\mu A$ $V_{CE} = 5V$
BV_{CBO}	Collector-Base Breakdown Voltage	20	MIN.	V $I_c = 10\mu A$ $I_E = 0$
BV_{CEO}	Collector to Emitter Voltage	20	MIN.	V $I_c = 10\mu A$ $I_B = 0$
BV_{EBO}	Emitter-Base Breakdown Voltage	6.2	MIN.	V $I_E = 10\mu A$ $I_c = 0$ <u>NOTE 2</u>
BV_{CCO}	Collector to Collector Voltage	45	MIN.	V $I_c = 10\mu A$ $I_E = 0$
h_{FE}	DC Current Gain	100 600	MIN. MAX.	$I_c = 10\mu A$ $V_{CE} = 5V$
h_{FE}	DC Current Gain	100 600	MIN. MAX.	$I_c = 100\mu A$ $V_{CE} = 5V$
h_{FE}	DC Current Gain	100	MIN.	$I_c = 1mA$ $V_{CE} = 5V$
$V_{CE(SAT)}$	Collector Saturation Voltage	0.5	MAX.	V $I_c = 1mA$ $I_B = 0.1 mA$
I_{CBO}	Collector Cutoff Current	0.2	MAX.	nA $I_E = 0$ $V_{CB} = 15V$
I_{EBO}	Emitter Cutoff Current	0.2	MAX.	nA $I_c = 0$ $V_{EB} = 3V$
C_{OBO}	Output Capacitance	2	MAX.	pF $I_E = 0$ $V_{CB} = 5V$
C_{C1C2}	Collector to Collector Capacitance	2	MAX.	pF $V_{cc} = 0$
I_{C1C2}	Collector to Collector Leakage Current	0.5	MAX.	nA $V_{cc} = \pm 45V$
f_T	Current Gain Bandwidth Product	200	MIN.	MHz $I_c = 1mA$ $V_{CE} = 5V$
NF	Narrow Band Noise Figure	3	MAX.	dB $I_c = 100\mu A$ $V_{CE} = 5V$ $BW = 200Hz$ $R_G = 10 K\Omega$ $f=1KHz$

MATCHING CHARACTERISTICS @ 25°C (unless otherwise noted)

SYMBOL	PARAMETER	LS358	UNITS	CONDITIONS
$ V_{BE1} - V_{BE2} $	Base Emitter Voltage Differential	0.4 1	TYP. MAX.	mV mV
$\Delta(V_{BE1} - V_{BE2})/{}^{\circ}\text{C}$	Base Emitter Voltage Differential Change with Temperature	1 10	TYP. MAX.	$\mu\text{V}/{}^{\circ}\text{C}$ $\mu\text{V}/{}^{\circ}\text{C}$
$ I_{B1} - I_{B2} $	Base Current Differential	5	MAX.	nA
$ \Delta(I_{B1} - I_{B2})/{}^{\circ}\text{C} $	Base Current Differential Change with Temperature	0.5	MAX.	nA/{}^{\circ}\text{C}
h_{FE1}/h_{FE2}	DC Current Gain Differential	5	TYP.	%
				$I_C = 10 \mu\text{A}$ $V_{CE} = 5\text{V}$



NOTES:

1. These ratings are limiting values above which the serviceability of any semiconductor may be impaired.
2. The reverse base-to-emitter voltage must never exceed 6.2 volts; the reverse base-to-emitter current must never exceed 10 μA .