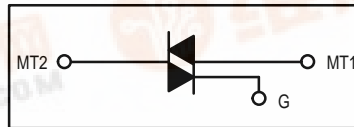


## Triacs

### Silicon Bidirectional Thyristors

... designed primarily for full-wave ac control applications, such as solid-state relays, motor controls, heating controls and power supplies; or wherever full-wave silicon gate controlled solid-state devices are needed. Triac type thyristors switch from a blocking to a conducting state for either polarity of applied anode voltage with positive or negative gate triggering.

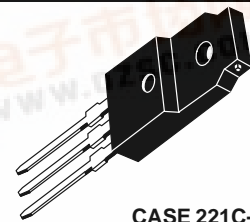
- Blocking Voltage to 800 Volts
- All Diffused and Glass Passivated Junctions for Greater Parameter Uniformity and Stability
- Small, Rugged, Thermowatt Construction for Low Thermal Resistance, High Heat Dissipation and Durability
- Gate Triggering Guaranteed in Three Modes (MAC15FP Series) or Four Modes (MAC15AFP Series)



## MAC15FP Series

## MAC15AFP Series

**ISOLATED TRIACs**  
**THYRISTORS**  
**15 AMPERES RMS**  
**200 thru 800 VOLTS**



CASE 221C-02  
 STYLE 3

#### MAXIMUM RATINGS (T<sub>J</sub> = 25°C unless otherwise noted.)

Rating	Symbol	Value	Unit
Repetitive Peak Off-State Voltage <sup>(1)</sup> (T <sub>J</sub> = -40 to +125°C, 1/2 Sine Wave 50 to 60 Hz, Gate Open)	V <sub>DRM</sub>		Volts
MAC15-4FP, MAC15A4FP		200	
MAC15-6FP, MAC15A6FP		400	
MAC15-8FP, MAC15A8FP		600	
MAC15-10FP, MAC15A10FP		800	
On-State RMS Current (T <sub>C</sub> = +80°C) <sup>(2)</sup> Full Cycle Sine Wave 50 to 60 Hz (T <sub>C</sub> = +95°C)	I <sub>T(RMS)</sub>	15 12	Amps
Peak Nonrepetitive Surge Current (One Full Cycle, 60 Hz, T <sub>C</sub> = +80°C) preceded and followed by rated current	I <sub>TSM</sub>	150	Amps
Peak Gate Power (T <sub>C</sub> = +80°C, Pulse Width = 2 μs)	P <sub>GM</sub>	20	Watts
Average Gate Power (T <sub>C</sub> = +80°C, t = 8.3 ms)	P <sub>G(AV)</sub>	0.5	Watt
Peak Gate Current	I <sub>GM</sub>	2	Amps
Peak Gate Voltage	V <sub>GM</sub>	10	Volts
RMS Isolation Voltage (T <sub>A</sub> = 25°C, Relative Humidity ≤ 20%)	V <sub>(ISO)</sub>	1500	Volts
Operating Junction Temperature	T <sub>J</sub>	-40 to +125	°C
Storage Temperature Range	T <sub>stg</sub>	-40 to +150	°C

1. V<sub>DRM</sub> for all types can be applied on a continuous basis. Blocking voltages shall not be tested with a constant current source such that the voltage ratings of the devices are exceeded.

2. The case temperature reference point for all T<sub>C</sub> measurements is a point on the center lead of the package as close as possible to the plastic body.

## MAC15FP Series MAC15AFP Series

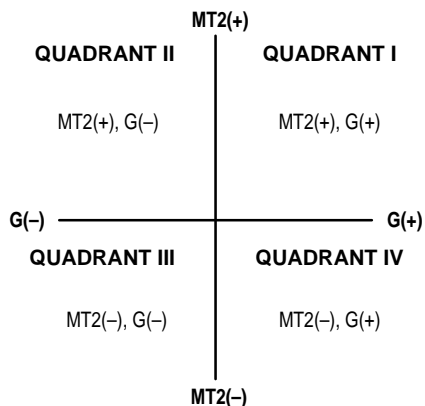
### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	$R_{\theta JC}$	2	$^{\circ}\text{C}/\text{W}$
Thermal Resistance, Case to Sink	$R_{\theta CS}$	2.2 (typ)	$^{\circ}\text{C}/\text{W}$
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	60	$^{\circ}\text{C}/\text{W}$

### ELECTRICAL CHARACTERISTICS ( $T_C = 25^{\circ}\text{C}$ unless otherwise noted.)

Characteristic	Symbol	Min	Typ	Max	Unit
Peak Blocking Current (Either Direction) $T_J = 25^{\circ}\text{C}$ ( $V_D = \text{Rated } V_{DRM}$ , $T_J = 125^{\circ}\text{C}$ , Gate Open)	$I_{DRM}$	—	—	10 2	$\mu\text{A}$ mA
Peak On-State Voltage (Either Direction) ( $I_{TM} = 21 \text{ A Peak}$ ; Pulse Width = 1 to 2 ms, Duty Cycle $\leq 2\%$ )	$V_{TM}$	—	1.3	1.6	Volts
Gate Trigger Current (Continuous dc) (Main Terminal Voltage = 12 Vdc, $R_L = 100 \text{ Ohms}$ ) MT2(+), G(+) MT2(+), G(-) MT2(-), G(-) MT2(-), G(+) "A" SUFFIX ONLY	$I_{GT}$	—	—	50 50 50 75	mA
Gate Trigger Voltage (Continuous dc) (Main Terminal Voltage = 12 Vdc, $R_L = 100 \text{ Ohms}$ ) MT2(+), G(+) MT2(+), G(-) MT2(-), G(-) MT2(-), G(+) "A" SUFFIX ONLY (Main Terminal Voltage = Rated $V_{DRM}$ , $R_L = 10 \text{ k}\Omega$ , $T_J = +110^{\circ}\text{C}$ ) MT2(+), G(+); MT2(-), G(-); MT2(+), G(-) MT2(-), G(+) "A" SUFFIX ONLY	$V_{GT}$	—	0.9 0.9 1.1 1.4	2 2 2 2.5	Volts
Holding Current (Either Direction) (Main Terminal Voltage = 12 Vdc, Gate Open, Initiating Current = 200 mA)	$I_H$	—	6	40	mA
Turn-On Time ( $V_D = \text{Rated } V_{DRM}$ , $I_{TM} = 17 \text{ A}$ , $I_{GT} = 120 \text{ mA}$ , Rise Time = 0.1 $\mu\text{s}$ , Pulse Width = 2 $\mu\text{s}$ )	$t_{gt}$	—	1.5	—	$\mu\text{s}$
Critical Rate of Rise of Commutation Voltage ( $V_D = \text{Rated } V_{DRM}$ , $I_{TM} = 21 \text{ A}$ , Commutating $di/dt = 7.6 \text{ A/ms}$ , Gate Unenergized, $T_C = 80^{\circ}\text{C}$ )	$dv/dt(c)$	—	5	—	$\text{V}/\mu\text{s}$

### QUADRANT DEFINITIONS



Trigger devices are recommended for gating on Triacs. They provide:

1. Consistent predictable turn-on points.
2. Simplified circuitry.
3. Fast turn-on time for cooler, more efficient and reliable operation.

### ELECTRICAL CHARACTERISTICS of RECOMMENDED BIDIRECTIONAL SWITCHES

Usage	General	
Part Number	MBS4991	MBS4992
$V_S$	6–10 V	7.5–9 V
$I_S$	350 $\mu\text{A}$ Max	120 $\mu\text{A}$ Max
$V_{S1}-V_{S2}$	0.5 V Max	0.2 V Max
Temperature Coefficient	0.02%/ $^{\circ}\text{C}$ Typ	

1. Ratings apply for open gate conditions. Thyristor devices shall not be tested with a constant current source for blocking capability such that the voltage applied exceeds the rated blocking voltage.

TYPICAL CHARACTERISTICS

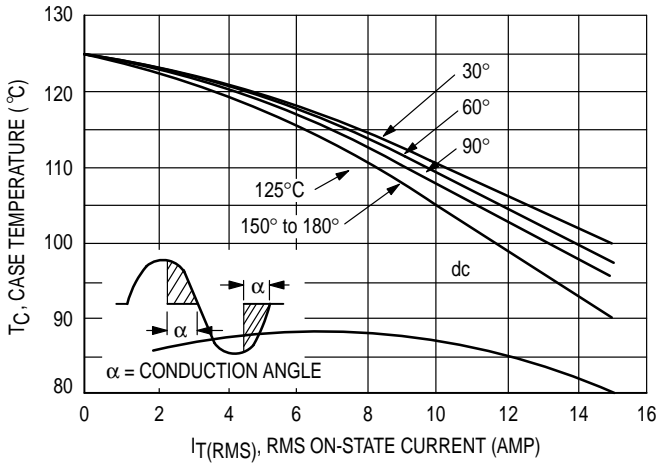


Figure 1. RMS Current Derating

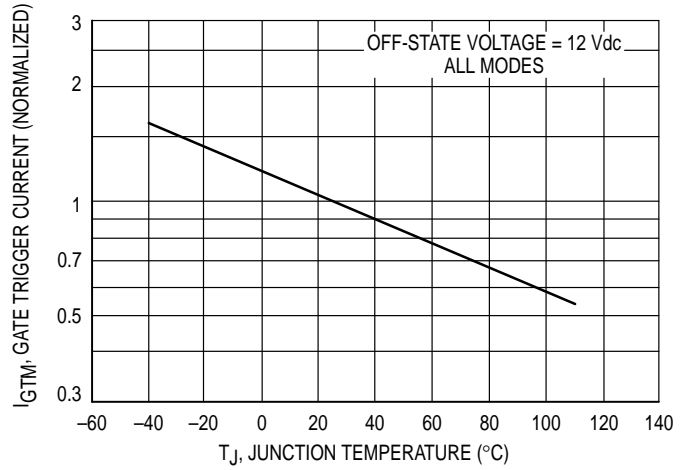


Figure 4. Typical Gate Trigger Current

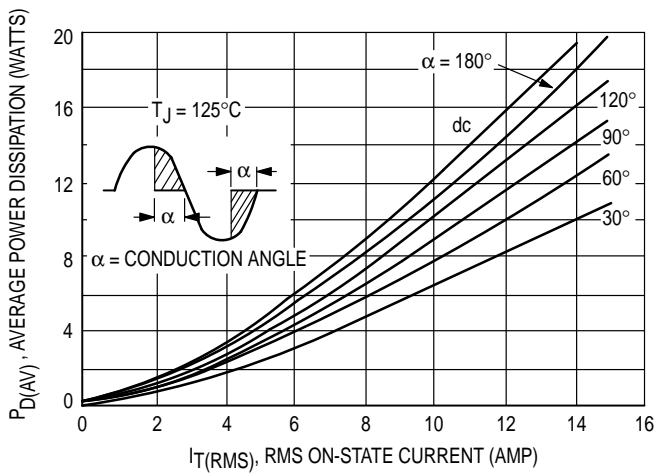


Figure 2. On-State Power Dissipation

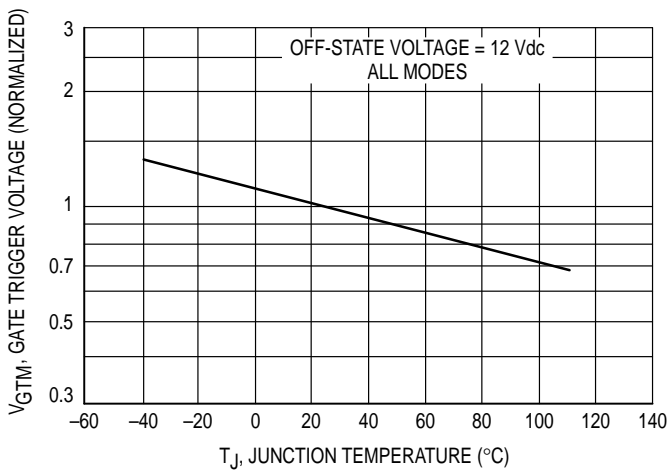


Figure 3. Typical Gate Trigger Voltage

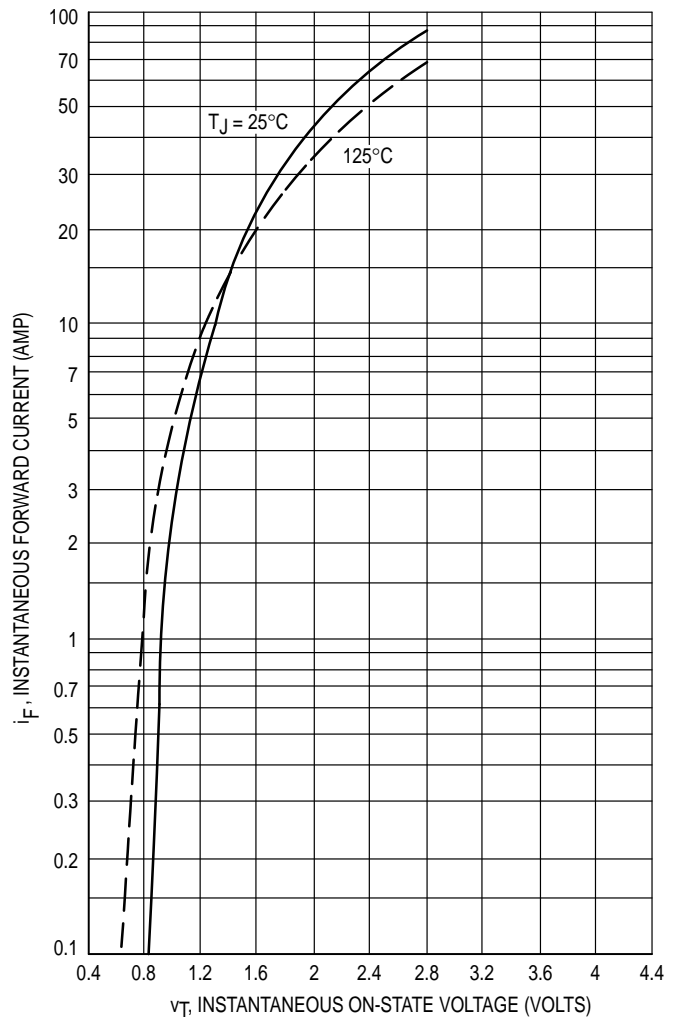
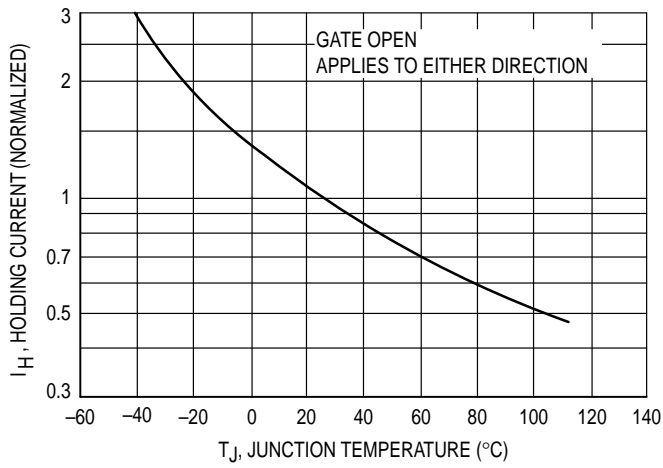
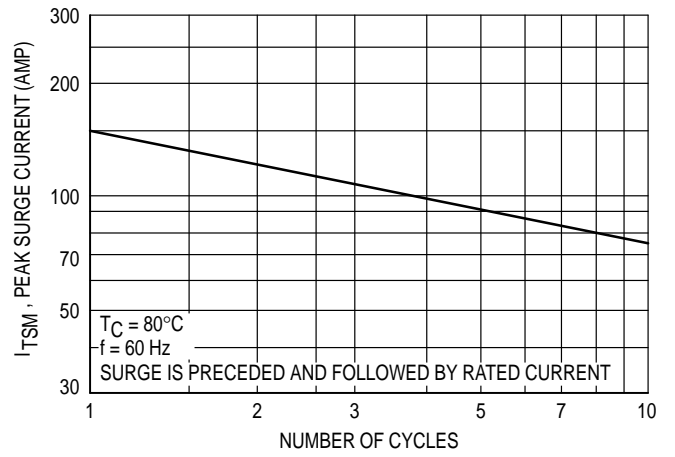


Figure 5. Maximum On-State Characteristics

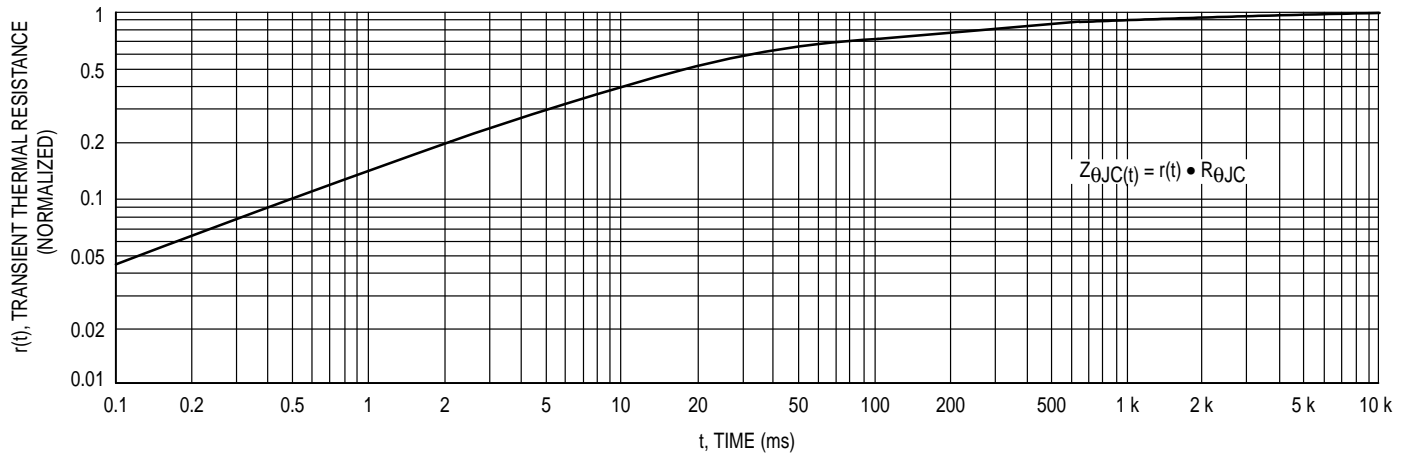
**MAC15FP Series MAC15AFP Series**



**Figure 6. Typical Holding Current**



**Figure 7. Maximum Nonrepetitive Surge Current**



**Figure 8. Thermal Response**