Triacs

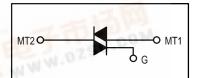
Silicon Bidirectional Triode Thyristors

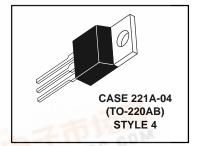
. . . designed primarily for industrial and consumer applications for full wave control of ac loads such as appliance controls, heater controls, motor controls, and other power switching applications.

- Sensitive Gate Triggering in Three Trigger Modes for AC Triggering on Sinking Current Sources (MAC310 Series)
- Four Mode Triggering (10 mA) for Drive Circuits that Source Current (MAC310A Series)
- All Diffused and Glass-Passivated Junctions for Parameter Uniformity and Stability
- Small, Rugged, Thermowatt Construction for Low Thermal Resistance, High Heat Dissipation
- Center Gate Geometry for Uniform Current Spreading

MAC310 Series MAC310A Series

TRIACs 10 AMPERES RMS 200 thru 600 VOLTS





MAXIMUM RATINGS ($T_C = 25^{\circ}C$ unless otherwise noted.)

| Rating | Symbol | Value | Unit |
|--|---------------------|-------------------|------------------|
| Peak Repetitive Off-State Voltage ⁽¹⁾ (T _J = -40 to 110°C, 1/2 Sine Wave 50 to 60 Hz, Gate Open) MAC310-4, MAC310A4 MAC310-6, MAC310A6 MAC310-8, MAC310A8 | VDRM | 200 400 600 | Volts |
| On-State RMS Current (T _C = 80°C) Full Cycle Sine Wave 50 to 60 Hz | I _{T(RMS)} | 10 | Amps |
| Peak Non-repetitive Surge Current (One Full Cycle 60 Hz, T _J = 110°C) | ITSM | 100 | Amps |
| Circuit Fusing (t = 8.3 ms) | l ² t | 40 | A ² s |
| Peak Gate Current (t ≤ 2 μs) | IGM | ±2 | Amps |
| Peak Gate Voltage (t ≤ 2 μs) | V _{GM} | ±10 | Volts |
| Peak Gate Power (t ≤ 2 μs) | P _{GM} | 20 | Watts |
| Average Gate Power (T _C = 80°C, t ≤ 8.3 ms) | P _{G(AV)} | 0.5 | Watts |
| Operating Junction Temperature Range | TJ | -40 to 110 | °C |
| Storage Temperature Range | T _{stg} | -40 to 150 | °C |
| Mounting Torque | _ | 8 | in-lb |

 VDRM for all types can be applied on a continuous basis. Blocking voltages shall not be tested with a constant current source such that the voltage ratings of the devices are exceeded. (continued)



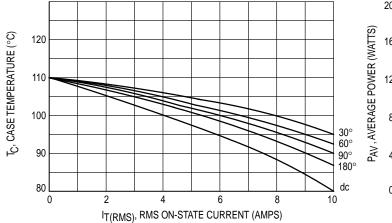
MAC310 Series MAC310A Series

THERMAL CHARACTERISTICS

| Characteristic | Symbol | Max | Unit |
|---|----------------|-----|------|
| Thermal Resistance, Junction to Case | $R_{	heta JC}$ | 2.2 | °C/W |
| Thermal Resistance, Junction to Ambient | $R_{	heta JA}$ | 60 | °C/W |

ELECTRICAL CHARACTERISTICS ($T_C = 25^{\circ}C$ and either polarity of MT2 to MT1 voltage unless otherwise noted.)

| Characteristic | Symbol | Min | Тур | Max | Unit |
|--|------------------|-------------|-------------|---------------|-------|
| Peak Blocking Current $T_J = 25^{\circ}C$ $(V_D = Rated V_{DRM}, T_J = 110^{\circ}C)$ | I _{DRM} | _ | | 10 2 | mA |
| Peak On-State Voltage (I_{TM} = 14 A Peak, Pulse Width \leq 2 ms, Duty Cycle \leq 2%) | V _{TM} | _ | _ | 2 | Volts |
| Gate Trigger Current (Continuous dc) $ (V_D = 12 \text{ V, R}_L = 100 \ \Omega) \\ \text{MT2(+), G(+); MT2(+), G(-); MT2(-), G(-)} \\ \text{MT2(-), G(+) "A" Suffix Only} $ | lGT | | | 5 10 | mA |
| Gate Trigger Voltage (Continuous dc) $ (V_D=12\ V,\ R_L=100\ \Omega) $ $ MT2(+),\ G(+);\ MT2(+),\ G(-);\ MT2(-),\ G(-) $ $ MT2(-),\ G(+)\ "A"\ Suffix\ Only $ $ (V_D=Rated\ V_{DRM},\ T_C=110\ ^\circ C,\ R_L=10\ k) $ $ All\ Trigger\ Modes $ | VGТ | 0.2 | _ _ _ | 2 2.5 — | Volts |
| Holding Current (V _D = 12 V, I _{TM} = 200 mA, Gate Open) | lн | _ | _ | 15 | mA |
| Gate-Controlled Turn-On Time (V_D = Rated V_{DRM} , I_{TM} = 14 A Peak, I_G = 30 mA) | ^t gt | _ | 1.5 | _ | μs |
| Critical Rate of Rise of Off-State Voltage (V_D = Rated V_{DRM} , Exponential Waveform, T_C = 110°C) | dv/dt | _ | 25 | _ | V/μs |
| Critical Rate of Rise of Commutation Voltage (VD = Rated VDRM, ITM = 14 A Peak, Commutating di/dt = 5 A/ms, Gate Unenergized, TC = 80°C) | dv/dt(c) | _ | 5 | _ | V/µs |



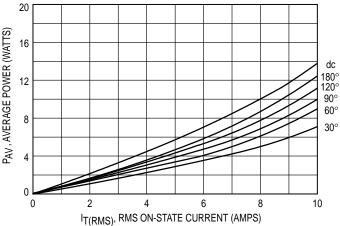
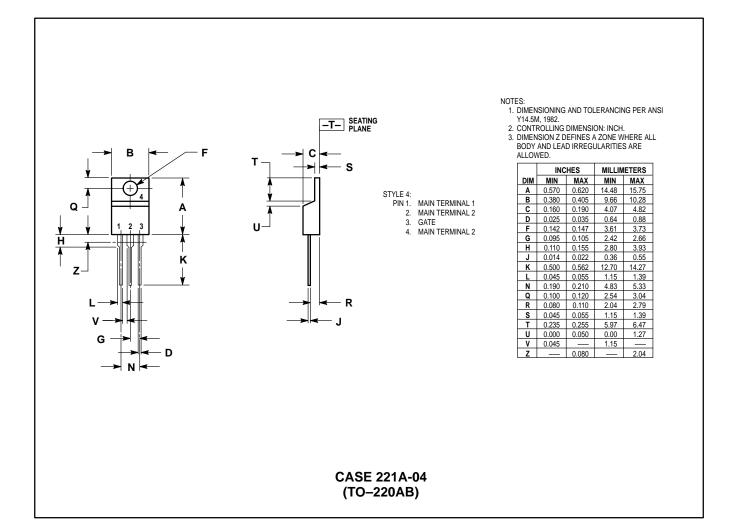


Figure 1. RMS Current Derating

Figure 2. On-State Power Dissipation

MAC310 Series MAC310A Series

PACKAGE DIMENSIONS



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MAC310 Series MAC310A Series

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