

REA1 А

RFB1

5002000

В

RFA2

BEB2

# **GaAs DPDT Switch** DC - 2 GHz

- Cascadable
- Low Insertion Loss
- Low DC Power Consumption
- Low Distortion Operation (Quiet Mode)
  Useful as a Building Distance
- Useful as a Building Block for
  - Digital Attenuators
  - Digital Delay Lines
  - Digital Phase Shifters
  - Digital Switched Filter Elements

## Guaranteed Specifications\*\* -55°C to +85°C

| •               |  |  |
|-----------------|--|--|
| Frequency Range |  | DC – 2.0 GHz                           |
| Insertion Loss  | DC – 0.5 GHz<br>DC – 1.0 GHz<br>DC – 2.0 GHz | 0.4 dB Max<br>0.4 dB Max<br>0.6 dB Max |
| VSWR            | DC – 0.5 GHz<br>DC – 1.0 GHz<br>DC – 2.0 GHz | 1.1:1 Max<br>1.2:1 Max<br>1.2:1 Max    |
| Isolation       | DC – 0.5 GHz<br>DC – 1.0 GHz<br>DC – 2.0 GHz | 25 dB Min<br>20 dB Min<br>15 dB Min    |

# **Operating Characteristics**

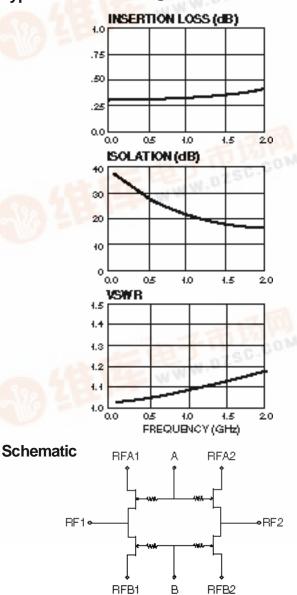
.dzsc.com

| (10% RF)<br>(10% RF)<br>sion<br>0/–5<br>+24 dBm<br>+30 dBm | +25  | Nominal<br>3 ns Typ<br>6 ns Typ<br>20 mV Typ<br>0/–8<br>5 dBm Typ  |
|--|--|--|
| sion<br>0/–5<br>+24 dBm<br>+30 dBm                         | +25  | 6 ns Typ<br>20 mV Typ<br>0/–8<br>5 dBm Typ   |
| 0/–5<br>+24 dBm<br>+30 dBm                                 |  | 6 dBm Typ  |
| 0/–5<br>+24 dBm<br>+30 dBm                                 |  | 6 dBm Typ  |
| +30 dBm  |  |  |
|  | +33  |  |
| oint   |  | 3 dBm Typ  |
| to +5 dBm)<br>IP2  |  | IP3  |
| +62  | +39  | dBm Typ  |
| +68 dBm  | +46  | dBm Typ  |
| 0 to -   | -0.2V @<br>8V @ 7  | 9 µA Max<br>5 µA Max   |
|  |  |  |
| ages.  | nected to a  | all RF   |
|  |  |  |
|  | +62<br>+68 dBm<br>tary Logic)<br>@ 25 μΑ Typ to<br>0.045" ><br>(1.13mm x 0.9 | bint<br>IP2<br>+62<br>+68 dBm<br>+68 dBm<br>+68 dBm<br>+46<br>tary Logic)<br>@ 25 μA Typ to -8V @ 7<br>0.045" x 0.038"<br>(1.13mm x 0.97mm x<br>impedance connected to a<br>tages. |

# Typical Performance @ +25°C

E

RF1



## V 2.00

**MASW2040** 

RF2

Ac

Be

### **Handling Precautions**

Permanent damage to the MASW2040 may occur if the following precautions are not adhered to:

- A. Cleanliness The MASW2040 should be handled in a clean environment. DO NOT attempt to clean unit after the MASW2040 is installed.
- B. Static Sensitivity All chip handling equipment and personnel should be DC grounded.
- C. Transient Avoid instrument and power supply transients while bias is applied to the MASW2040.Use shielded signal and bias cables to minimize inductive pick-up.
- D. Bias Apply voltage to either control port A or B only when the other is grounded. Neither port should be allowed to "float".
- E. General Handling It is recommended that the MASW2040 chip be handled along the long side of the die with a sharp pair of bent tweezers. DO NOT touch the surface of the chip with fingers or tweezers.

#### Mounting

The MASW2040 is back-metallized with Pd/Ni/Au(100/1,000/ 10,000Å) metallization. It can be die-mounted with AuSn eutectic preforms or with thermally conductive epoxy. The package surface should be clean and flat before attachment.

#### Eutectic Die Attach:

- A. A 80/20 gold/tin preform is recommended with a work surface temperature of approximately 255°C and a tool temperature of 265°C. When hot 90/10 nitrogen/hydrogen gas is applied, tool tip temperature should be approximately 290°C.
- B. DO NOT expose the MASW2040 to a temperature greater than 320°C for more than 20 seconds. No more than 3 seconds of scrubbing should be required for attachment.

#### Epoxy Die Attach:

- A. Apply a minimum amount of epoxy and place the MASW2040 into position. A thin epoxy fillet should be visible around the perimeter of the chip.
- B. Cure epoxy per manufacturer's recommended schedule.
- C. Electrically conductive epoxy may be used but is not required.

#### Wire Bonding

- A. Ball or wedge with 1.0 mil diameter pure gold wire. Thermosonic wirebonding with a nominal stage temperature of 150°C and a ball bonding force of 40 to 50 grams or wedge bonding force of 18 to 22 grams is recommended. Ultrasonic energy and time should be adjusted to the minimum levels achieve reliable wirebonds.
- B. Wirebonds should be started on the chip and terminated on the package. GND bonds should be as short as possible; at least three and no more than four bond wires from ground

## **Truth Table**

| Control Inputs                  |                                 | Condition of Switch |            |            |            |
|---------------------------------|---------------------------------|---------------------|------------|------------|------------|
| Α                               | В                               | RF1 - RFA1          | RF1 - RFB1 | RF2 - RFA2 | RF2 - RFB2 |
| V <sub>IN</sub> H               | V <sub>IN</sub> L <sub>OW</sub> | OFF                 | ON         | OFF        | ON         |
| V <sub>IN</sub> L <sub>OW</sub> | V <sub>IN</sub> H <sub>I</sub>  | ON                  | OFF        | ON         | OFF        |

 $V_{IN}L_{OW} = \overline{0.0 \text{ TO } -0.2\text{V}}$  $V_{IN}H_{I} = -0.5\text{V}$ 

| Maximum Ratings               |                            |  |  |
|-------------------------------|----------------------------|--|--|
| A. Control Value (A or B):    | -8.5 Vdc                   |  |  |
| B. Max Input RF Power:        | +34 dBm<br>(500 MHz-4 GHz) |  |  |
| C. Storage Temperature:       | –65°C to +175°C            |  |  |
| D. Max Operating Temperature: | +175°C                     |  |  |

| BondPad Dimensions — Inches (mm) |                               |  |
|----------------------------------|-------------------------------|--|
| RF1, RF2                         | 0.004 x 0.008 (0.100 x 0.200) |  |
| RFA1, RFB1                       | 0.004 x 0.005 (0.100 x 0.125) |  |
| RFA2, RFB2                       | 0.004 x 0.005 (0.100 x 0.125) |  |
| A, B, Ac, Bc                     | 0.004 x 0.004 (0.100 x 0.100) |  |
|                                  |                               |  |

| Die Size — Inches (mm)                     |  |
|--|--|
| 0.045 x 0.038 x 0.010 (1.13 x 0.97 x 0.25) |  |