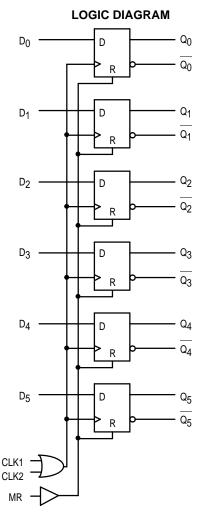
# **6-Bit D Register**

The MC10E/100E151 contains 6 D-type, edge-triggered, master-slave flip-flops with differential outputs. Data enters the master when both CLK1 and CLK2 are LOW, and is transferred to the slave when CLK1 or CLK2 (or both) go HIGH. The asynchronous Master Reset (MR) makes all Q outputs go LOW.

- 1100MHz Min. Toggle Frequency
- Differential Outputs
- Asynchronous Master Reset
- Dual Clocks
- Extended 100E VEE Range of 4.2V to 5.46V
- 75kΩ Input Pulldown Resistors



### **PIN NAMES**

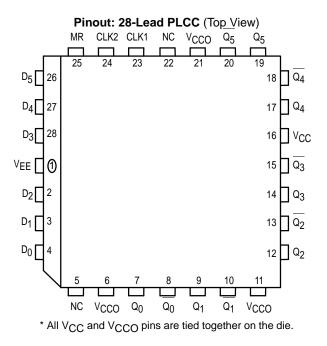
Pin	Function						
D <sub>0</sub> – D <sub>5</sub> CLK1, CLK2	Data Inputs Clock Inputs						
MR	Master Reset						
$\frac{Q_0}{Q_0} - \frac{Q_5}{Q_5}$	True Outputs Inverted Outputs						

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MC10E151 MC100E151

6-BIT D REGISTER







## MC10E151 MC100E151

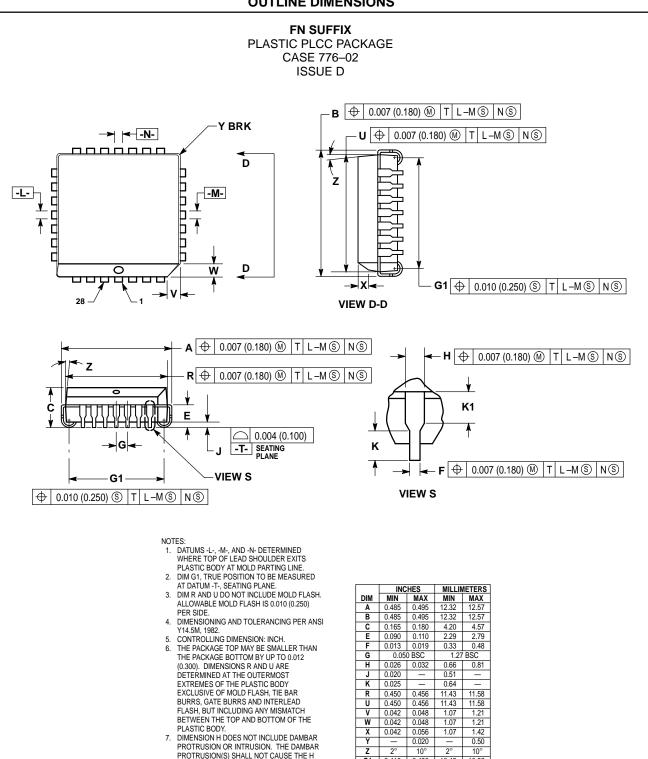
### **DC CHARACTERISTICS** (VEE = VEE(min) to VEE(max); VCC = VCCO = GND)

		0°C			25°C			85°C				
Symbol	Characteristic	min	typ	max	min	typ	max	min	typ	max	Unit	Condition
IIН	Input HIGH Current			150			150			150	μA	
IEE	Power Supply Current 10E 100E		65 65	78 78		65 65	78 78		65 75	78 90	mA	

### **AC CHARACTERISTICS** ( $V_{EE} = V_{EE}(min)$ to $V_{EE}(max)$ ; $V_{CC} = V_{CCO} = GND$ )

		0°C		25°C			85°C					
Symbol	Characteristic	min	typ	max	min	typ	max	min	typ	max	Unit	Condition
fMAX	Max. Toggle Frequency	1100	1400		1100	1400		1100	1400		MH z	
<sup>t</sup> PLH <sup>t</sup> PHL	Propagation Delay to Output Clk MR	475 475	650 650	800 850	475 475	650 650	800 850	475 475	650 650	800 850	ps	
t <sub>s</sub>	Setup TIme D	0	-175		0	-175		0	-175		ps	
<sup>t</sup> h	Hold Time D	350	175		350	175		350	175		ps	
<sup>t</sup> RR	Reset Recovery Time	750	550		750	550		750	550			ps
<sup>t</sup> PW	Minimum Pulse Width CLK, MR	400			400			400			ps	
<sup>t</sup> SKEW	Within-Device Skew		65			65			65		ps	1
tr tf	Rise/Fall Times 20 - 80%	300	450	700	300	450	700	300	450	700	ps	

1. Within-device skew is defined as identical transitions on similar paths through a device.



DIMENSION TO BE GREATER THAN 0.037 (0.940). THE DAMBAR INTRUSION(S) SHALL

NOT CAUSE THE H DIMENSION TO BE SMALLER THAN 0.025 (0.635).

10.42

10.92

G1 0.410 0.430

K1 0.040

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