16:1 Multiplexer

The MC10E/100E164 is a 16:1 multiplexer with a differential output. The select inputs (SEL0, 1, 2, 3) control which one of the sixteen data inputs (A0 - A15) is propagated to the output.

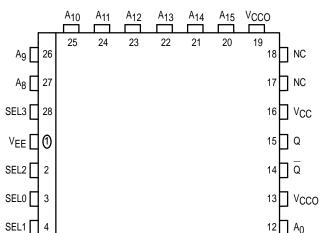
Special attention to the design layout results in a typical skew between the 16 inputs of only 50ps.

- 850ps Data Input to Output
- Differential Output
- Extended 100E VEE Range of 4.2V to 5.46V
- Internal 75kΩ Input Pulldown Resistors

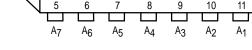
MC10E164 MC100E164

16:1 MULTIPLEXER





Pinout: 28-Lead PLCC (Top View)



* All V_{CC} and V_{CCO} pins are tied together on the die.

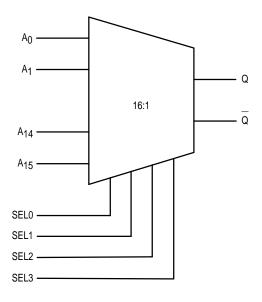
 $12 \square A_0$

PIN NAMES

4

Pin	Function
A ₀ – A ₁₅	Data Inputs
SE <u>L[</u> 0:3]	Select Inputs
Q, Q	Output







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MC10E164 MC100E164

DC CHARACTERISTICS (VEE = VEE(min) to VEE(max); VCC = VCCO = GND)

		0°C		25°C			85°C					
Symbol	Characteristic	min	typ	max	min	typ	max	min	typ	max	Unit	Condition
I _{IH}	Input HIGH Current			150			150			150	μA	
IEE	Power Supply Current										mA	
	10E		59	71		59	71		59	71		
	100E		59	71		59	71		68	81		

AC CHARACTERISTICS ($V_{EE} = V_{EE}(min)$ to $V_{EE}(max)$; $V_{CC} = V_{CCO} = GND$)

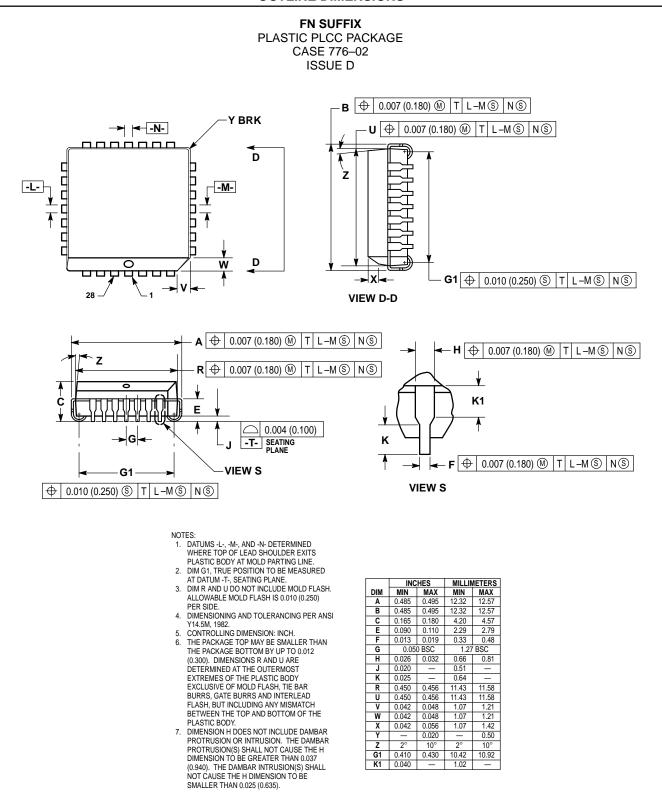
			0°C			25°C			85°C			
Symbol	Characteristic	min	typ	max	min	typ	max	min	typ	max	Unit	Condition
^t PLH	Propagation Delay to Output										ps	
^t PHL	A Input	350	600	850	350	600	850	350	600	850		
	SEL0	500	700	900	500	700	900	500	700	900		
	SEL1	400	675	900	400	675	900	400	675	900		
	SEL2	400	675	900	400	675	900	400	675	900		
	SEL3	400	550	700	400	550	700	400	550	700		
^t SKEW	Within Device Skew		50			50			50		ps	1
tr	Rise/Fall Times										ps	
tf	20 - 80%	275	400	550	275	400	550	275	400	550		

1. Within Device skew is defined as the difference in the A to Q delay between the 16 different A inputs.

FUNCTION TABLE

SEL3	SEL2	SEL1	SEL0	Data
L	L	L	L	A0
L	L	L	н	A1
L	L	Н	L	A2
L	L	Н	н	A3
L	н	L	L	A4
L	н	L	н	A5
L	н	Н	L	A6
L	н	Н	н	A7
Н	L	L	L	A8
Н	L	L	Н	A9
н	L	Н	L	A10
н	L	Н	н	A11
н	н	L	L	A12
Н	Н	L	Н	A13
Н	Н	Н	L	A14
Н	Н	Н	Н	A15





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