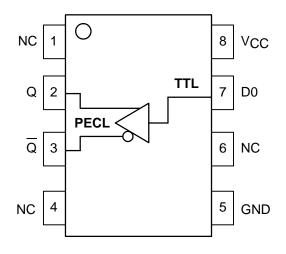
TTL to Differential PECL Translator

The MC10ELT/100ELT20 is a TTL to differential PECL translator. Because PECL (Positive ECL) levels are used only +5V and ground are required. The small outline 8-lead SOIC package and the single gate of the ELT20 makes it ideal for those applications where space, performance and low power are at a premium. Because the mature MOSAIC 1.5 process is used, low cost can be added to the list of features.

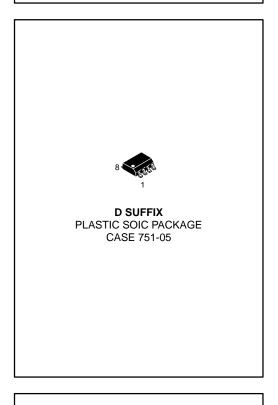
The ELT20 is available in both ECL standards: the 10ELT is compatible with positive MECL 10H logic levels while the 100ELT is compatible with positive ECL 100K logic levels.

- 1.5ns Typical Propagation Delay
- Differential PECL Outputs
- Small Outline SOIC Package
- PNP TTL Inputs for Minimal Loading
- Flow Through Pinouts

LOGIC DIAGRAM AND PINOUT ASSIGNMENT



MC10ELT20 MC100ELT20



PIN DESCRIPTION									
FUNCTION									
Diff PECL Outputs TTL Input +5.0V Supply Ground									



1/95

MC10ELT20 MC100ELT20

MAXIMUM RATINGS*

Symbol	Parameter	Value	Unit	
VCC	DC Supply Voltage (Referenced to GND)	7.0	V	
VIN	Input Voltage		0 to V _{CC}	V
IOUT	Current Applied to Output in Low Output State	Continuous Surge	50 100	mA
т _А	Operating Temperature Range (In Free-Air)		-40 to 85	°C
T _{STG}	Storage Temperature Range		–55 to +150	°C

* Maximum Ratings are those values beyond which damage to the device may occur. Functional operation should be restricted to the Recommended Operating Conditions.

TTL INPUT DC CHARACTERISTICS (V_{CC} = 4.75V to 5.25V; $T_A = -40^{\circ}C$ to 85°C)

Symbol	Characteristic	Min	Тур	Max	Unit	Condition
Ιн	Input HIGH Current			20	μΑ	V _{IN} = 2.7V
ІІНН	Input HIGH Current			100	μΑ	V _{IN} = 7.0V
۱ _{IL}	Input LOW Current			-0.6	mA	V _{IN} = 0.5V
VIK				-1.2	V	I _{IN} = -18mA
VIH	Input HIGH Voltage	2.0			V	
VIL	Input LOW Voltage			0.8	V	

PECL OUTPUT DC CHARACTERISTICS (V_{CC} = 4.75V to 5.25V; $T_A = -40^{\circ}C$ to 85°C)

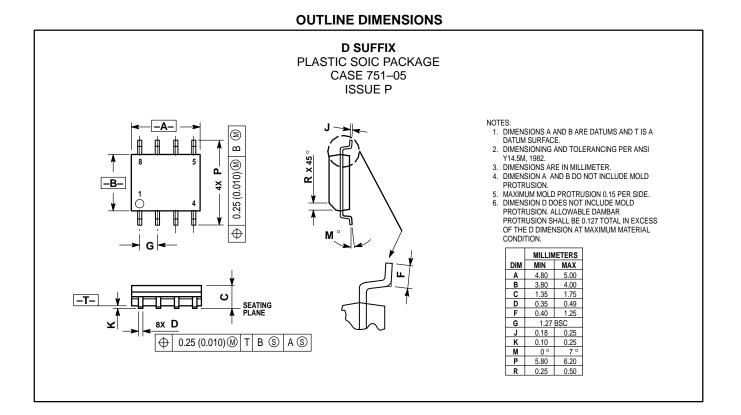
		-40	D°C	0°C		25°C			85°C			
Symbol	Characteristic	Min	Max	Min	Max	Min	Тур	Max	Min	Max	Unit	Condition
VOH	Output HIGH 10ELT 1 Voltage 100ELT 1	3.920 3.915	4.11 4.12	3.980 3.975	4.16 4.12	4.020 3.975	4.10 4.05	4.19 4.12	4.080 3.975	4.27 4.12	V	V _{CC} = 5.0V
V _{OL}	Output LOW 10ELT 1 Voltage 100ELT 1	3.05 3.17	3.350 3.445	3.05 3.19	3.37 3.38	3.05 3.19	3.25 3.30	3.37 3.38	3.05 3.19	3.40 3.35	V	V _{CC} = 5.0V
ICC	Power Supply Current		16		16			16		16	mA	

1. Levels will vary 1:1 with V_{CC}.

AC CHARACTERISTICS (V_{CC} = 4.75V to 5.25V; T_A = -40° C to 85° C)

		-40°C		0°C		25°C			85°C			
Symbol	Characteristic	Min	Max	Min	Max	Min	Тур	Max	Min	Max	Unit	Condition
^t PLH	Propagation Delay1	0.6	1.2	0.65	1.45	0.9	1.2	1.5	0.6	1.35	ns	
^t PHL	Propagation Delay1	0.4	1.0	0.45	1.05	0.5	0.8	1.1	0.7	1.30	ns	
t _r /t _f	Output Rise/Fall Time	0.15	1.5	0.15	1.5	0.15		1.5	0.15	1.5	ns	20–80%
fMAX	Maximum Input Frequency	100		100		100			100		MHz	

1. Specifications for standard TTL input signal.



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