

PRELIMINARY

Notice: This is not a final specification.
Some parametric limits are subject to change.

MGFC36V6472A

6.4~7.2GHz BAND 4W INTERNALLY MATCHED GaAs FET

DESCRIPTION

The MGFC36V6472A is an internally impedance-matched GaAs power FET especially designed for use in 6.4~7.2GHz band amplifiers. The hermetically sealed metal-ceramic package guarantees high reliability.

FEATURES

- Class A operation
- Internally matched to 50Ω system
- High output power
 $P_{1dB} = 4W(TYP) @ 6.4\sim 7.2GHz$
- High power gain
 $GLP = 9dB(TYP) @ 6.4\sim 7.2GHz$
- High power added efficiency
 $\eta_{add} = 30\%(TYP) @ 6.4\sim 7.2GHz$
- Hermetically sealed metal-ceramic package
- Low distortion [Item : - 51]
 $IM_3 = -45dBc(TYP) @ P_o = 25(dBm) S.C.L.$

APPLICATION

Item-01 : 6.4~7.2GHz band power amplifier
Item-51 : Digital radio communication

QUALITY GRADE

- IG

ABSOLUTE MAXIMUM RATINGS (Ta = 25°C)

Symbol	Parameter	Ratings	Unit
V _{GD0}	Gate to drain voltage	- 15	V
V _{GS0}	Gate to source voltage	- 15	V
I _D	Drain current	3.75	A
I _{GR}	Reverse gate current	- 10	mA
I _{GF}	Forward gate current	21	mA
P _T	Total power dissipation * 1	25	W
T _{ch}	Channel temperature	175	°C
T _{stg}	Storage temperature	- 65~+ 175	°C

* 1 : T_c = 25°C

ELECTRICAL CHARACTERISTICS (Ta = 25°C)

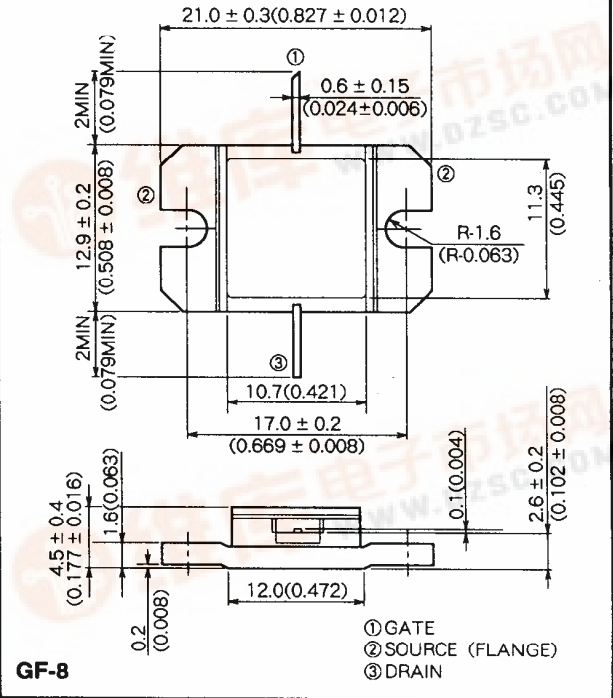
Symbol	Parameter	Test conditions	Limits			Unit
			Min	Typ	Max	
I _{DSS}	Saturated drain current	V _{DS} = 3V, V _{GS} = 0V	-	-	3.75	A
g _m	Transconductance	V _{DS} = 3V, I _D = 1.1A	-	1	-	S
V _{GS(off)}	Gate to source cut-off voltage	V _{DS} = 3V, I _D = 10mA	-	-	- 4.5	V
P _{1dB}	Output power at 1dB gain compression	V _{DS} = 10V, I _D = 1.2A, f = 6.4~7.2GHz	35	36	-	dBm
GLP	Linear power gain		8	9	-	dB
I _D	Drain current		-	-	1.8	A
η _{add}	Power added efficiency		-	30	-	%
IM ₃	3rd order IM distortion * 1		- 42	- 45	-	dBc
R _{th(ch-c)}	Thermal resistance * 2	ΔV _r method	-	5	6	°C/W

* 1 : Item-51, 2-tone test P_o = 25dBm Single Carrier Level f = 7.2GHz Δf = 10MHz

* 2 : Channel to case

OUTLINE DRAWING

Unit : millimeters (inches)



GF-8

- ① GATE
- ② SOURCE (FLANGE)
- ③ DRAIN

RECOMMENDED BIAS CONDITIONS

- V_{DS} = 10V
- I_D = 1.2A
- R_G = 100(Ω)
- Refer to Bias Procedure

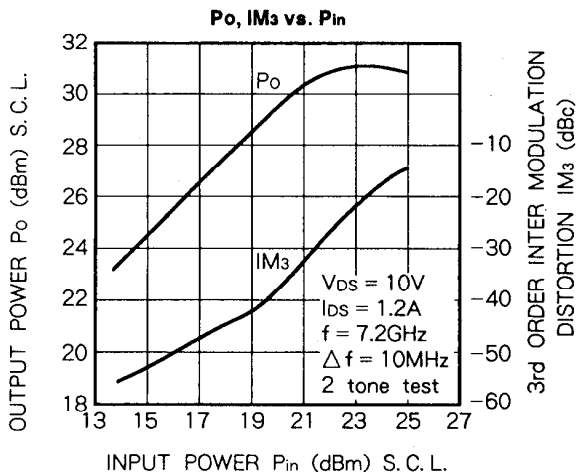
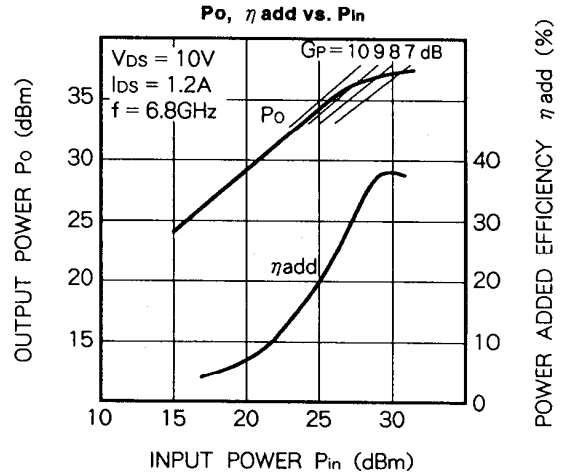
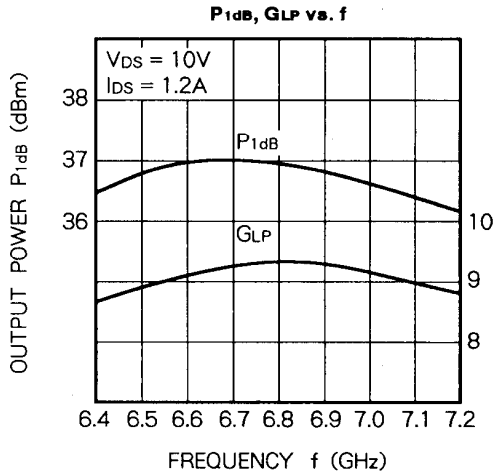


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TYPICAL CHARACTERISTICS



S PARAMETERS (T_a = 25°C, V_{DS} = 10V, I_{DS} = 1.2A)

f (GHz)	S parameters							
	S ₁₁		S ₂₁		S ₁₂		S ₂₂	
	Magn.	Angle(deg.)	Magn.	Angle(deg.)	Magn.	Angle(deg.)	Magn.	Angle(deg.)
6.4	0.50	178	2.68	-61	0.078	-110	0.15	48
6.5	0.43	162	2.76	-75	0.084	-124	0.18	32
6.6	0.36	143	2.83	-90	0.088	-138	0.21	15
6.7	0.30	122	2.88	-104	0.090	-152	0.24	-1
6.8	0.25	99	2.93	-119	0.092	-165	0.27	-17
6.9	0.21	77	2.92	-133	0.095	-179	0.29	-31
7.0	0.18	53	2.87	-148	0.098	167	0.30	-47
7.1	0.15	31	2.80	-162	0.101	153	0.31	-62
7.2	0.12	1	2.71	-177	0.102	137	0.32	-79